

20V N-Channel Trench MOSFET(Preliminary)

General Description

- Trench Power technology
- Low R_{DS(ON)}
- Low Gate Charge
- Optimized for fast-switching applications

Applications

- Synchronous Rectification in DC/DC and AC/DC Converters
- Isolated DC/DC Converters in Telecom and Industrial

Product Summary

 V_{DS} 20V I_{D} (at V_{GS} =10V) 180A

 $R_{DS(ON)}$ (at $V_{GS}=10V$) < 2.2m Ω

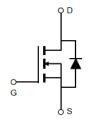
$$\begin{split} R_{DS(ON)} & \text{ (at V_{GS} = 4.5V)} & < 2.3 \text{m} \Omega \\ R_{DS(ON)} & \text{ (at V_{GS} = 2.5V)} & < 3.3 \text{m} \Omega \end{split}$$

100% UIS Tested



TO-220





Part Number	Package Type	Form	Marking
TTP180N02GT	TO-220	Tube	180N02GT

Absolute Maximum Ratings (T_A =25°C unless otherwise noted)

Parameter		Symbol	Maximum	Units
Drain-Source Voltage		V _{DS}	20	V
Gate-Source Voltage		V _{GS}	±20	V
Continuous Drain Current B	T _C =25°C	I _D	105	٨
Continuous Drain Current	T _C =100°C		105	A
Pulsed Drain Current ^A		I _{DM}	540	Α
Avalanche Current A		I _{AS}	33	Α
Single Pulse Avalanche Energy L =0.3mH ^A		E _{AS}	163.4	mJ
Power Dissipation ^C	T _C =25°C	P _D	143	W
Power Dissipation 5	T _C =100°C		71.4	W
Junction and Storage Temperature Range		T _J , T _{STG}	-55 to 175	°C
		•	•	

Thermal Characteristics

Parameter		Symbol	Maximum	Units	
Maximum Junction-to-Case	Steady-State	$R_{\Theta JC}$	1.05	0004	
Maximum Junction-to-Ambient	Steady-State	R _{eJA}	100	°C/W	



Comple ed	Devenuetos	Conditions		Value			
Symbol	Parameter			Min	Тур	Max	Units
STATIC P	ARAMETERS						
BV_{DSS}	Drain-Source Breakdown Voltage	$I_D = 250 \mu A, V_{GS} = 0 V$		20			V
	Zero Gate Voltage Drain Current	V _{DS} =20V, V _{GS} =0V	T _J =25°C			1	μΑ
I _{DSS}			T _J =100°C			25	
I _{GSS}	Gate-Body Leakage Current	$V_{DS} = 0V, V_{GS} = \pm 20V$				±100	nA
V _{GS(th)}	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$		0.5	0.7	1.2	V
		V _{GS} =10V, I _D =30A			1.7	2.2	mΩ
R _{DS(ON)}	Static Drain-Source On-Resistance	V _{GS} =4.5V, I _D =30A			1.8	2.3	mΩ
		V _{GS} =2.5V, I _D =30A			2.5	3.3	mΩ
g _{FS}	Forward Transconductance	V _{DS} =10V, I _D =20A			34		S
V _{SD}	Diode Forward Voltage	I _S =50A, V _{GS} =0V				1	V
I _s	Maximum Body-Diode Continuous Curre	rent ^B				105	Α
DYNAMIC	PARAMETERS						
C _{iss}	Input Capacitance	V _{GS} =0V, V _{DS} =10V, f =1MH _Z			6973		
C _{oss}	Output Capacitance				1587		pF
C _{rss}	Reverse Transfer Capacitance				1316		
SWITCHIN	NG PARAMETERS						
Q _g (10V)	Total Gate Charge				270		
Q_{gs}	Gate Source Charge	$V_{GS} = 10V, V_{DS} = 10V, I_{D} = 50A$			15		nC
Q_{gd}	Gate Drain Charge				50		
t _{D(on)}	Turn-On Delay Time	$V_{GS} = 10V, V_{DS} = 10V, I_{D} = 50A,$ $R_{G} = 3\Omega$			21		
t _r	Turn-On Rise Time				28		ns
$T_{D(off)}$	Turn-Off Delay Time				31		
t _f	Turn-Off Fall Time				25		
t _{rr}	Body Diode Reverse Recovery Time	I 504 JUL 4001			27		ns
Q _{rr}	Body Diode Reverse Recovery Charge	I _F =50A, di/dt =100A/μs			24		nC

- A. Single pulse width limited by maximum junction temperature.
- B. The maximum current rating is package limited.
- C. The power dissipation P_D is based on $T_{J(MAX)} = 175$ °C, using junction-to-case thermal resistance, and is more useful in setting the upper dissipation limit for cases where additional heatsinking is used.

TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS

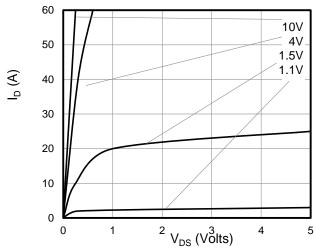


Figure 1: On-Region Characteristics

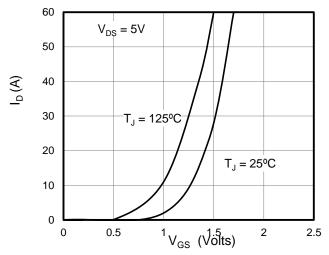


Figure 2: Transfer Characteristics

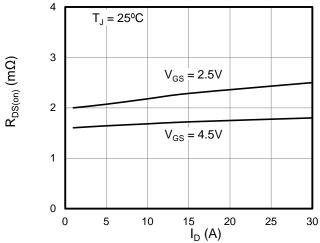


Figure 3: On-Resistance vs. Drain Current

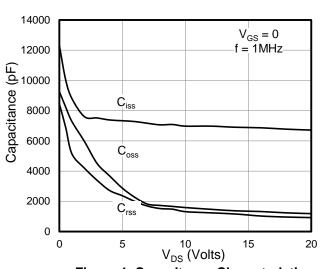


Figure 4: Capacitance Characteristics

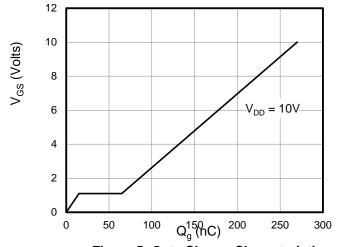


Figure 5: Gate Charge Characteristics

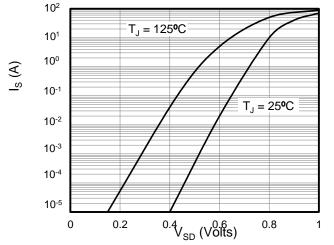


Figure 6: Body Diode Forward Voltage

 $Z_{\theta, JC}$ Normalized Transient Thermal Resistance

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TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS

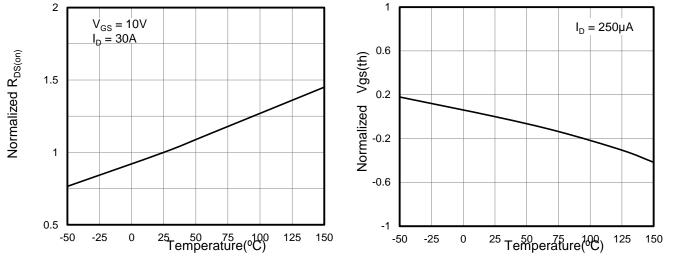


Figure 7: On-Resistance vs. Junction Temperature

Figure 8: Vgs(th) vs. Junction Temperature

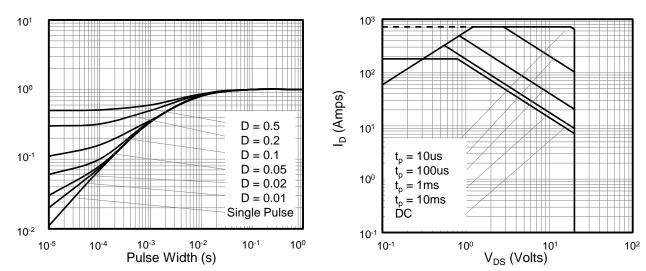


Figure 11: Normalized Transient Thermal Resistance

Figure 12: Safe Operating Area

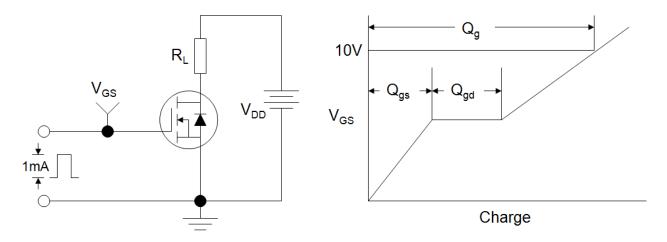


Figure A: Gate Charge Test Circuit and Waveforms

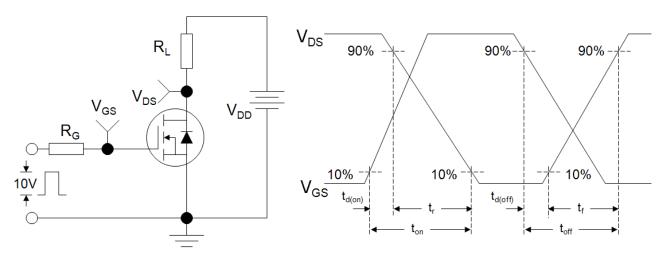


Figure B: Resistive Switching Test Circuit and Waveforms

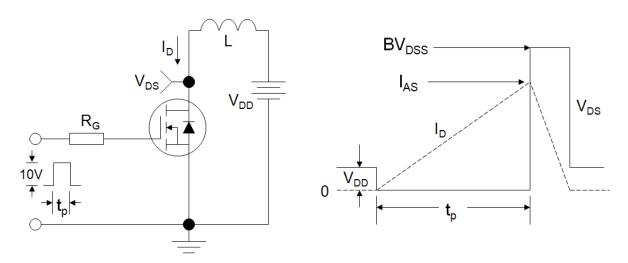
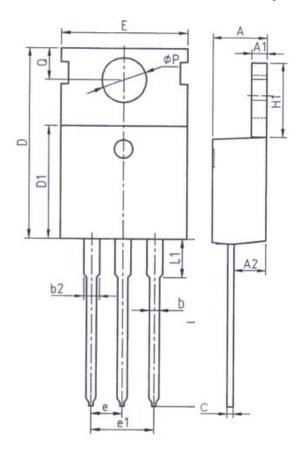
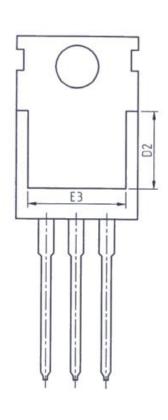


Figure C: Unclamped Inductive Switching (UIS) Test Circuit and Waveforms



TO-220(华天)





Unit: mm				
Symbol	Min.	Max.		
Α	4. 37	4. 77		
A1	1. 25	1. 45		
A2	2. 20	2. 60		
b	0. 70	0. 95		
b2	1. 17	1. 47		
С	0. 40	0. 65		
D	15. 10	16. 10		
D1	8. 80	9. 40		
D2	5. 50	_		

Unit: mm				
Symbol	Min.	Max.		
E	9. 70	10. 30		
E3	7. 00	ı		
е	2. 54BSC			
e1	5. 08BSC			
H1	6. 25	6. 85		
L	12. 75	13.80		
L1	-	3. 40		
Р	3. 40	3. 80		
Q	2. 60	3. 00		



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