




20V N-Channel Trench MOSFET(Preliminary)

<p>General Description</p> <ul style="list-style-type: none"> ● Trench Power technology ● Low $R_{DS(ON)}$ ● Low Gate Charge ● Optimized for fast-switching applications <p>Applications</p> <ul style="list-style-type: none"> ● Synchronous Rectification in DC/DC and AC/DC Converters ● Isolated DC/DC Converters in Telecom and Industrial 	<p>Product Summary</p> <table border="0"> <tr> <td>V_{DS}</td> <td>20V</td> </tr> <tr> <td>I_D (at $V_{GS}=10V$)</td> <td>3A</td> </tr> <tr> <td>$R_{DS(ON)}$ (at $V_{GS}=10V$)</td> <td>< 23.5mΩ</td> </tr> <tr> <td>$R_{DS(ON)}$ (at $V_{GS}=4.5V$)</td> <td>< 27.5mΩ</td> </tr> <tr> <td>$R_{DS(ON)}$ (at $V_{GS}=2.5V$)</td> <td>< 36.5mΩ</td> </tr> </table> <p>100% UIS Tested</p> 	V_{DS}	20V	I_D (at $V_{GS}=10V$)	3A	$R_{DS(ON)}$ (at $V_{GS}=10V$)	< 23.5mΩ	$R_{DS(ON)}$ (at $V_{GS}=4.5V$)	< 27.5mΩ	$R_{DS(ON)}$ (at $V_{GS}=2.5V$)	< 36.5mΩ
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Part Number	Package Type	Form	Marking
TTK8205	TSSOP-8	Tape&Reel	8205

Absolute Maximum Ratings ($T_A = 25^{\circ}C$ unless otherwise noted)

Parameter	Symbol	Maximum	Units
Drain-Source Voltage	V_{DS}	20	V
Gate-Source Voltage	V_{GS}	± 20	V
Continuous Drain Current ^B	I_D	$T_C = 25^{\circ}C$	3
		$T_C = 70^{\circ}C$	3
Pulsed Drain Current ^A	I_{DM}	9	A
Avalanche Current ^A	I_{AS}	7	A
Single Pulse Avalanche Energy L = 0.3mH ^A	E_{AS}	7.4	mJ
Power Dissipation ^C	P_D	$T_C = 25^{\circ}C$	1.5
		$T_C = 70^{\circ}C$	0.96
Junction and Storage Temperature Range	T_J, T_{STG}	-55 to 150	$^{\circ}C$

Thermal Characteristics

Parameter	Symbol	Maximum	Units
Maximum Junction-to-Case	$R_{\theta JC}$	14.4	$^{\circ}C/W$
Maximum Junction-to-Ambient	$R_{\theta JA}$	100	



Electrical Characteristics($T_J = 25^\circ\text{C}$ unless otherwise noted)							
Symbol	Parameter	Conditions	Value			Units	
			Min	Typ	Max		
STATIC PARAMETERS							
BV_{DSS}	Drain-Source Breakdown Voltage	$I_D = 250\mu\text{A}, V_{GS} = 0\text{V}$	20			V	
I_{DSS}	Zero Gate Voltage Drain Current	$V_{DS} = 20\text{V}, V_{GS} = 0\text{V}$	$T_J = 25^\circ\text{C}$		1	μA	
			$T_J = 100^\circ\text{C}$		25		
I_{GSS}	Gate-Body Leakage Current	$V_{DS} = 0\text{V}, V_{GS} = \pm 20\text{V}$			± 100	nA	
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_D = 250\mu\text{A}$	0.5	0.7	1.2	V	
$R_{DS(on)}$	Static Drain-Source On-Resistance		$V_{GS} = 10\text{V}, I_D = 3\text{A}$		18.5	23.5	$\text{m}\Omega$
			$V_{GS} = 4.5\text{V}, I_D = 3\text{A}$		21.5	27.5	$\text{m}\Omega$
			$V_{GS} = 2.5\text{V}, I_D = 3\text{A}$		28	36.5	$\text{m}\Omega$
g_{FS}	Forward Transconductance	$V_{DS} = 10\text{V}, I_D = 3\text{A}$		8		S	
V_{SD}	Diode Forward Voltage	$I_S = 3\text{A}, V_{GS} = 0\text{V}$			1	V	
I_S	Maximum Body-Diode Continuous Current ^B				3	A	
DYNAMIC PARAMETERS							
C_{iss}	Input Capacitance	$V_{GS} = 0\text{V}, V_{DS} = 10\text{V}, f = 1\text{MHz}$		408		pF	
C_{oss}	Output Capacitance			67			
C_{rss}	Reverse Transfer Capacitance			24			
SWITCHING PARAMETERS							
$Q_g(10\text{V})$	Total Gate Charge	$V_{GS} = 10\text{V}, V_{DS} = 10\text{V}, I_D = 6\text{A}$		12		nC	
$Q_g(4.5\text{V})$				5.8			
Q_{gs}			Gate Source Charge		0.7		
Q_{gd}			Gate Drain Charge		1.8		
$t_{D(on)}$	Turn-On Delay Time	$V_{GS} = 10\text{V}, V_{DS} = 10\text{V}, I_D = 6\text{A}, R_G = 2.5\Omega$		15		ns	
t_r	Turn-On Rise Time			17			
$T_{D(off)}$	Turn-Off Delay Time			42			
t_f	Turn-Off Fall Time			40			

A. Single pulse width limited by maximum junction temperature.

B. The maximum current rating is package limited.

C. The power dissipation P_D is based on $T_{J(MAX)} = 150^\circ\text{C}$, using junction-to-case thermal resistance, and is more useful in setting the upper dissipation limit for cases where additional heatsinking is used.



TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS

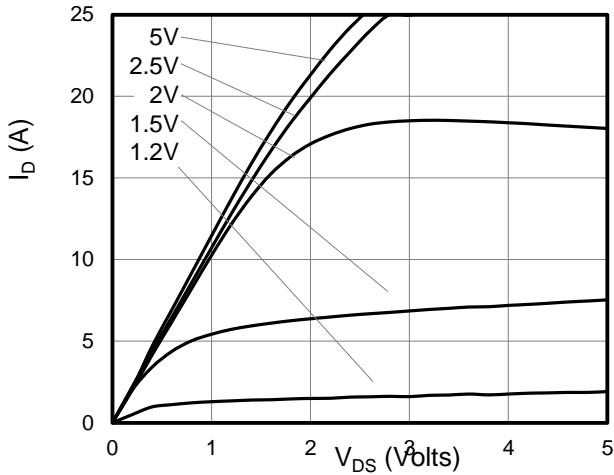


Figure 1: On-Region Characteristics

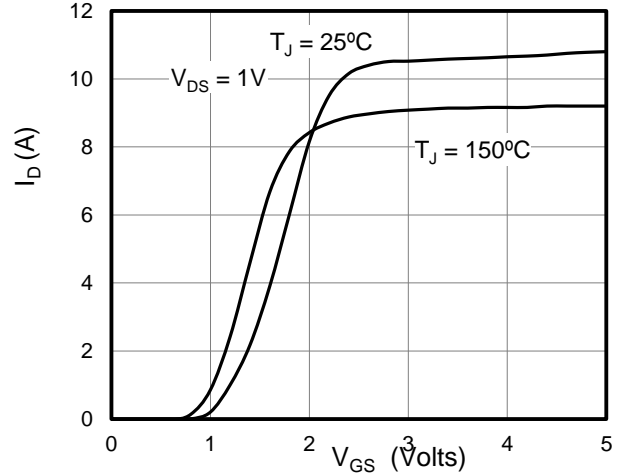


Figure 2: Transfer Characteristics

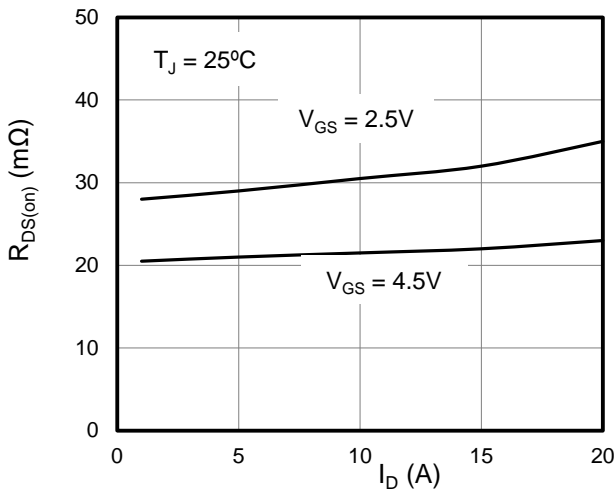


Figure 3: On-Resistance vs. Drain Current

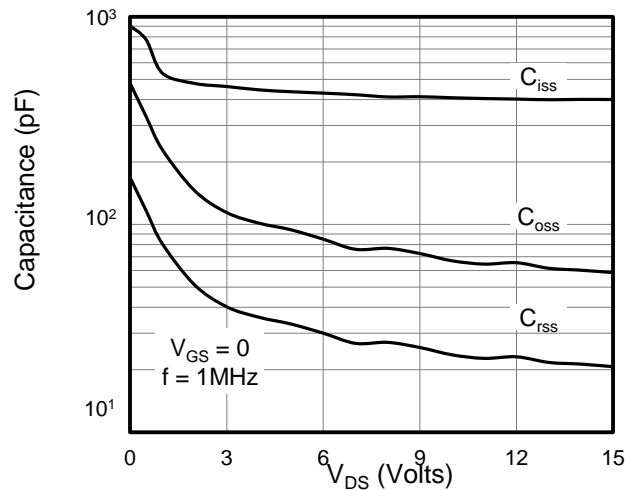


Figure 4: Capacitance Characteristics

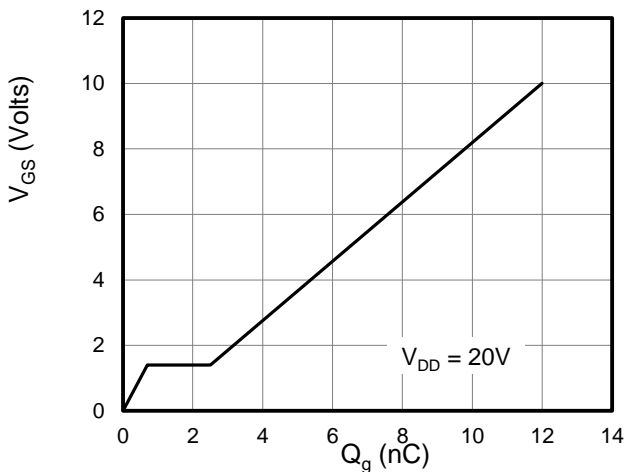


Figure 5: Gate Charge Characteristics

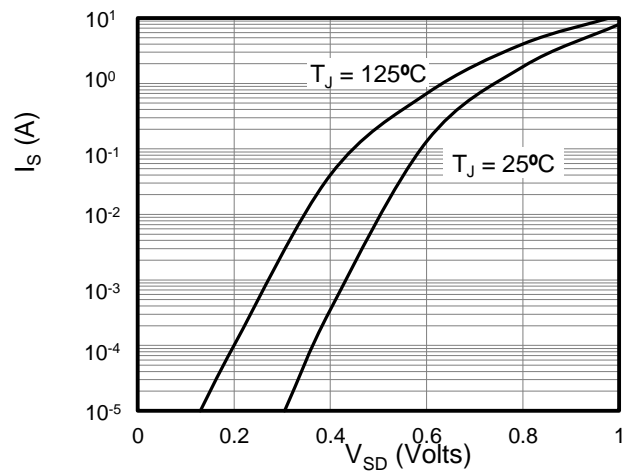


Figure 6: Body Diode Forward Voltage



TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS

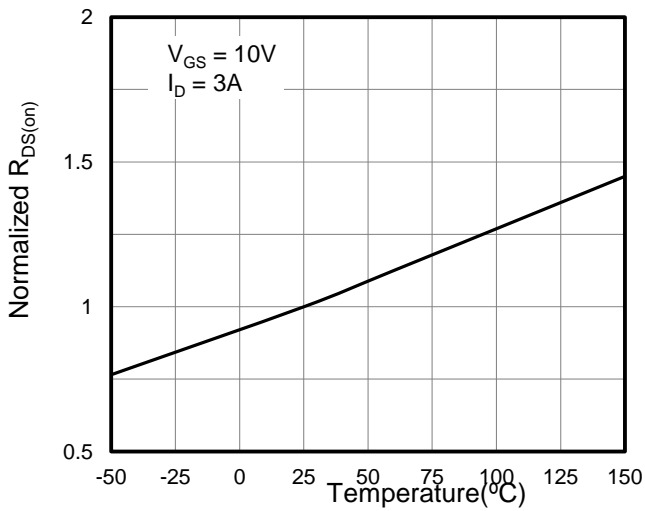


Figure 7: On-Resistance vs. Junction Temperature

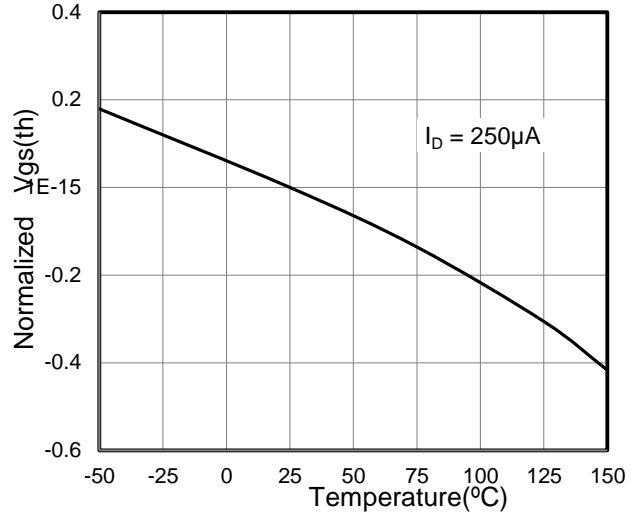


Figure 8: V_GS(th) vs. Junction Temperature

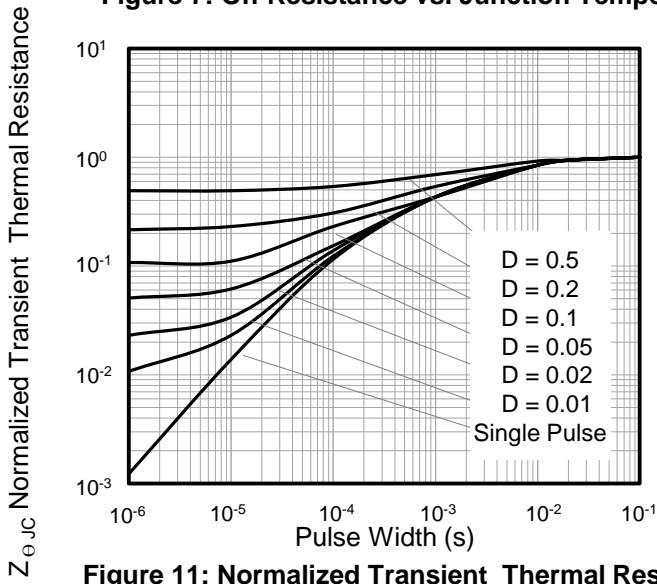


Figure 11: Normalized Transient Thermal Resistance

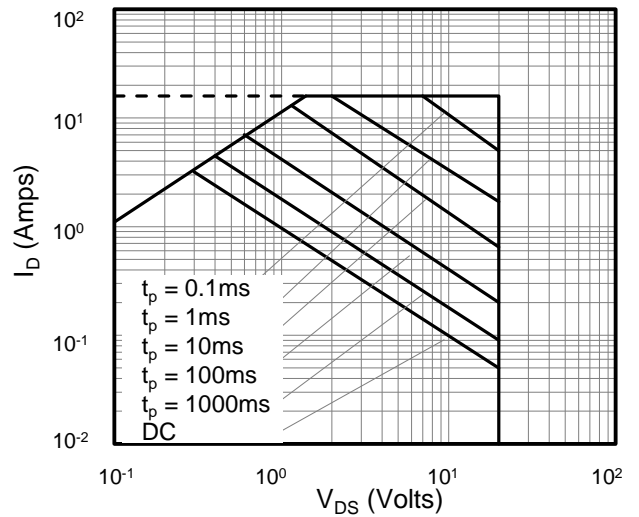


Figure 12: Safe Operating Area

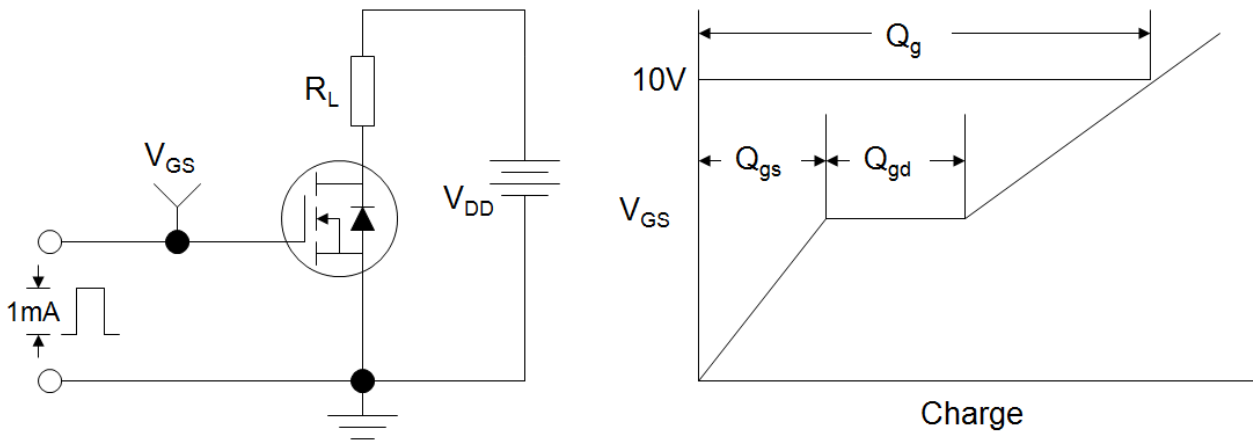


Figure A: Gate Charge Test Circuit and Waveforms

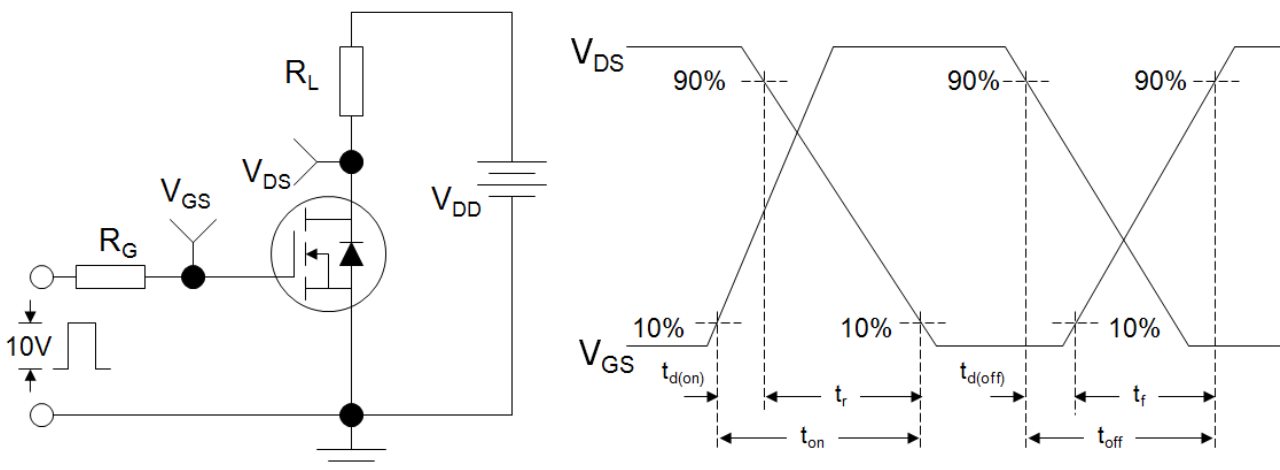


Figure B: Resistive Switching Test Circuit and Waveforms

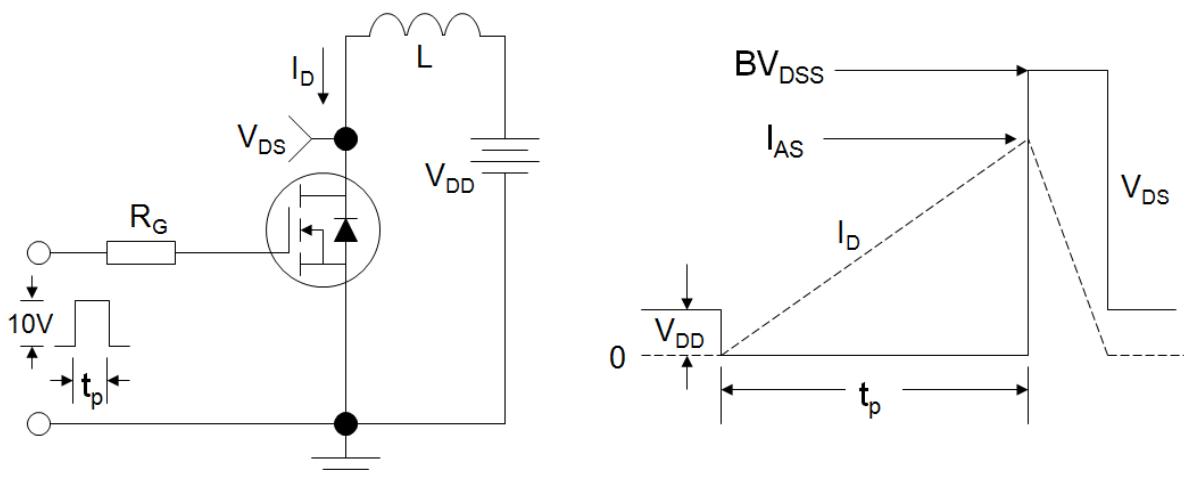
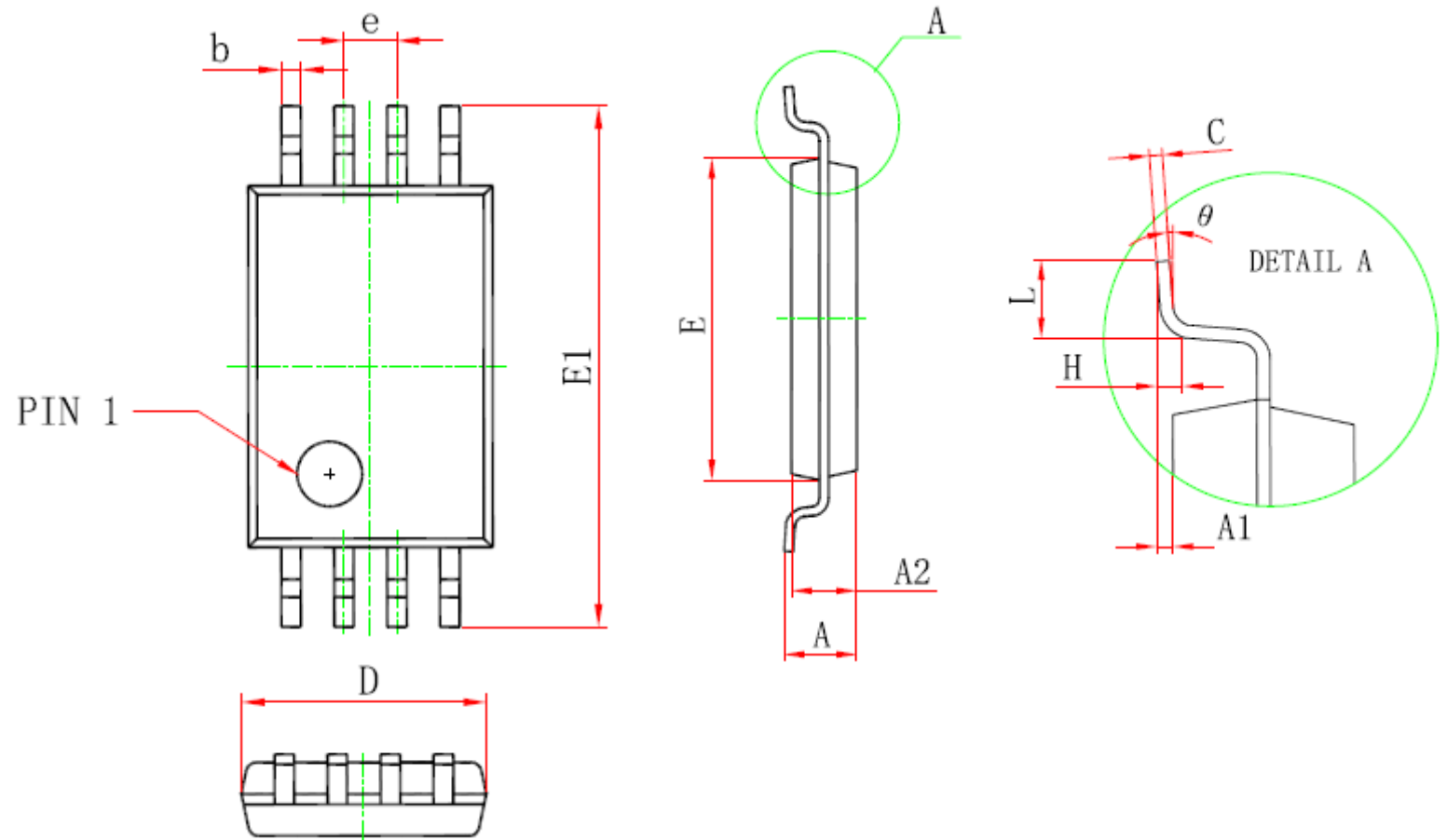


Figure C: Unclamped Inductive Switching (UIS) Test Circuit and Waveforms



TSSOP-8



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
D	2.900	3.100	0.114	0.122
E	4.300	4.500	0.169	0.177
b	0.190	0.300	0.007	0.012
c	0.090	0.200	0.004	0.008
E1	6.250	6.550	0.246	0.258
A		1.200		0.047
A2	0.800	1.000	0.031	0.039
A1	0.050	0.150	0.002	0.006
e	0.65 (BSC)		0.026 (BSC)	
L	0.500	0.700	0.020	0.028
H	0.25 (TYP)		0.01 (TYP)	
θ	1°	7°	1°	7°



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