
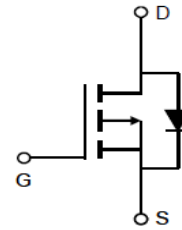
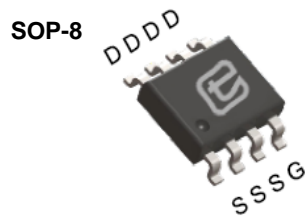


**30V P-Channel Trench MOSFET(Preliminary)**

<p><b>General Description</b></p> <ul style="list-style-type: none"> <li>● Trench Power technology</li> <li>● Low <math>R_{DS(ON)}</math></li> <li>● Low Gate Charge</li> <li>● Optimized for fast-switching applications</li> </ul> <p><b>Applications</b></p> <ul style="list-style-type: none"> <li>● Synchronous Rectification in DC/DC and AC/DC Converters</li> <li>● Isolated DC/DC Converters in Telecom and Industrial</li> </ul>	<p><b>Product Summary</b></p> <table> <tr> <td><math>V_{DS}</math></td> <td>-30V</td> </tr> <tr> <td><math>I_D</math> (at <math>V_{GS}=10V</math>)</td> <td>-12A</td> </tr> <tr> <td><math>R_{DS(ON)}</math> (at <math>V_{GS}=-10V</math>)</td> <td>&lt; 7.5m<math>\Omega</math></td> </tr> <tr> <td><math>R_{DS(ON)}</math> (at <math>V_{GS}=-4.5V</math>)</td> <td>&lt; 12m<math>\Omega</math></td> </tr> </table> <p>100% UIS Tested</p> 	$V_{DS}$	-30V	$I_D$ (at $V_{GS}=10V$ )	-12A	$R_{DS(ON)}$ (at $V_{GS}=-10V$ )	< 7.5m $\Omega$	$R_{DS(ON)}$ (at $V_{GS}=-4.5V$ )	< 12m $\Omega$
$V_{DS}$	-30V								
$I_D$ (at $V_{GS}=10V$ )	-12A								
$R_{DS(ON)}$ (at $V_{GS}=-10V$ )	< 7.5m $\Omega$								
$R_{DS(ON)}$ (at $V_{GS}=-4.5V$ )	< 12m $\Omega$								



Part Number	Package Type	Form	Marking
TTJ90P03AT	SOP-8	Tape & Reel	90P03AT

**Absolute Maximum Ratings ( $T_A = 25^\circ\text{C}$  unless otherwise noted)**

Parameter	Symbol	Maximum	Units
Drain-Source Voltage	$V_{DS}$	- 30	V
Gate-Source Voltage	$V_{GS}$	$\pm 20$	V
Continuous Drain Current <sup>B</sup>	$I_D$	$T_C = 25^\circ\text{C}$	-12
		$T_C = 100^\circ\text{C}$	-9.6
Pulsed Drain Current <sup>A</sup>	$I_{DM}$	-36	A
Avalanche Current <sup>A</sup>	$I_{AS}$	-22	A
Single Pulse Avalanche Energy $L = 0.3\text{mH}$ <sup>A</sup>	$E_{AS}$	135	mJ
Power Dissipation <sup>C</sup>	$P_D$	$T_C = 25^\circ\text{C}$	79
		$T_C = 100^\circ\text{C}$	39.5
Junction and Storage Temperature Range	$T_J, T_{STG}$	-55 to 175	$^\circ\text{C}$

**Thermal Characteristics**

Parameter	Symbol	Maximum	Units
Maximum Junction-to-Case	Steady-State $R_{\theta JC}$	1.9	$^\circ\text{C/W}$
Maximum Junction-to-Ambient	Steady-State $R_{\theta JA}$	100	



Electrical Characteristics( $T_J = 25^\circ\text{C}$ unless otherwise noted)						
Symbol	Parameter	Conditions	Value			Units
			Min	Typ	Max	
<b>STATIC PARAMETERS</b>						
$BV_{DSS}$	Drain-Source Breakdown Voltage	$I_D = -250\mu\text{A}, V_{GS} = 0\text{V}$	-30			V
$I_{DSS}$	Zero Gate Voltage Drain Current	$V_{DS} = -30\text{V}, V_{GS} = 0\text{V}$	$T_J = 25^\circ\text{C}$		-1	$\mu\text{A}$
			$T_J = 100^\circ\text{C}$		-100	
$I_{GSS}$	Gate-Body Leakage Current	$V_{DS} = 0\text{V}, V_{GS} = \pm 20\text{V}$			$\pm 100$	nA
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_D = -250\mu\text{A}$	-1	-1.7	-2.4	V
$R_{DS(ON)}$	Static Drain-Source On-Resistance	$V_{GS} = -10\text{V}, I_D = -20\text{A}$		6.3	7.5	$\text{m}\Omega$
		$V_{GS} = -4.5\text{V}, I_D = -20\text{A}$		10	12	$\text{m}\Omega$
$g_{FS}$	Forward Transconductance	$V_{DS} = -5\text{V}, I_D = -20\text{A}$		30		S
$V_{SD}$	Diode Forward Voltage	$I_S = -15\text{A}, V_{GS} = 0\text{V}$			-1	V
$I_S$	Maximum Body-Diode Continuous Current <sup>B</sup>				-46	A
<b>DYNAMIC PARAMETERS</b>						
$C_{iss}$	Input Capacitance	$V_{GS} = 0\text{V}, V_{DS} = -15\text{V}, f = 1\text{MHz}$		4942		$\text{pF}$
$C_{oss}$	Output Capacitance			473		
$C_{rss}$	Reverse Transfer Capacitance			461		
<b>SWITCHING PARAMETERS</b>						
$Q_g(10\text{V})$	Total Gate Charge	$V_{GS} = -10\text{V}, V_{DS} = -15\text{V}, I_D = -20\text{A}$		82		nC
$Q_{gs}$	Gate Source Charge			14		
$Q_{gd}$	Gate Drain Charge			16		
$t_{D(on)}$	Turn-On Delay Time	$V_{GS} = -10\text{V}, V_{DS} = -15\text{V}, I_D = -20\text{A}, R_G = 2.5\Omega$		182		ns
$t_r$	Turn-On Rise Time			262		
$T_{D(off)}$	Turn-Off Delay Time			1.3		
$t_f$	Turn-Off Fall Time			9.8		
$t_{rr}$	Body Diode Reverse Recovery Time	$I_F = -15\text{A}, di/dt = 100\text{A}/\mu\text{s}$		34		ns
$Q_{rr}$	Body Diode Reverse Recovery Charge			79		nC

A. Single pulse width limited by maximum junction temperature.

B. The maximum current rating is package limited.

C. The power dissipation  $P_D$  is based on  $T_{J(MAX)} = 175^\circ\text{C}$ , using junction-to-case thermal resistance, and is more useful in setting the upper dissipation limit for cases where additional heatsinking is used.



Typical Characteristics  $T_J = 25^\circ\text{C}$ , unless otherwise noted

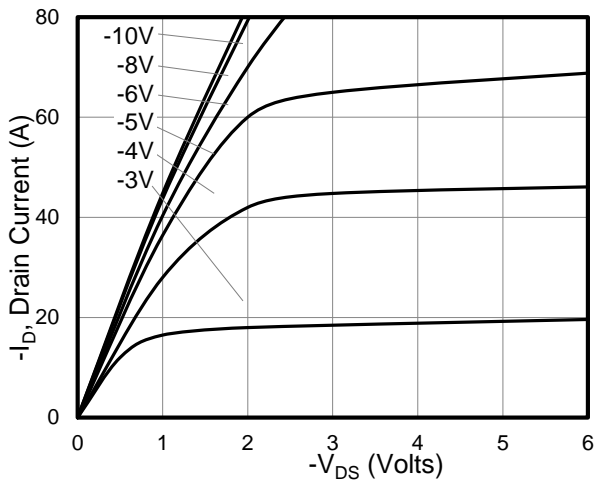


Figure 1: On-Region Characteristics

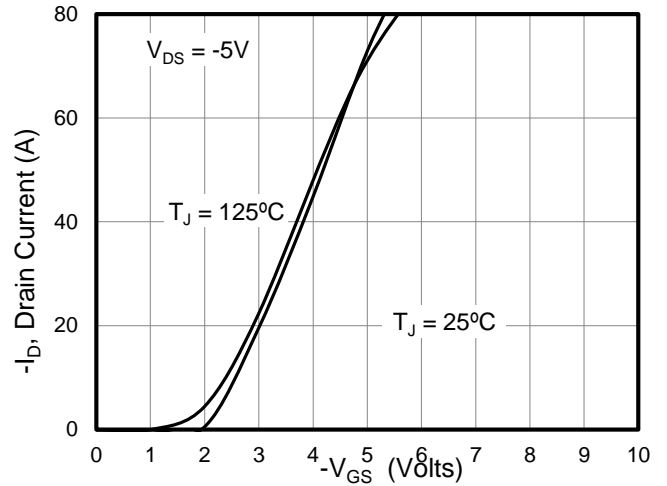


Figure 2: Transfer Characteristics

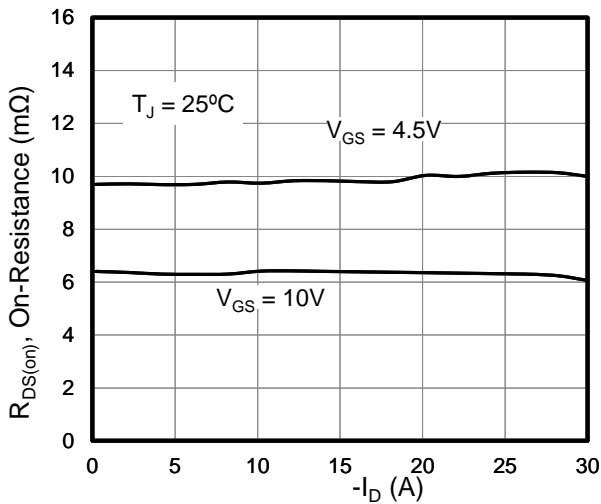


Figure 3: On-Resistance vs. Drain Current

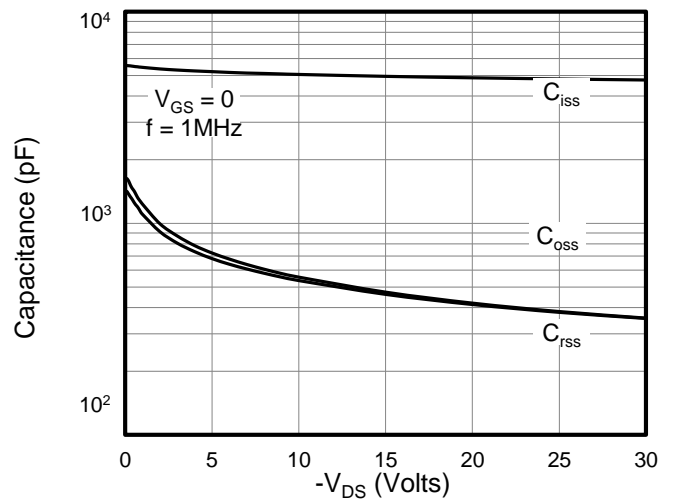


Figure 4: Capacitance Characteristics

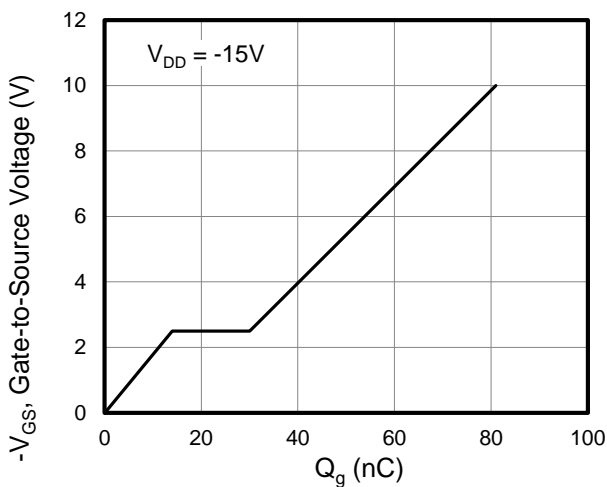


Figure 5: Gate Charge Characteristics

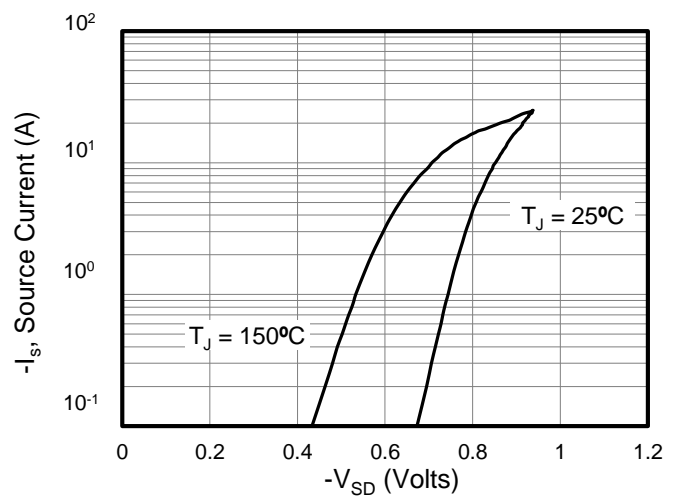


Figure 6: Body Diode Forward Voltage



Typical Characteristics  $T_J = 25^\circ\text{C}$ , unless otherwise noted

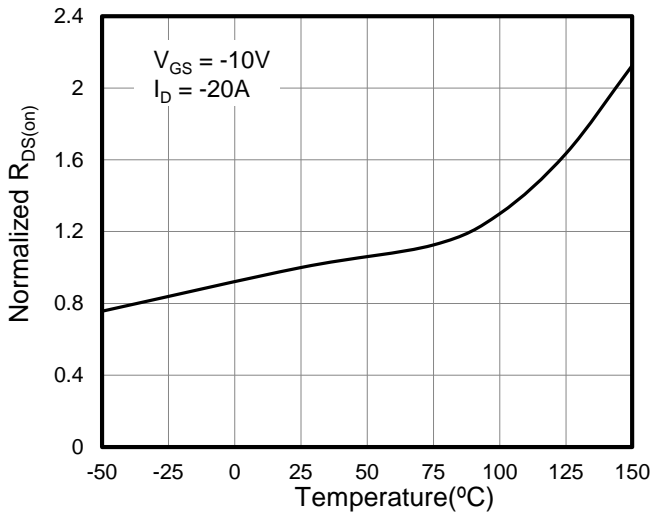


Figure 7: On-Resistance vs. Junction Temperature

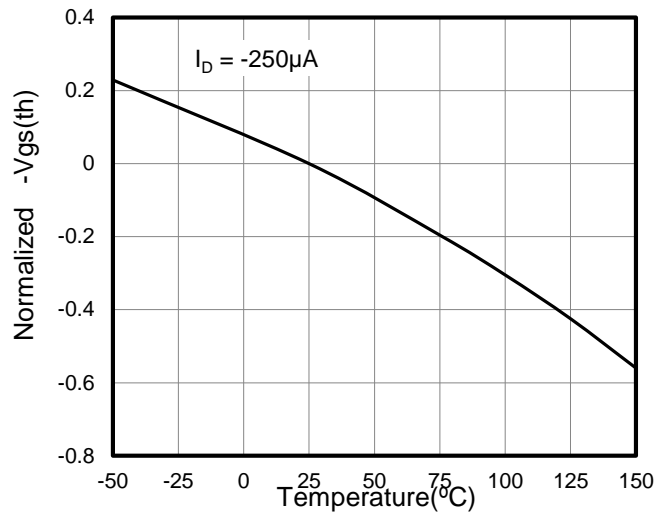


Figure 8:  $V_{GS(th)}$  vs. Junction Temperature

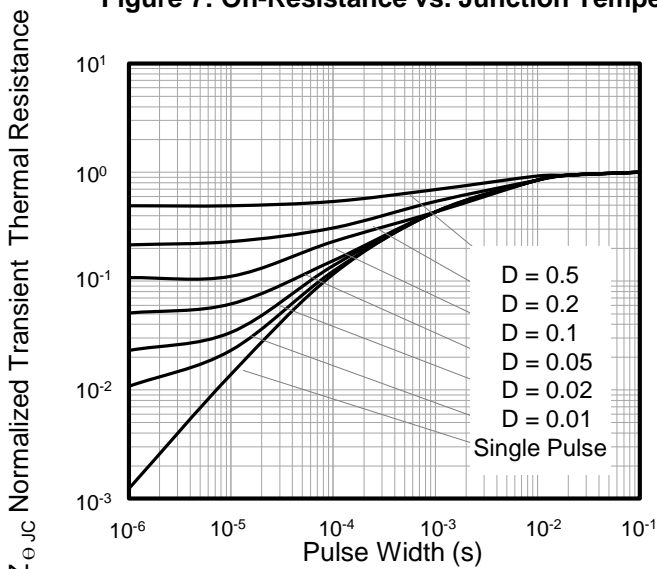


Figure 11: Normalized Transient Thermal Resistance

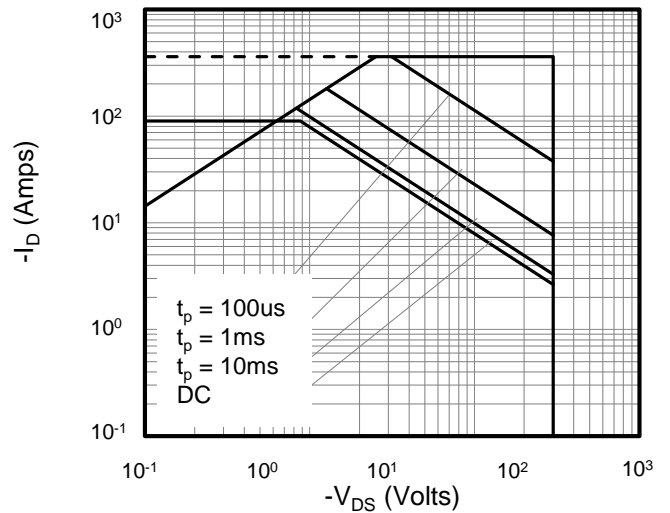


Figure 12: Safe Operating Area



Figure A: Gate Charge Test Circuit and Waveform

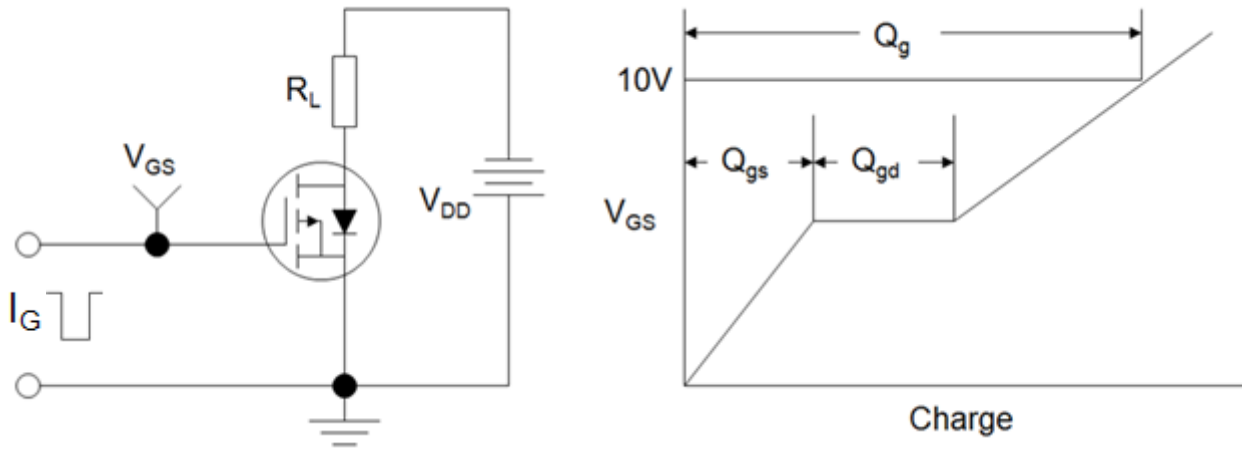


Figure B: Resistive Switching Test Circuit and Waveform

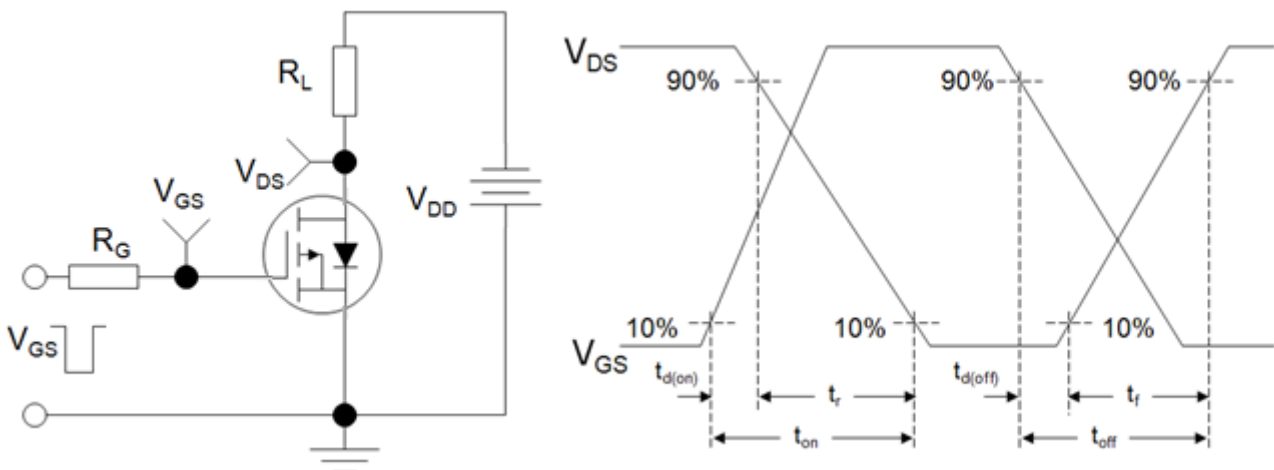
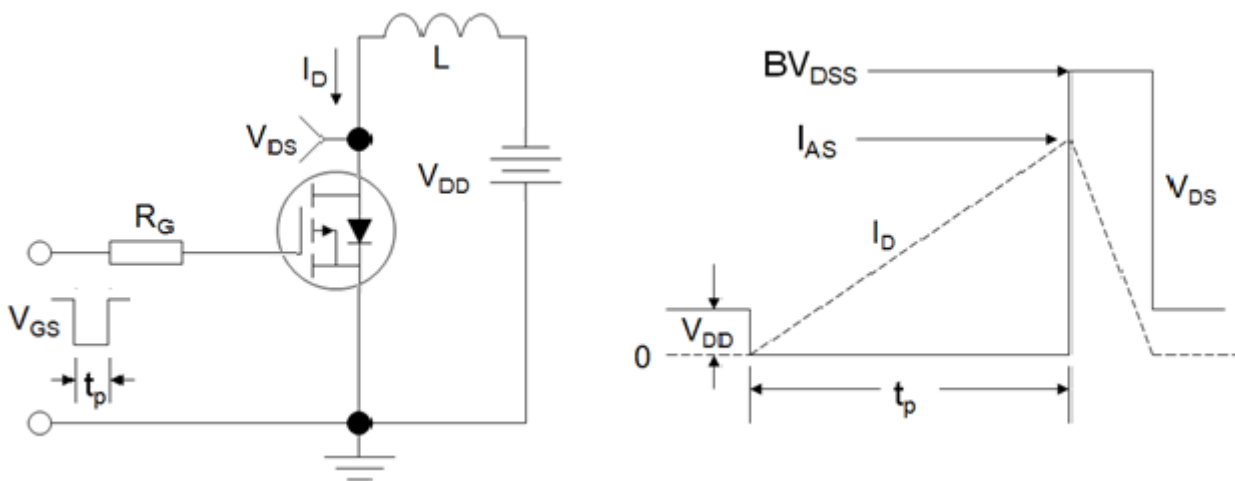
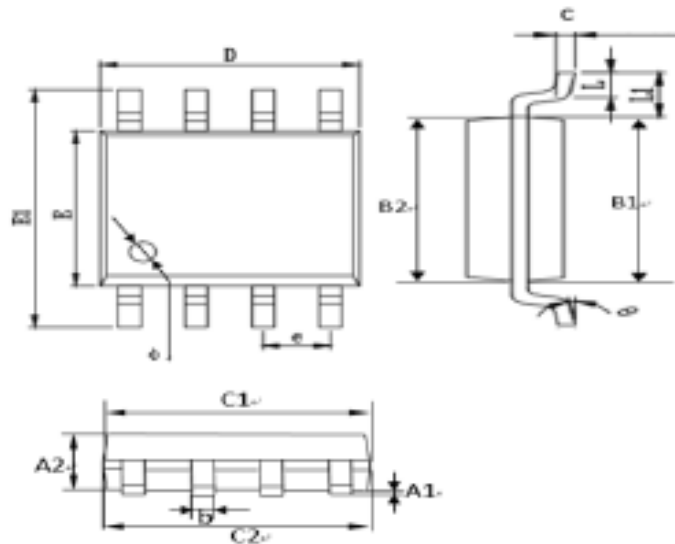


Figure C: Unclamped Inductive Switching Test Circuit and Waveform





### SOP-8(N)



符号	标准	下公差	上公差	下限值		上限值	
				内控	放行	内控	放行
A1 足高	ESOP8	0.04	0.02	0.025	0.02	0.055	0.06
	SOP8	0.095	0.045	0.06	0.05	0.13	0.14
A2	1.4	0.1	0.1	1.35	1.3	1.45	1.5
B1	3.9	0.05	0.05	3.86	3.85	3.94	3.95
B2	3.85	0.05	0.05	3.81	3.80	3.89	3.90
b	0.42	0.03	0.03	0.40	0.39	0.44	0.45
C1	4.85	0.05	0.05	4.81	4.80	4.89	4.90
C2	4.9	0.05	0.05	4.86	4.85	4.94	4.95
c	0.235	0.025	0.025	0.215	0.21	0.255	0.26
D	4.9	0.2	0.2	4.8	4.7	5.0	5.1
E	3.9	0.2	0.2	3.8	3.7	4.0	4.1
E1	6.0	0.2	0.2	5.9	5.8	6.1	6.2
e	1.27	0.03	0.03	1.25	1.24	1.29	1.30
L	0.6	0.1	0.1	0.52	0.5	0.68	0.7
*L1	1.045	0.055	0.055	1.00	0.99	1.09	1.1
$\theta_1$	4°	4°	4°	0°	0°	8°	8°



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