
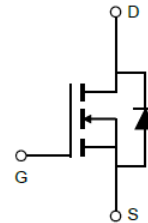
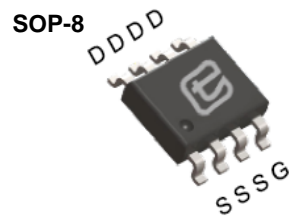


**30V N-Channel Trench MOSFET(Preliminary)**

<p>General Description</p> <ul style="list-style-type: none"> ● Trench Power technology ● Low $R_{DS(ON)}$ ● Low Gate Charge ● Optimized for fast-switching applications <p>Applications</p> <ul style="list-style-type: none"> ● Synchronous Rectification in DC/DC and AC/DC Converters ● Isolated DC/DC Converters in Telecom and Industrial 	<p>Product Summary</p> <table> <tr> <td>V_{DS}</td> <td>30V</td> </tr> <tr> <td>I_D (at $V_{GS}=10V$)</td> <td>15A</td> </tr> <tr> <td>$R_{DS(ON)}$ (at $V_{GS}=10V$)</td> <td><8mΩ</td> </tr> <tr> <td>$R_{DS(ON)}$ (at $V_{GS}=4.5V$)</td> <td><10mΩ</td> </tr> </table> <div style="text-align: right;">  </div>	V_{DS}	30V	I_D (at $V_{GS}=10V$)	15A	$R_{DS(ON)}$ (at $V_{GS}=10V$)	<8m Ω	$R_{DS(ON)}$ (at $V_{GS}=4.5V$)	<10m Ω
V_{DS}	30V								
I_D (at $V_{GS}=10V$)	15A								
$R_{DS(ON)}$ (at $V_{GS}=10V$)	<8m Ω								
$R_{DS(ON)}$ (at $V_{GS}=4.5V$)	<10m Ω								



Part Number	Package Type	Form	Marking
TTJ15N03AT	SOP-8	Tape & Reel	TTJ15N03AT

Absolute Maximum Ratings ($T_A = 25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	Maximum	Units
Drain-Source Voltage	V_{DS}	30	V
Gate-Source Voltage	V_{GS}	± 20	V
Continuous Drain Current ^B	I_D	$T_C = 25^\circ\text{C}$	15
		$T_C = 70^\circ\text{C}$	12
Pulsed Drain Current ^A	I_{DM}	45	A
Avalanche Current ^A	I_{AS}	23	A
Single Pulse Avalanche Energy	E_{AS}	79	mJ
Power Dissipation ^C	P_D	$T_C = 25^\circ\text{C}$	3.4
		$T_C = 70^\circ\text{C}$	2.1
Junction and Storage Temperature Range	T_J, T_{STG}	-55 to 150	$^\circ\text{C}$

Thermal Characteristics

Parameter	Symbol	Maximum	Units
Maximum Junction-to-Lead	$R_{\theta JL}$	22	$^\circ\text{C/W}$
Maximum Junction-to-Ambient	$R_{\theta JA}$	100	



Electrical Characteristics($T_J = 25^\circ\text{C}$ unless otherwise noted)						
Symbol	Parameter	Conditions	Value			Units
			Min	Typ	Max	
STATIC PARAMETERS						
BV_{DSS}	Drain-Source Breakdown Voltage	$I_D = 250\mu\text{A}, V_{GS} = 0\text{V}$	30			V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{DS} = 30\text{V}, V_{GS} = 0\text{V}$	$T_J = 25^\circ\text{C}$		1	μA
			$T_J = 125^\circ\text{C}$		100	
I_{GSS}	Gate-Body Leakage Current	$V_{DS} = 0\text{V}, V_{GS} = \pm 20\text{V}$			± 100	nA
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_D = 250\mu\text{A}$	1	1.4	2	V
$R_{DS(on)}$	Static Drain-Source On-Resistance	$V_{GS} = 10\text{V}, I_D = 15\text{A}$		6.7	8	$\text{m}\Omega$
		$V_{GS} = 4.5\text{V}, I_D = 15\text{A}$		8.3	10	$\text{m}\Omega$
g_{FS}	Forward Transconductance	$V_{DS} = 5\text{V}, I_D = 15\text{A}$		21		S
V_{SD}	Diode Forward Voltage	$I_S = 15\text{A}, V_{GS} = 0\text{V}$			1	V
I_S	Maximum Body-Diode Continuous Current ^B				15	A
DYNAMIC PARAMETERS						
C_{iss}	Input Capacitance	$V_{GS} = 0\text{V}, V_{DS} = 15\text{V}, f = 1\text{MHz}$		1592		pF
C_{oss}	Output Capacitance			229		
C_{rss}	Reverse Transfer Capacitance			183		
R_g	Gate Resistance	$f = 1\text{MHz}$		1.5		Ω
SWITCHING PARAMETERS						
$Q_g(10\text{V})$	Total Gate Charge	$V_{GS} = 10\text{V}, V_{DS} = 15\text{V}, I_D = 15\text{A}$		29		nC
$Q_g(4.5\text{V})$	Total Gate Charge			15		
Q_{gs}	Gate Source Charge			3.8		
Q_{gd}	Gate Drain Charge			7		
$t_{D(on)}$	Turn-On Delay Time	$V_{GS} = 10\text{V}, V_{DS} = 15\text{V}, I_D = 15\text{A}, R_G = 1.8\Omega$		33.3		ns
t_r	Turn-On Rise Time			3.3		
$T_{D(off)}$	Turn-Off Delay Time			56.6		
t_f	Turn-Off Fall Time			3.5		
t_{rr}	Body Diode Reverse Recovery Time	$I_F = 20\text{A}, di/dt = 100\text{A}/\mu\text{s}$		38		ns
Q_{rr}	Body Diode Reverse Recovery Charge			16.6		nC

A. Single pulse width limited by maximum junction temperature.

B. The maximum current rating is package limited.

C. The power dissipation P_D is based on $T_{J(MAX)} = 150^\circ\text{C}$, using junction-to-case thermal resistance, and is more useful in setting the upper dissipation limit for cases where additional heatsinking is used.



TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS

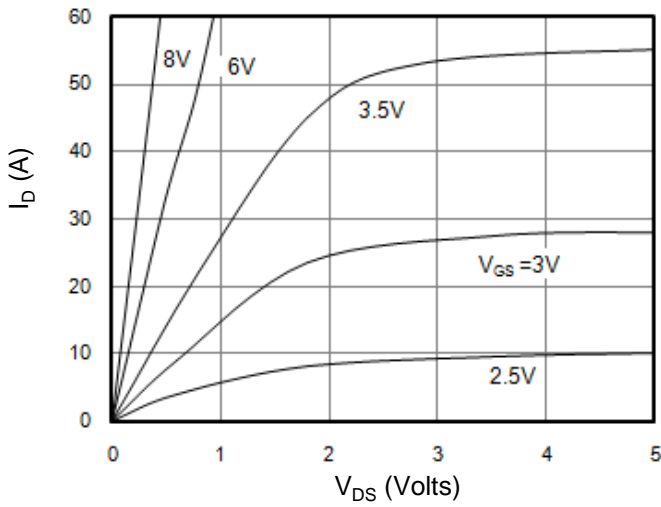


Figure 1: On-Region Characteristics

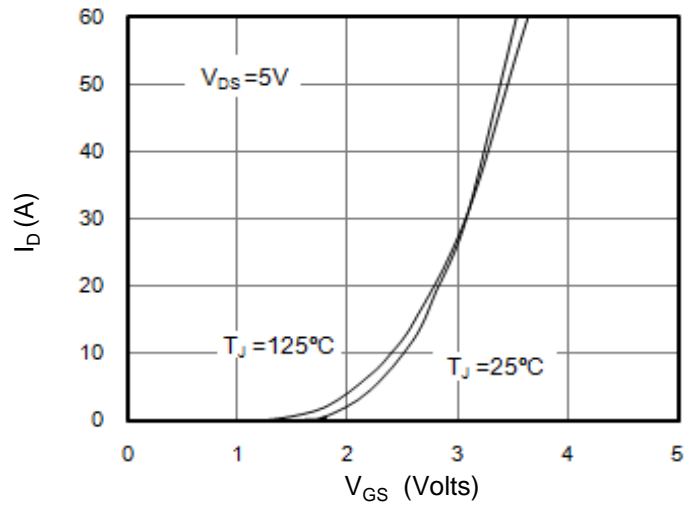


Figure 2: Transfer Characteristics

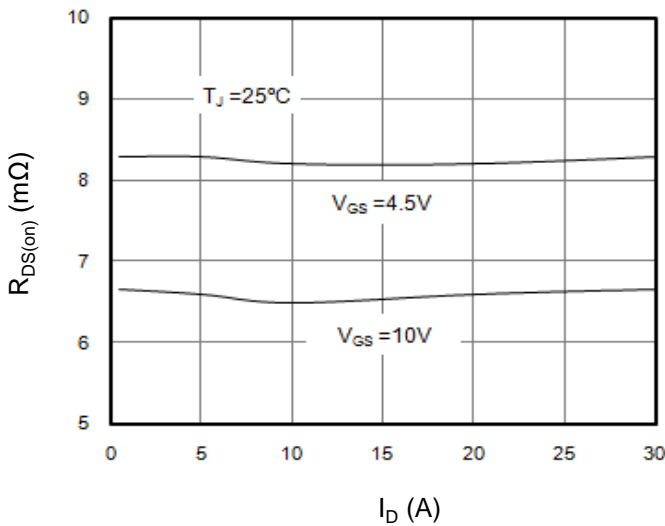


Figure 3: On-Resistance vs. Drain Current

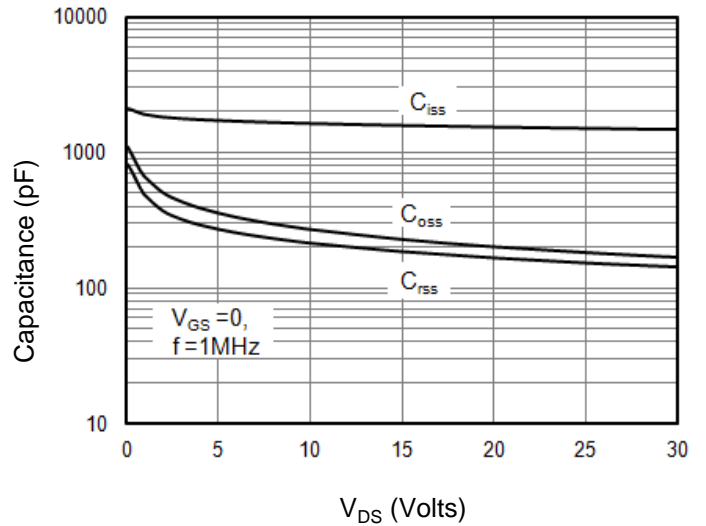


Figure 4: Capacitance Characteristics

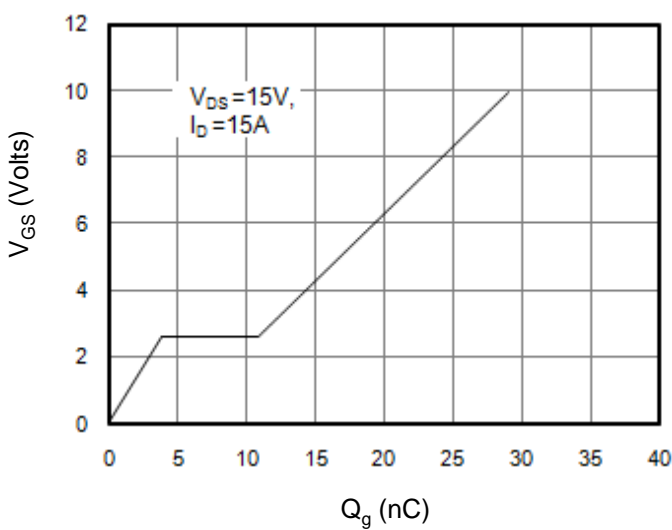


Figure 5: Gate Charge Characteristics

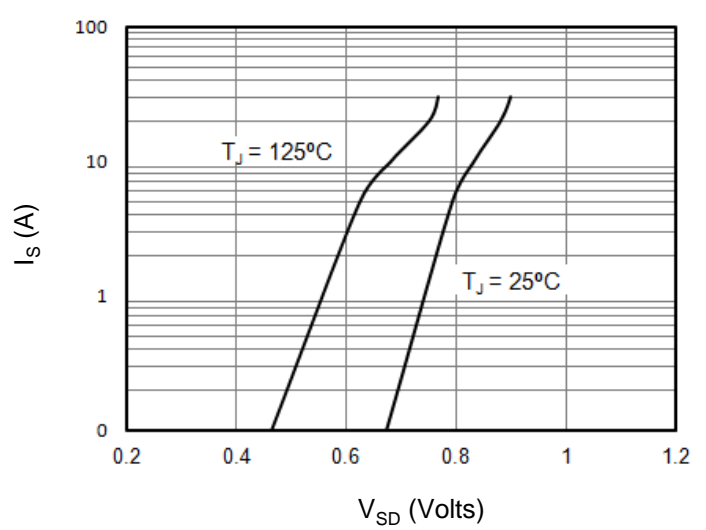


Figure 6: Body Diode Forward Voltage



TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS

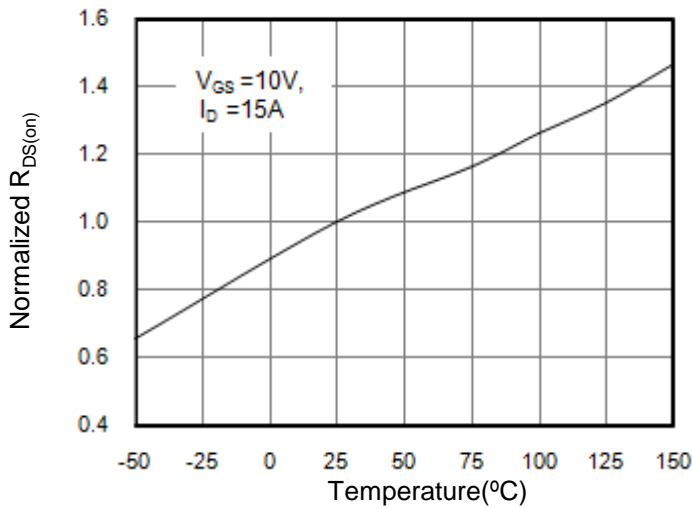


Figure 7: On-Resistance vs. Junction Temperature

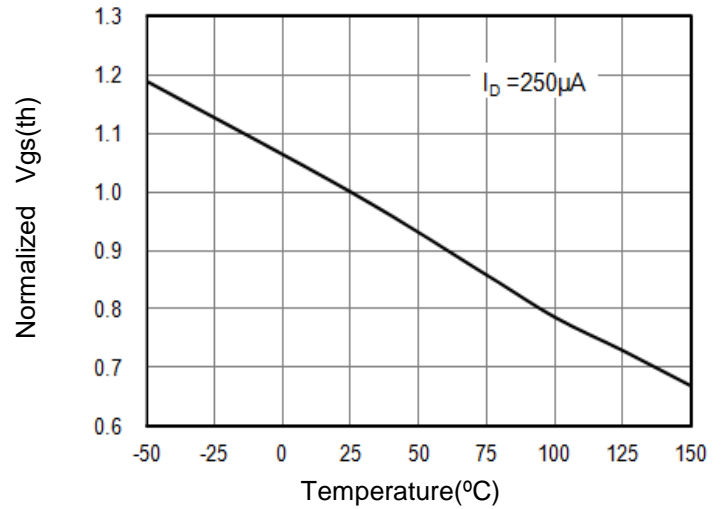


Figure 8: V_GS(th) vs. Junction Temperature

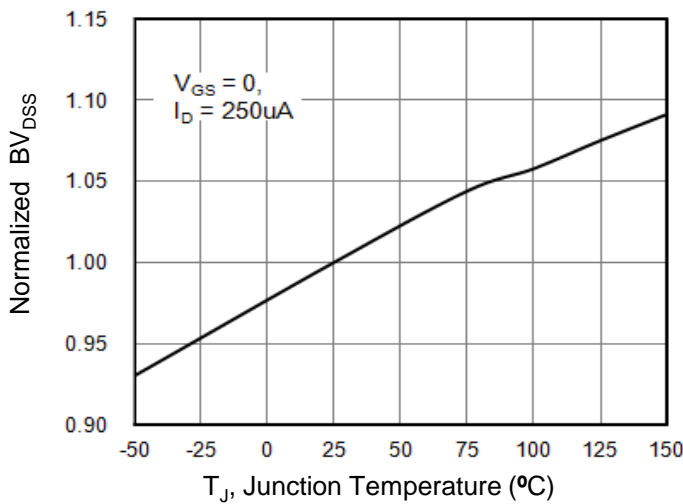


Figure 9: BV_DS vs. Junction Temperature

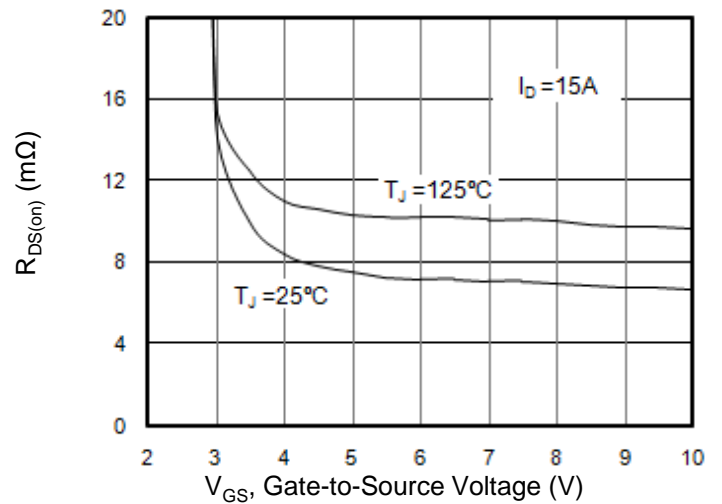


Figure 10: On-Resistance vs. Gate-Source Voltage

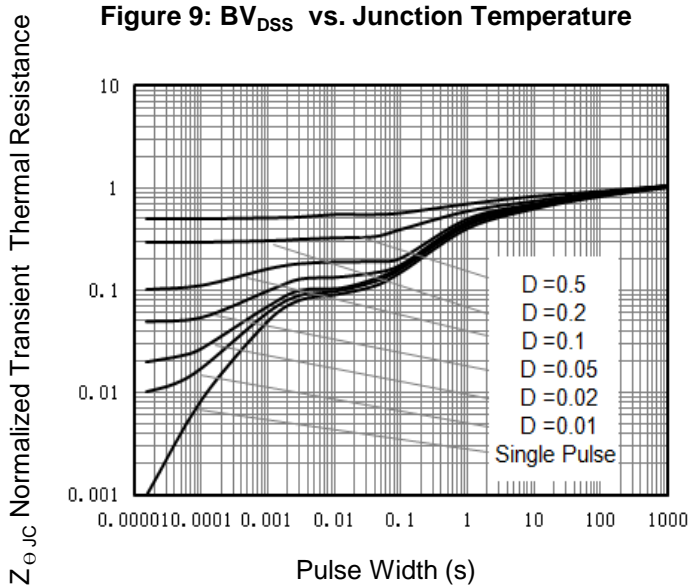


Figure 11: Normalized Transient Thermal Resistance

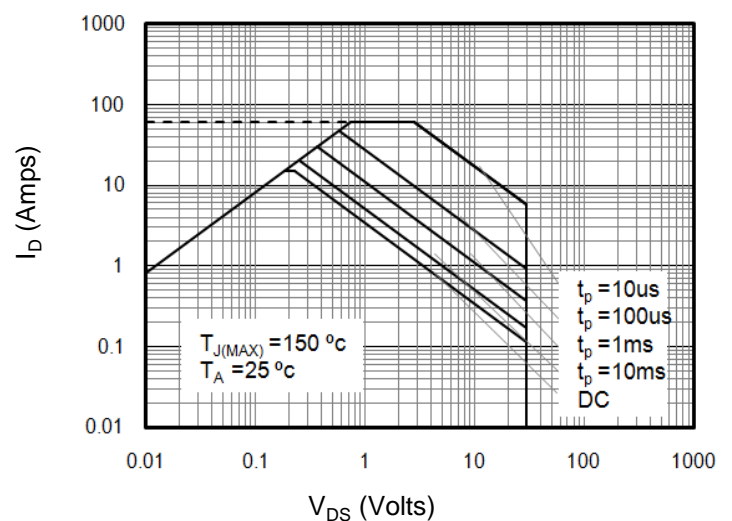


Figure 12: Safe Operating Area



Figure A: Gate Charge Test Circuit and Waveforms

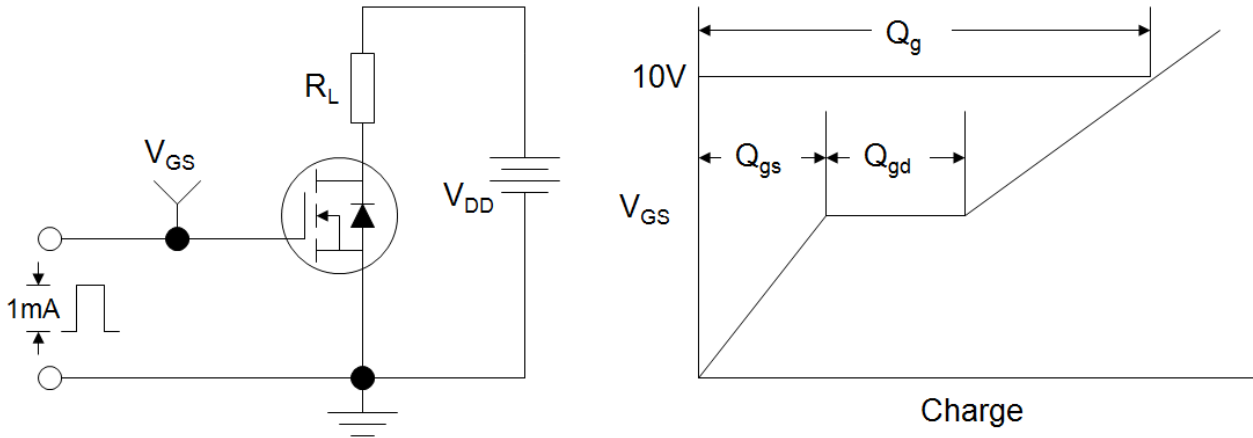


Figure B: Resistive Switching Test Circuit and Waveforms

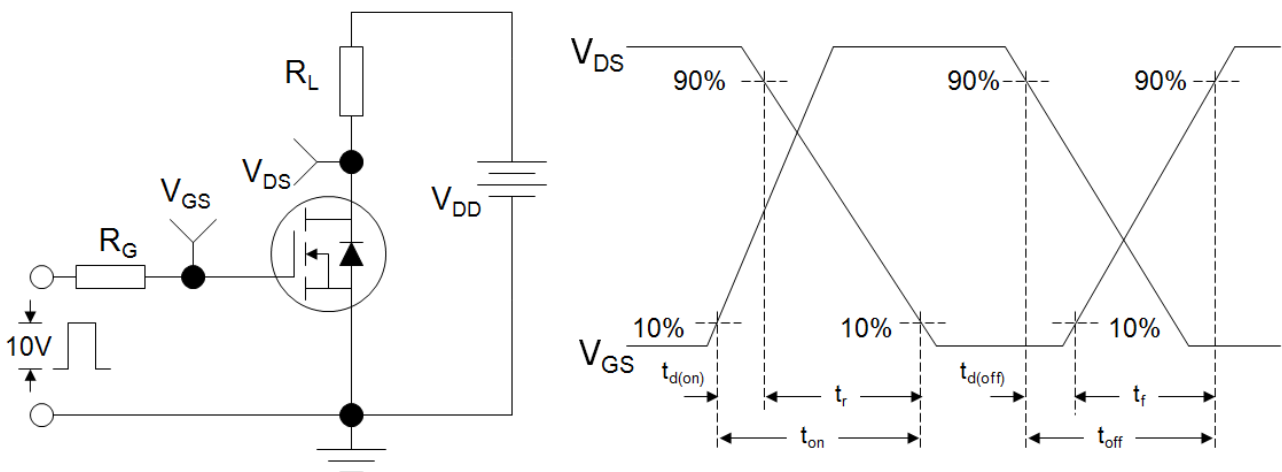
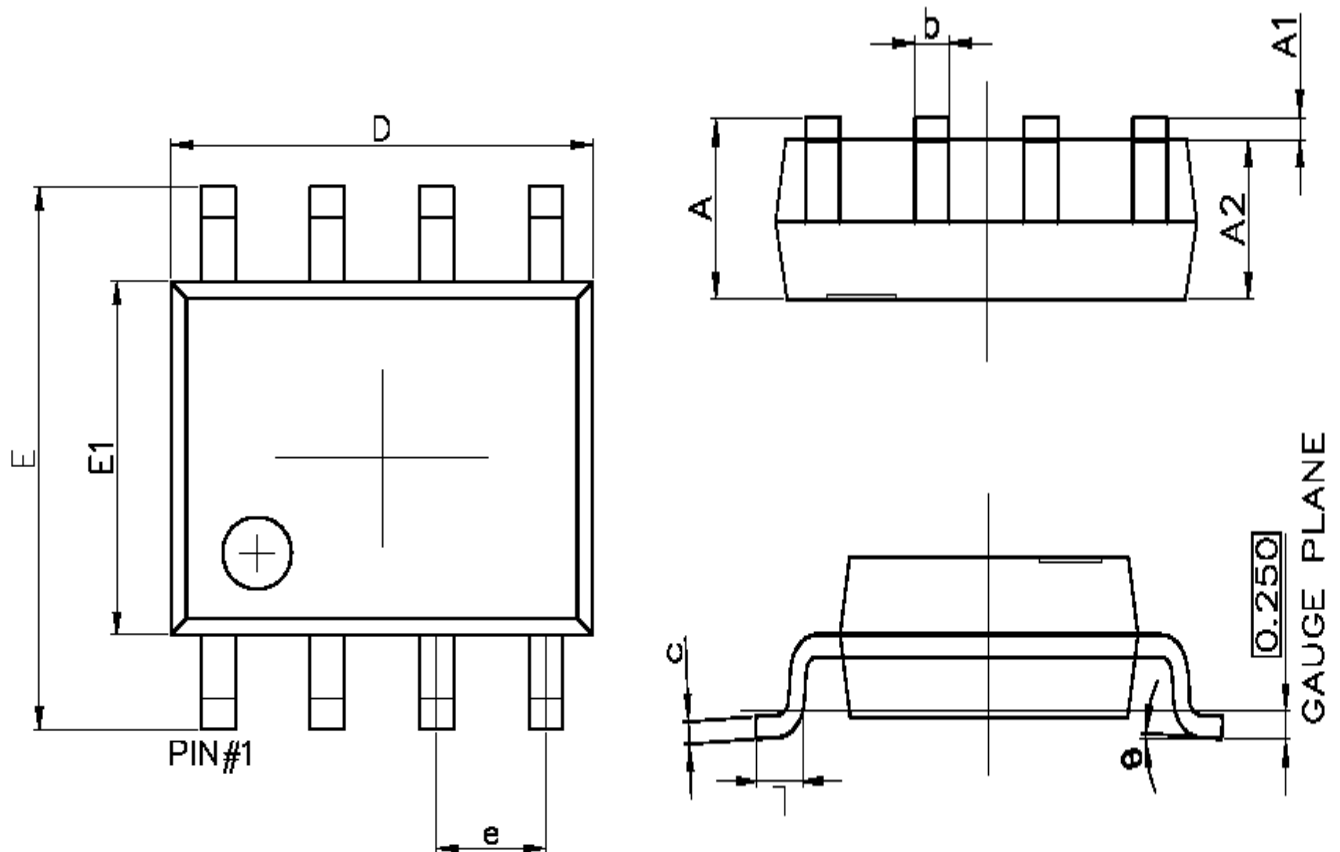


Figure C: Unclamped Inductive Switching (UIS) Test Circuit and Waveforms





SOP-8



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	1.450	1.750	0.053	0.069
A1	0.100	0.250	0.004	0.010
A2	1.350	1.550	0.053	0.061
b	0.330	0.510	0.013	0.020
c	0.170	0.250	0.007	0.010
D	4.700	5.100	0.185	0.201
e	1.270 (BSC)		0.050 (BSC)	
E	5.800	6.200	0.228	0.244
E1	3.800	4.000	0.150	0.157
L	0.400	1.270	0.016	0.050
θ	0°	8°	0°	8°



Disclaimer

All product specifications and data are subject to change without notice.

For documents and material available from this datasheet, Wuxi Unigroup does not warrant or assume any legal liability or responsibility for the accuracy, completeness of any product or technology disclosed hereunder.

No license, express or implied, by estoppels or otherwise, to any intellectual property rights is granted by this document or by any conduct of Wuxi Unigroup.

The products shown herein are not designed for use in medical, life-saving, or life-sustaining applications. Customers using or selling Wuxi Unigroup products not expressly indicated for use in such applications do so entirely at their own risk and agree to fully indemnify Wuxi Unigroup for any damages arising or resulting from such use or sale.

Wuxi Unigroup disclaims any and all liability arising out of the use or application of any product described herein or of any information provided herein to the maximum extent permitted by law. The product specifications do not expand or otherwise modify Wuxi Unigroup's terms and conditions of purchase, including but not limited to the warranty expressed therein, which apply to these products.

Wuxi Unigroup Microelectronics CO., LTD. strives to supply high-quality high-reliability products. However, any and all semiconductor products fail with some probability. It is possible that these probabilistic failures could give rise to accidents or events that could endanger human lives that could give rise to smoke or fire, or that could cause damage to other property. When designing equipment, adopt safety measures so that these kinds of accidents or events cannot occur. Such measures include but are not limited to protective circuits and error prevention circuits for safe design, redundant design, and structural design.

In the event that any or all Wuxi Unigroup products (including technical data, services) described or contained herein are controlled under any of applicable local export control laws and regulations, such products must not be exported without obtaining the export license from the authorities concerned in accordance with the above law.

Information (including circuit diagrams and circuit parameters) herein is for example only. It is not guaranteed for volume production. Wuxi Unigroup believes information herein is accurate and reliable, but no guarantees are made or implied regarding its use or any infringements of intellectual property rights or other rights of third parties.