

30V N-Channel Trench MOSFET(Preliminary)

-			Product Summary	
 Trench Power technology Low P 			V _{DS}	30V
 Low R_{DS(ON)} Low Gate Charge 			I_D (at V_{GS} =10V)	160A
 High Current Capability 			$R_{DS(ON)}$ (at V _{GS} =10V) $R_{DS(ON)}$ (at V _{GS} =4.5V)	< 1.8mΩ < 2.5mΩ
			$(\alpha_{\rm CS} - 4.5 \text{ V})$	< 2.01112
Applications				
• Synchronous Rectification in	n DC/DC and AC	DC Converters	100% UIS Tested	Pous
 Isolated DC/DC Converters i 	n Telecom and Ir	ndustrial		Kons
C	PFN5x6	S S G	G G S	
Part Number	Packa	де Туре	Form	Marking
TTG160N03AT	DF	N5x6	Tape&Reel	160N03AT
Absolute Maximum Ra Parameter	tings (T _A =2	5ºC unless o	therwise noted) Maximum	Units
	tings (T _A =2	1		Units V
Parameter	tings (T _A =2	Symbol	Maximum	
Parameter Drain-Source Voltage Gate-Source Voltage	tings (T _A =2 T _c =25°C	Symbol V _{DS} V _{GS}	Maximum 30	V V
Parameter Drain-Source Voltage	1	Symbol V _{DS}	Maximum 30 ±20	V
Parameter Drain-Source Voltage Gate-Source Voltage	T _c =25°C	Symbol V _{DS} V _{GS}	Maximum 30 ±20 160	V V
Parameter Drain-Source Voltage Gate-Source Voltage Continuous Drain Current	T _c =25°C	Symbol V _{DS} V _{GS} I _D	Maximum 30 ±20 160 110	V V A
Parameter Drain-Source Voltage Gate-Source Voltage Continuous Drain Current A Pulsed Drain Current	T _c =25°C	Symbol V _{DS} V _{GS} I _D I _{DM}	Maximum 30 ±20 160 110 480	V V A A
Parameter Drain-Source Voltage Gate-Source Voltage Continuous Drain Current A Pulsed Drain Current Avalanche Current A Single Pulse Avalanche Energy	$T_{c} = 25^{\circ}C$ $T_{c} = 100^{\circ}C$	Symbol V _{DS} V _{GS} I _D I _{DM} I _{AS} E _{AS}	Maximum 30 ±20 160 110 480 56	V V A A A A
Parameter Drain-Source Voltage Gate-Source Voltage Continuous Drain Current Avalanche Current	$T_{c} = 25^{\circ}C$ $T_{c} = 100^{\circ}C$ L = 0.3mH ^A	Symbol V _{DS} V _{GS} I _D I _{DM} I _{AS}	Maximum 30 ±20 160 110 480 56 470	V V A A A M J
Parameter Drain-Source Voltage Gate-Source Voltage Continuous Drain Current A Pulsed Drain Current Avalanche Current A Single Pulse Avalanche Energy	$T_{c} = 25^{\circ}C$ $T_{c} = 100^{\circ}C$ $L = 0.3mH^{A}$ $T_{c} = 25^{\circ}C$ $T_{c} = 100^{\circ}C$	Symbol V _{DS} V _{GS} I _D I _{DM} I _{AS} E _{AS}	Maximum 30 ±20 160 110 480 56 470 136	V V A A A M M W
Parameter Drain-Source Voltage Gate-Source Voltage Continuous Drain Current A Pulsed Drain Current Avalanche Current A Single Pulse Avalanche Energy Power Dissipation C	$T_{c} = 25^{\circ}C$ $T_{c} = 100^{\circ}C$ $L = 0.3mH^{A}$ $T_{c} = 25^{\circ}C$ $T_{c} = 100^{\circ}C$	Symbol V _{DS} V _{GS} I _D I _{DM} I _{AS} E _{AS} P _D	Maximum 30 ±20 160 110 480 56 470 136 68	V V A A A M M W W
Parameter Drain-Source Voltage Gate-Source Voltage Continuous Drain Current Avalanche Drain Current Avalanche Current Asingle Pulse Avalanche Energy Power Dissipation C Junction and Storage Temperatu	$T_{c} = 25^{\circ}C$ $T_{c} = 100^{\circ}C$ $L = 0.3mH^{A}$ $T_{c} = 25^{\circ}C$ $T_{c} = 100^{\circ}C$	Symbol V _{DS} V _{GS} I _D I _{DM} I _{AS} E _{AS} P _D	Maximum 30 ±20 160 110 480 56 470 136 68	V V A A A M M W W
Parameter Drain-Source Voltage Gate-Source Voltage Continuous Drain Current Avalanche Current Avalanche Current Power Dissipation C Junction and Storage Temperatu Thermal Characteristics	$T_{c} = 25^{\circ}C$ $T_{c} = 100^{\circ}C$ $L = 0.3mH^{A}$ $T_{c} = 25^{\circ}C$ $T_{c} = 100^{\circ}C$	Symbol V _{DS} V _{GS} I _D I _{DM} I _{AS} E _{AS} P _D T _J , T _{STG}	Maximum 30 ±20 160 110 480 56 470 136 68 -55 to 175	V V A A A M M W W W V V



Electric	cal Characteristics(T _J =25°C u	nless otherwise r	noted)				
Cumphiel	Devemeter	rameter Conditions		Value			
Symbol	Farameter			Min	Тур	Max	Units
STATIC P	ARAMETERS					-	
BV_{DSS}	Drain-Source Breakdown Voltage	$I_{D} = 250 \mu A, V_{GS} = 0V$		30			V
I _{DSS} Zero Gate Voltage Drain Current		T _J =25⁰C			1		
	Zero Gate Voltage Drain Current	V _{DS} =30V, V _{GS} =0V	T _J =125°C			100	μA
I _{GSS}	Gate-Body Leakage Current	$V_{DS} = 0V, V_{GS} = \pm 20V$				±100	nA
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _D =250µA		1	1.6	2.4	V
	Statia Drain Source On Desistance	V _{GS} =10V, I _D =30A		1.3	1.8	mΩ	
R _{DS(ON)}	Static Drain-Source On-Resistance	V _{GS} =4.5V, I _D =30A		2.0	2.5	mΩ	
9 _{FS}	Forward Transconductance	V _{DS} =10V, I _D =20A			61		S
V _{SD}	Diode Forward Voltage	I _S =30A, V _{GS} =0V				1	V
ls	Maximum Body-Diode Continuous Curre	ent ^B				51	А
DYNAMIC	PARAMETERS			_	-	-	_
C _{iss}	Input Capacitance				8826		
C _{oss}	Output Capacitance	V _{GS} =0V, V _{DS} =15V, f =1MH _Z			1320		pF
C _{rss}	Reverse Transfer Capacitance				1386		
R _g	Gate Resistance	f =1MH _z			1.7		Ω
SWITCHI	NG PARAMETERS						
Q _g	Total Gate Charge	V _{GS} =10V,V _{DS} =15V, I _D =50A			177		
Q _{gs}	Gate Source Charge				29		nC
Q_{gd}	Gate Drain Charge				35		
t _{D(on)}	Turn-On Delay Time	V _{GS} =10V,V _{DS} =15V, I _D =50A,			30		
t _r	Turn-On Rise Time				29		
T _{D(off)}	Turn-Off Delay Time	$R_{G} = 3\Omega$			101		ns
t _f	Turn-Off Fall Time	1			48		
t _{rr}	Body Diode Reverse Recovery Time	I _F =30A, di/dt =100A/µ			47		ns
Q _{rr}	Body Diode Reverse Recovery Charge	$\mu_{\rm F} = 30 \text{Å}, \ \text{u/ut} = 100 \text{Å/}\mu$	15		43		nC

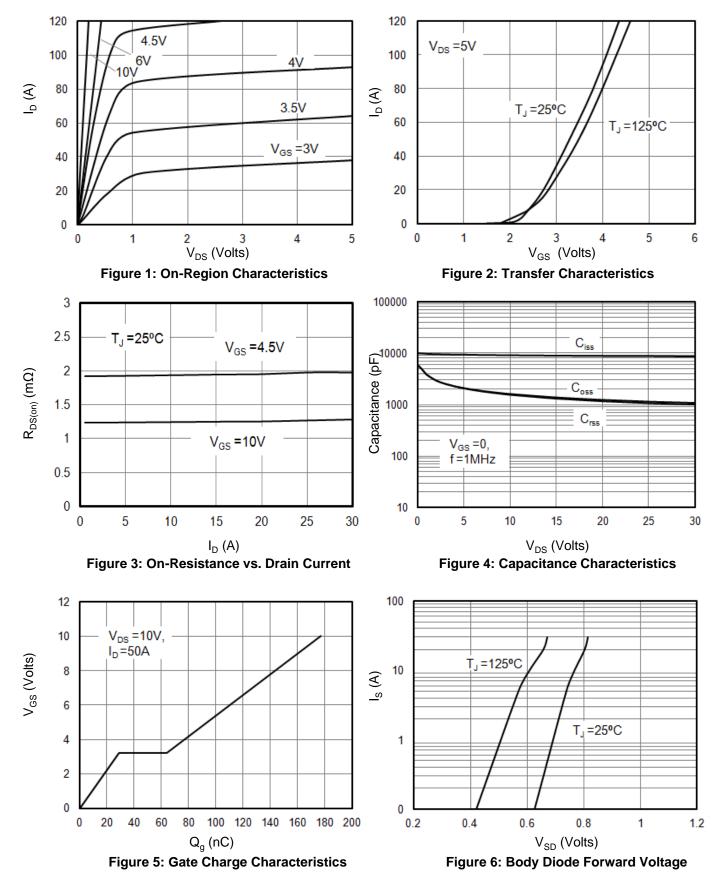
A. Single pulse width limited by maximum junction temperature.

B. The maximum current rating is package limited.

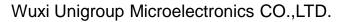
C. The power dissipation P_D is based on $T_{J(MAX)} = 175^{\circ}$ C, using junction-to-case thermal resistance, and is more useful in setting the upper dissipation limit for cases where additional heatsinking is used.



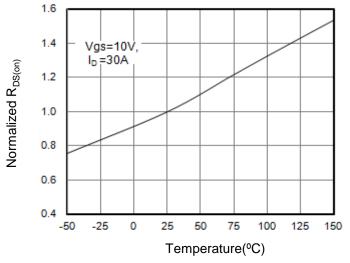
TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS

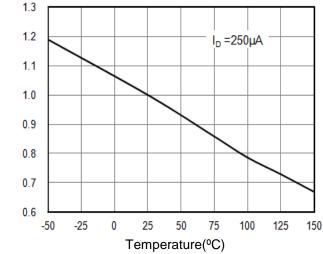


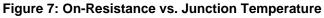


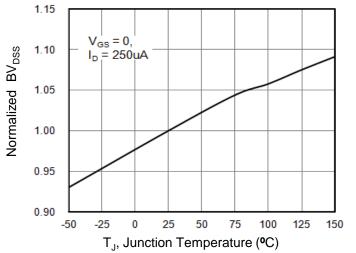


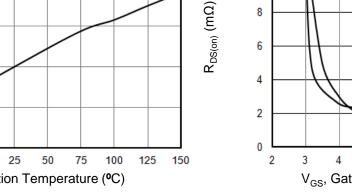
TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS











Normalized Vgs(th)

Figure 9: BV_{DSS} vs. Junction Temperature Figure 10: On-Resistance vs. Gate-Source Voltage

D =0.5

D =0.2

D =0.1

D =0.05

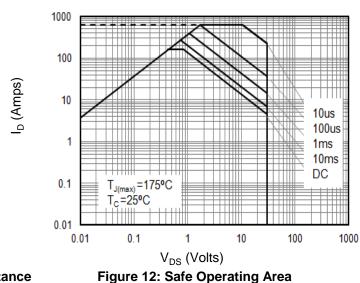
D =0.02

D =0.01

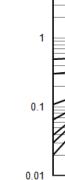
Single Pulse

1

0.1



 $Z_{\,\Theta\,JC}$ Normalized Transient Thermal Resistance

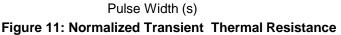


0.00001

0.0001

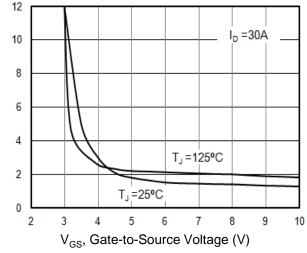
0.001

10

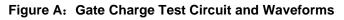


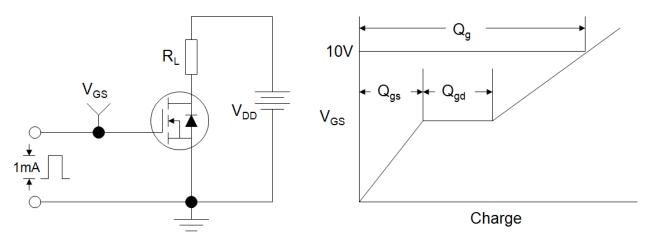
0.01

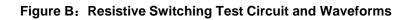




10







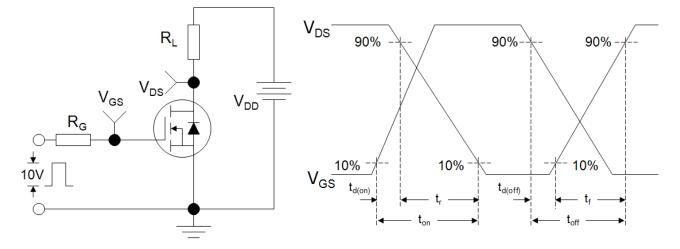
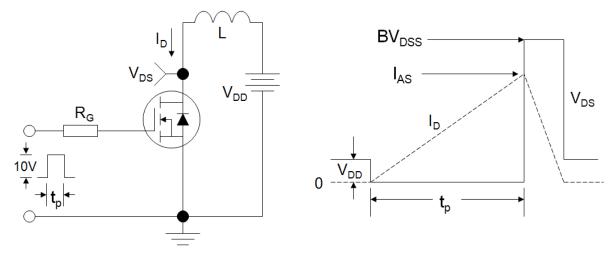
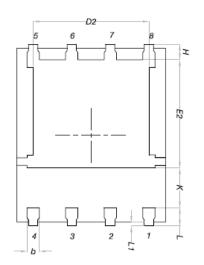
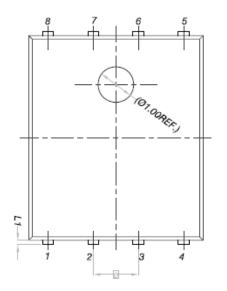
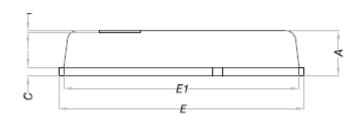


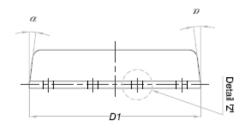
Figure C: Unclamped Inductive Switching (UIS) Test Circuit and Waveforms









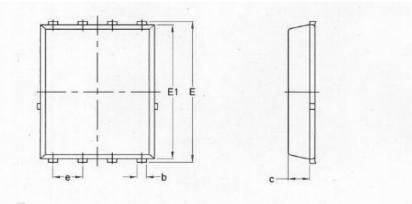


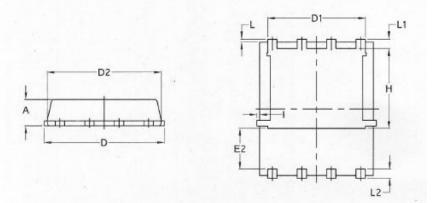
	N	1ILLIMET	ERS		N	IILLIMET	ERS
DIM.	MIN.	NOM.	MAX.	DIM.	MIN.	NOM.	MAX.
А	0.90	1.00	1.10	E	5.90	6.00	6.10
A1	0	-	0.05	E1	5.70	5.75	5.80
b	0.33	0.41	0.51	E2	3.38	3.58	3.78
С	0.20	0.25	0.30	е		1.27 BSC	
D1	4.80	4.90	5.00	Н	0.41	0.51	0.61
D2	3.61	3.81	3.96	К	1.10	-	-
				L	0.51	0.61	0.71
				L1	0.06	0.13	0.20
				α	0°	-	12°

DFN5x6(M)









1	S Y	COMMON					
	M B O	M	M	INCH			
	ĉ	MIN.	MAX.	MIN.	MAX.		
	А	1.03	1.17	0.0406	0.0461		
	b	0.34	0.48	0.0134	0.0189		
	с	0.824	0.970	0.0324	0.0382		
7	D	4.80	5.40	0.1890	0.2126		
	D1	4.11	4.31	0.1618	0.1697		
1	D2	4.80	5.00	0.1890	0.1969		
	E	5.95	6.15	0.2343	0.2421		
	E1	5.65	5.85	0.2224	0.2303		
	E2	1.60	-	0.0630			
e		1.27 BSC		0.05 BSC			
1	L	0.05	0.25	0.0020	0.0098		
	L1	0.38	0.50	0.0150	0.0197		
	L2	0.38	0.50	0.0150	0.0197		
4	н	3.30	3.50	0.1299	0.1378		
	1	_	0.18	_	0.0070		



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