
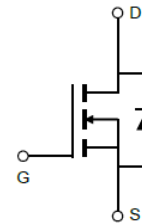
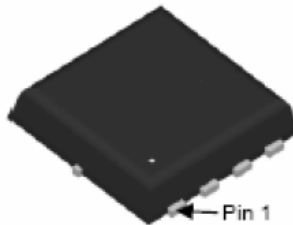


**20V N-Channel Trench MOSFET(Preliminary)**

<p>General Description</p> <ul style="list-style-type: none"> ● Trench Power technology ● Low $R_{DS(ON)}$ ● Low Gate Charge ● Optimized for fast-switching applications <p>Applications</p> <ul style="list-style-type: none"> ● Synchronous Rectification in DC/DC and AC/DC Converters ● Isolated DC/DC Converters in Telecom and Industrial 	<p>Product Summary</p> <table> <tr> <td>V_{DS}</td> <td>20V</td> </tr> <tr> <td>I_D (at $V_{GS}=10V$)</td> <td>120A</td> </tr> <tr> <td>$R_{DS(ON)}$ (at $V_{GS}=10V$)</td> <td>< 3.4mΩ</td> </tr> <tr> <td>$R_{DS(ON)}$ (at $V_{GS}=4.5V$)</td> <td>< 3.8mΩ</td> </tr> <tr> <td>$R_{DS(ON)}$ (at $V_{GS}=2.5V$)</td> <td>< 5.1mΩ</td> </tr> </table> <p>100% UIS Tested</p> 	V_{DS}	20V	I_D (at $V_{GS}=10V$)	120A	$R_{DS(ON)}$ (at $V_{GS}=10V$)	< 3.4m Ω	$R_{DS(ON)}$ (at $V_{GS}=4.5V$)	< 3.8m Ω	$R_{DS(ON)}$ (at $V_{GS}=2.5V$)	< 5.1m Ω
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DFN3.3x3.3



Part Number	Package Type	Form	Marking
TTG120N02GT	DFN3.3x3.3	Tape&Reel	120N02GT

Absolute Maximum Ratings ($T_A = 25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	Maximum	Units
Drain-Source Voltage	V_{DS}	20	V
Gate-Source Voltage	V_{GS}	± 20	V
Continuous Drain Current ^B	I_D	$T_C = 25^\circ\text{C}$	17
		$T_C = 100^\circ\text{C}$	17
Pulsed Drain Current ^A	I_{DM}	360	A
Avalanche Current ^A	I_{AS}	26	A
Single Pulse Avalanche Energy $L = 0.3\text{mH}$ ^A	E_{AS}	101	mJ
Power Dissipation ^C	P_D	$T_C = 25^\circ\text{C}$	108
		$T_C = 100^\circ\text{C}$	54.3
Junction and Storage Temperature Range	T_J, T_{STG}	-55 to 175	$^\circ\text{C}$

Thermal Characteristics

Parameter	Symbol	Maximum	Units
Maximum Junction-to-Case	$R_{\theta JC}$	1.38	$^\circ\text{C/W}$
Maximum Junction-to-Ambient	$R_{\theta JA}$	100	



Electrical Characteristics($T_J = 25^\circ\text{C}$ unless otherwise noted)							
Symbol	Parameter	Conditions	Value			Units	
			Min	Typ	Max		
STATIC PARAMETERS							
BV_{DSS}	Drain-Source Breakdown Voltage	$I_D = 250\mu\text{A}, V_{GS} = 0\text{V}$	20			V	
I_{DSS}	Zero Gate Voltage Drain Current	$V_{DS} = 20\text{V}, V_{GS} = 0\text{V}$	$T_J = 25^\circ\text{C}$		1	μA	
			$T_J = 100^\circ\text{C}$		25		
I_{GSS}	Gate-Body Leakage Current	$V_{DS} = 0\text{V}, V_{GS} = \pm 20\text{V}$			± 100	nA	
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_D = 250\mu\text{A}$	0.5	0.7	1.2	V	
$R_{DS(on)}$	Static Drain-Source On-Resistance		$V_{GS} = 10\text{V}, I_D = 30\text{A}$		2.6	3.4	$\text{m}\Omega$
			$V_{GS} = 4.5\text{V}, I_D = 30\text{A}$		2.9	3.8	$\text{m}\Omega$
			$V_{GS} = 2.5\text{V}, I_D = 30\text{A}$		3.9	5.1	$\text{m}\Omega$
g_{FS}	Forward Transconductance	$V_{DS} = 10\text{V}, I_D = 20\text{A}$		15		S	
V_{SD}	Diode Forward Voltage	$I_S = 50\text{A}, V_{GS} = 0\text{V}$			1	V	
I_S	Maximum Body-Diode Continuous Current ^B				17	A	
DYNAMIC PARAMETERS							
C_{iss}	Input Capacitance	$V_{GS} = 0\text{V}, V_{DS} = 10\text{V}, f = 1\text{MHz}$		4066		pF	
C_{oss}	Output Capacitance			1107			
C_{rss}	Reverse Transfer Capacitance			655			
SWITCHING PARAMETERS							
$Q_g(10\text{V})$	Total Gate Charge	$V_{GS} = 10\text{V}, V_{DS} = 10\text{V}, I_D = 50\text{A}$		118		nC	
Q_{gs}	Gate Source Charge			7			
Q_{gd}	Gate Drain Charge			22			
$t_{D(on)}$	Turn-On Delay Time	$V_{GS} = 10\text{V}, V_{DS} = 10\text{V}, I_D = 50\text{A}, R_G = 3\Omega$		12		ns	
t_r	Turn-On Rise Time			11			
$T_{D(off)}$	Turn-Off Delay Time			39			
t_f	Turn-Off Fall Time			18			
t_{rr}	Body Diode Reverse Recovery Time	$I_F = 50\text{A}, di/dt = 100\text{A}/\mu\text{s}$		19		ns	
Q_{rr}	Body Diode Reverse Recovery Charge			17		nC	

A. Single pulse width limited by maximum junction temperature.

B. The maximum current rating is package limited.

C. The power dissipation P_D is based on $T_{J(MAX)} = 175^\circ\text{C}$, using junction-to-case thermal resistance, and is more useful in setting the upper dissipation limit for cases where additional heatsinking is used.



TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS

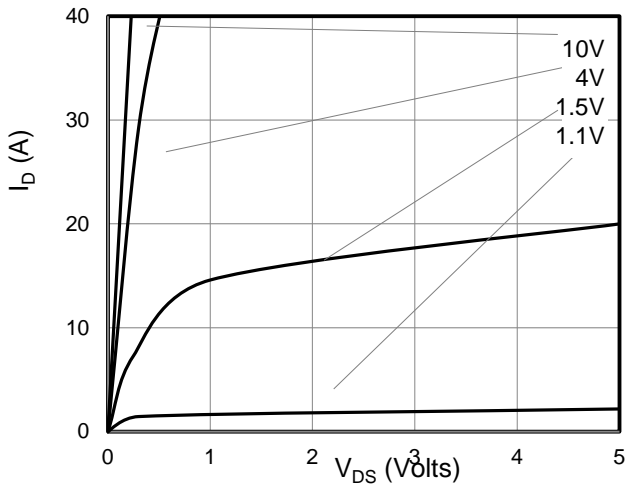


Figure 1: On-Region Characteristics

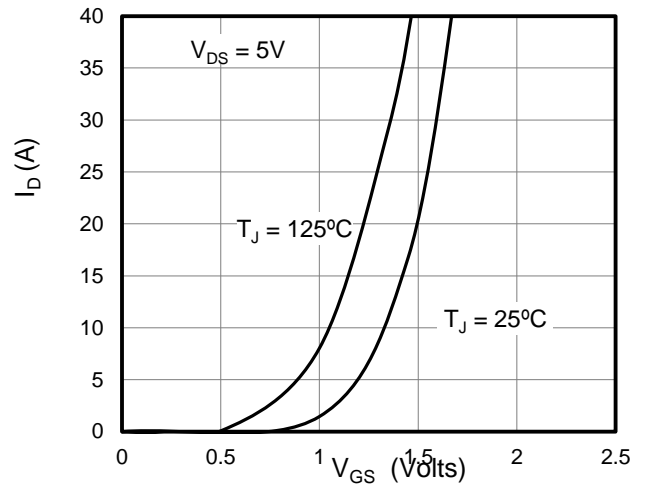


Figure 2: Transfer Characteristics

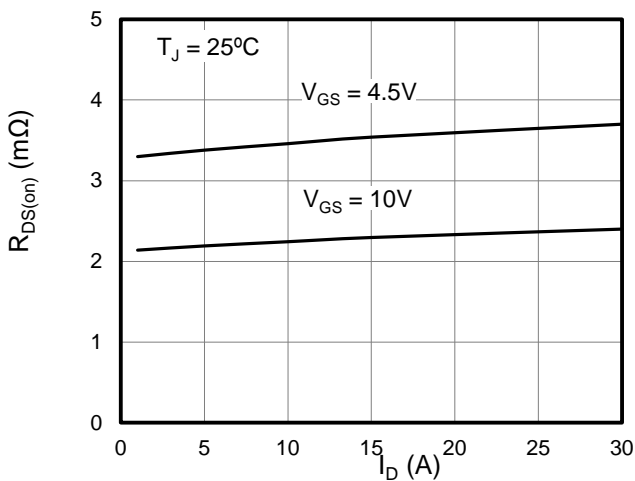


Figure 3: On-Resistance vs. Drain Current

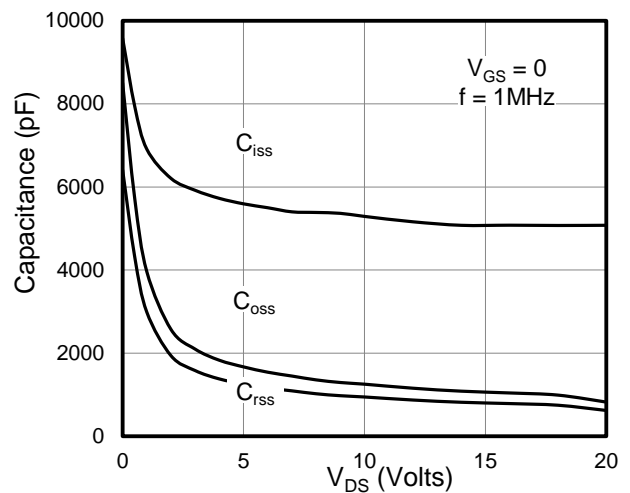


Figure 4: Capacitance Characteristics

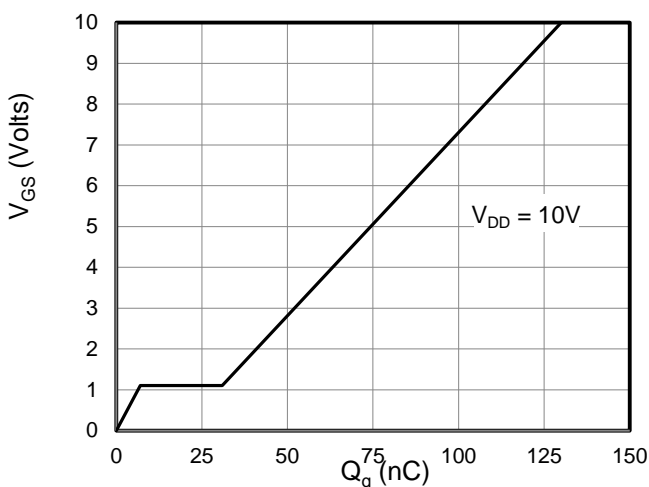


Figure 5: Gate Charge Characteristics

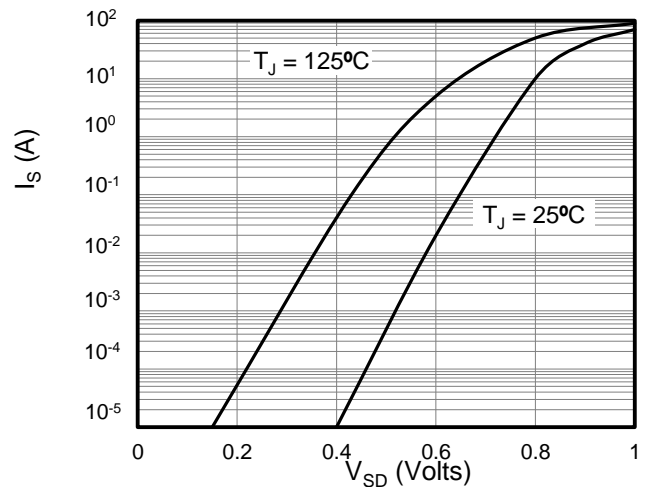


Figure 6: Body Diode Forward Voltage



TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS

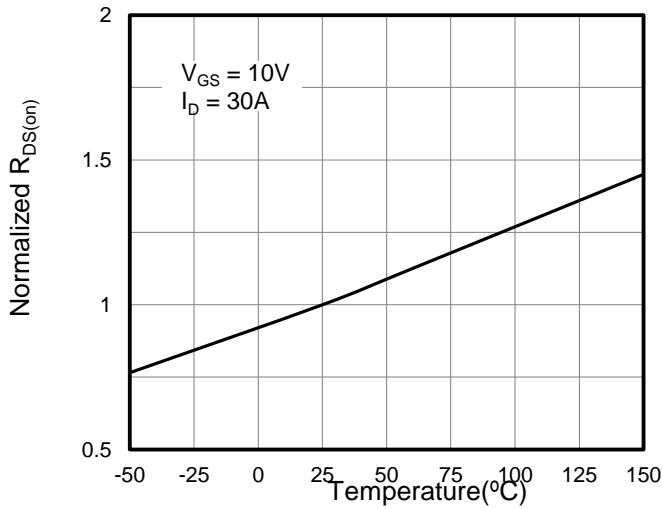


Figure 7: On-Resistance vs. Junction Temperature

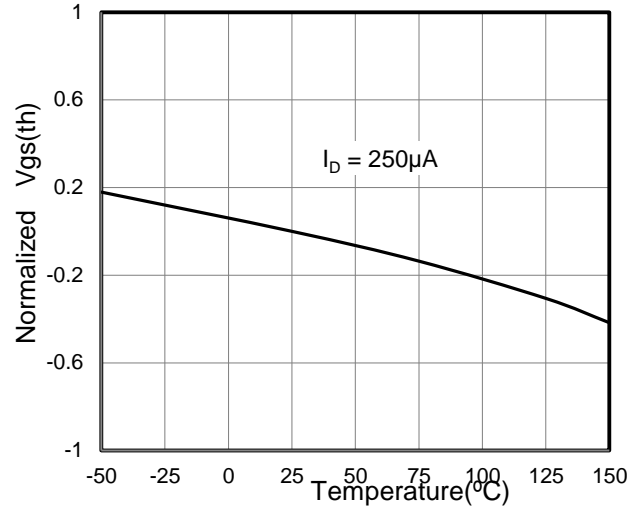


Figure 8: $V_{GS(th)}$ vs. Junction Temperature

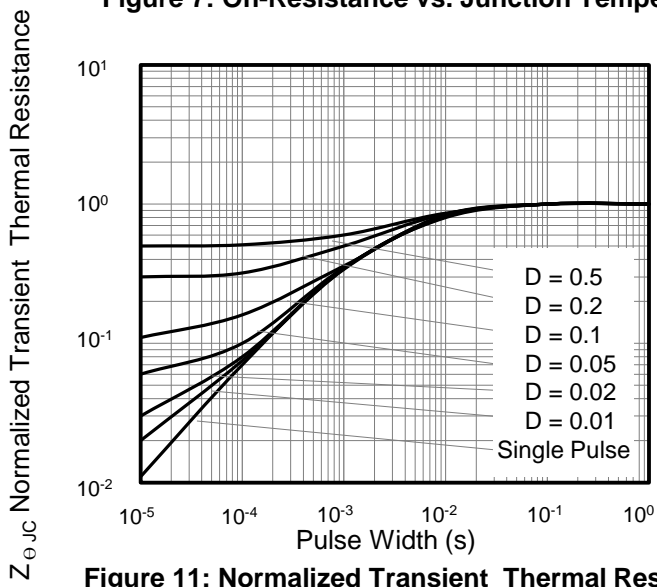


Figure 11: Normalized Transient Thermal Resistance

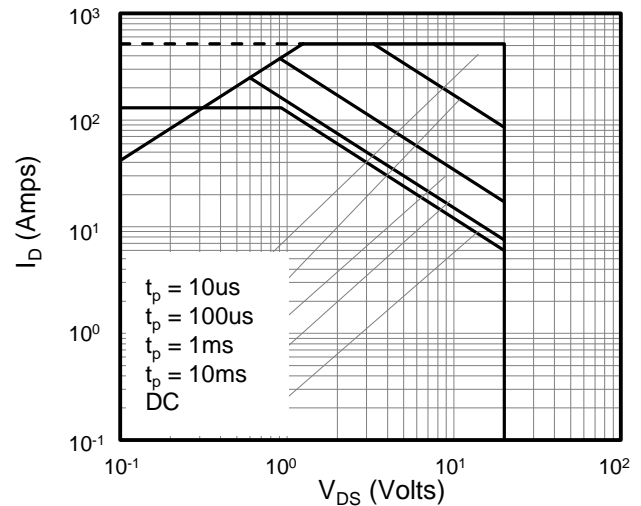


Figure 12: Safe Operating Area

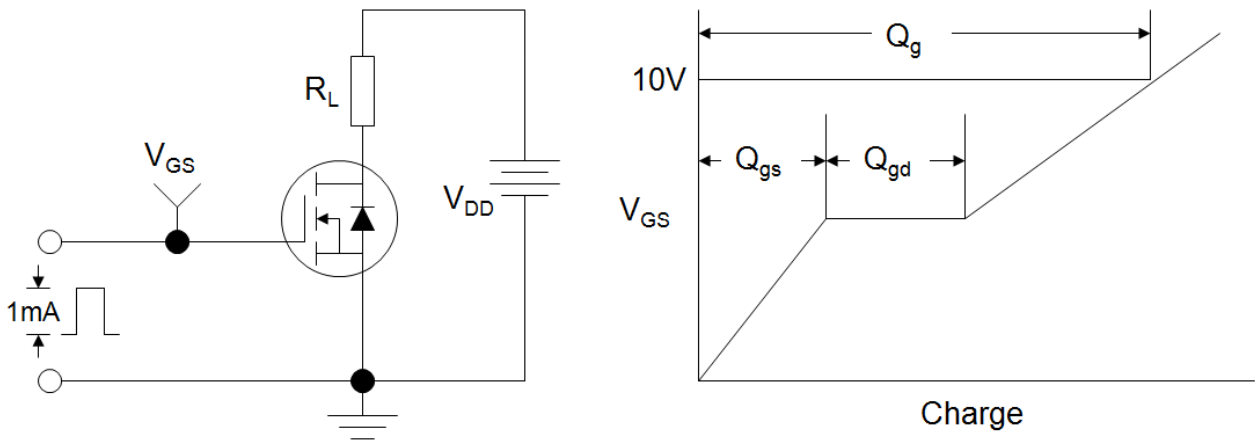


Figure A: Gate Charge Test Circuit and Waveforms

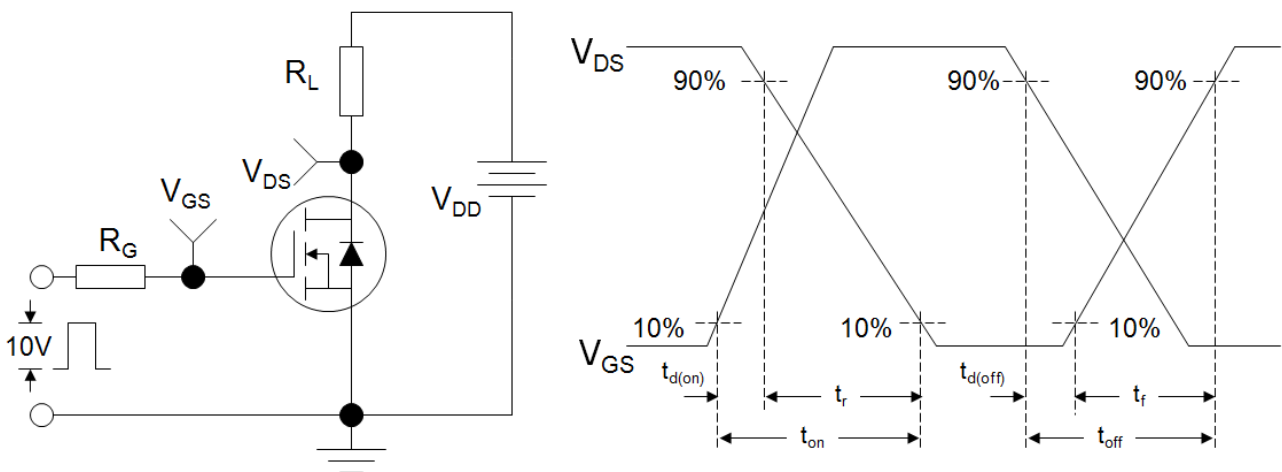


Figure B: Resistive Switching Test Circuit and Waveforms

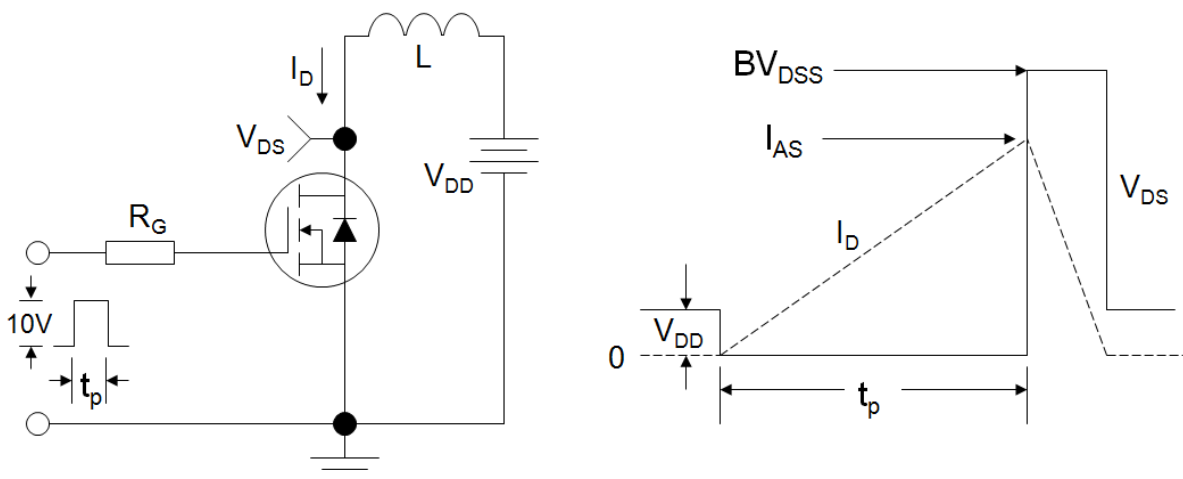
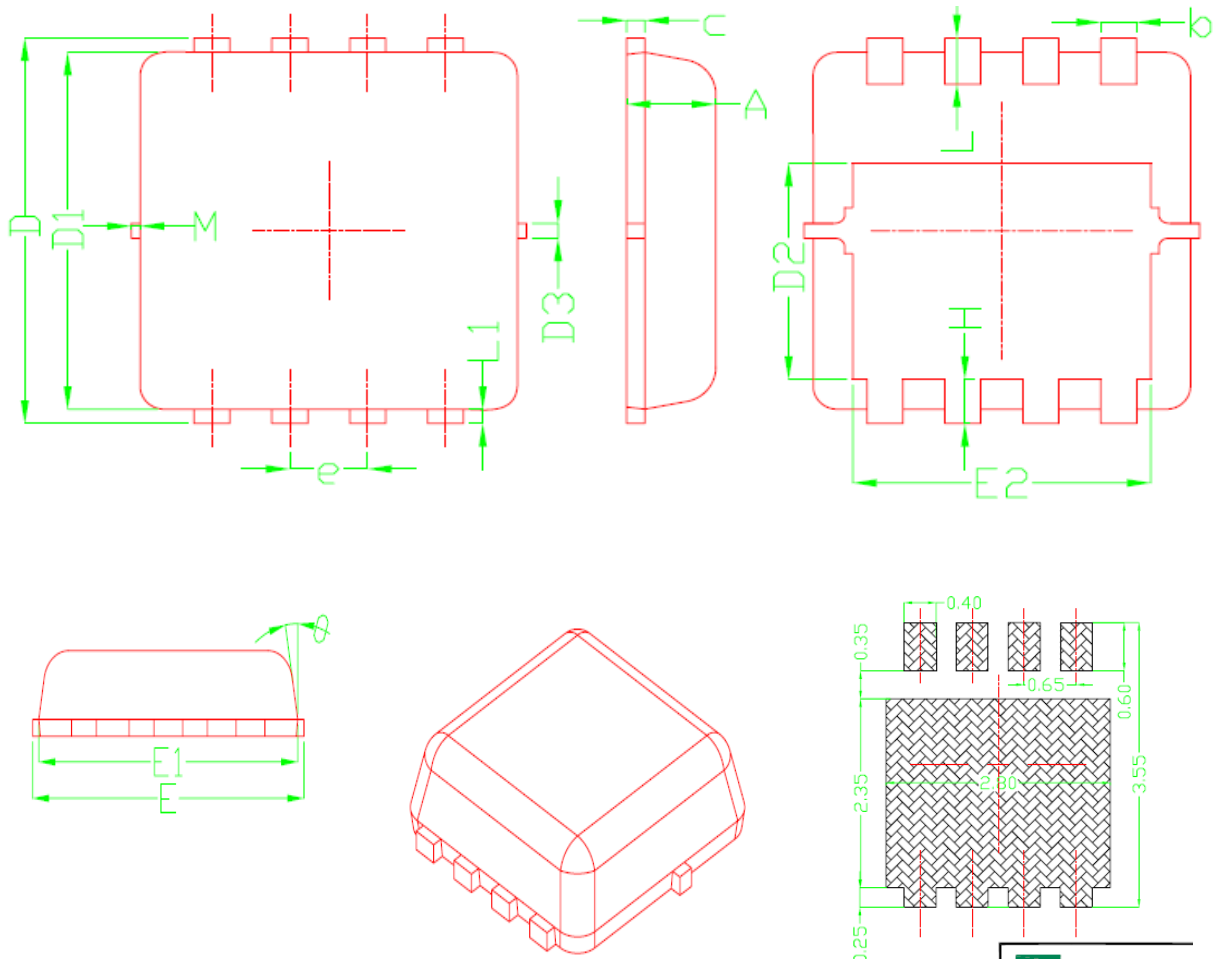


Figure C: Unclamped Inductive Switching (UIS) Test Circuit and Waveforms



DFN 3.3x3.3



SYMBOL	DIMENSIONAL REOMTS		
	MIN	NOM	MAX
A	0.70	0.75	0.80
b	0.25	0.30	0.35
c	0.10	0.15	0.25
D	3.25	3.35	3.45
D1	3.00	3.10	3.20
D2	1.78	1.88	1.98
D3	---	0.13	---
E	3.20	3.30	3.40
E1	3.00	3.15	3.20
E2	2.39	2.49	2.59
e	0.65BSC		
H	0.30	0.39	0.50
L	0.30	0.40	0.50
L1	---	0.13	---
θ	---	10°	12°
M	*	*	0.15
* Not specified			



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