

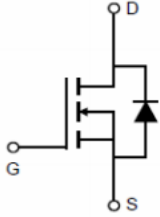


**30V N-Channel Trench MOSFET(Preliminary)**

<b>Features</b> <ul style="list-style-type: none"> <li>● Trench Power Technology</li> <li>● Low <math>R_{DS(ON)}</math></li> <li>● Low Gate Charge</li> <li>● Optimized for Fast-switching Applications</li> </ul> <b>Applications</b> <ul style="list-style-type: none"> <li>● Synchronous Rectification in DC/DC and AC/DC Converters</li> <li>● Isolated DC/DC Converters in Telecom and Industrial</li> </ul>		<b>Product Summary</b> <table> <tr> <td><math>V_{DS}</math></td> <td>30V</td> </tr> <tr> <td><math>R_{DS(ON)}</math> (at <math>V_{GS}=10V</math>)</td> <td>&lt; 5m<math>\Omega</math></td> </tr> <tr> <td><math>R_{DS(ON)}</math> (at <math>V_{GS}=4.5V</math>)</td> <td>&lt; 7m<math>\Omega</math></td> </tr> <tr> <td><math>I_D</math> (at <math>V_{GS}=10V</math>)</td> <td>90A</td> </tr> <tr> <td>100% UIS Tested</td> <td></td> </tr> </table> 		$V_{DS}$	30V	$R_{DS(ON)}$ (at $V_{GS}=10V$ )	< 5m $\Omega$	$R_{DS(ON)}$ (at $V_{GS}=4.5V$ )	< 7m $\Omega$	$I_D$ (at $V_{GS}=10V$ )	90A	100% UIS Tested	
$V_{DS}$	30V												
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<b>TO-252</b>  G D S													
<b>Device</b>	<b>Package</b>	<b>Marking</b>											
TTD90N03AT	TO-252	90N03AT											

**Absolute Maximum Ratings**  $T_C = 25^\circ\text{C}$ , unless otherwise noted

Parameter	Symbol	Value	Unit
Drain-Source Voltage ( $V_{GS} = 0V$ )	$V_{DSS}$	30	V
Continuous Drain Current <sup>B</sup>	$I_D$	$T_C = 25^\circ\text{C}$	51
		$T_C = 100^\circ\text{C}$	51
Pulsed Drain Current <sup>A</sup>	$I_{DM}$	270	A
Gate-Source Voltage	$V_{GS}$	$\pm 20$	V
Single Pulse Avalanche Energy $L = 0.3\text{mH}$ <sup>A</sup>	$E_{AS}$	72	mJ
Avalanche Current <sup>A</sup>	$I_{AS}$	22	A
Power Dissipation <sup>C</sup>	$P_D$	$T_C = 25^\circ\text{C}$	108
		$T_C = 100^\circ\text{C}$	82
Operating Junction and Storage Temperature Range	$T_J, T_{SGT}$	-55~+175	$^\circ\text{C}$

**Thermal Resistance**

Parameter	Symbol	Value	Unit
Thermal Resistance, Junction-to-Case	$R_{thJC}$	1.45	$^\circ\text{C}/\text{W}$
Thermal Resistance, Junction-to-Ambient	$R_{thJA}$	100	



Specifications $T_J = 25^\circ\text{C}$ , unless otherwise noted						
Parameter	Symbol	Test Conditions	Value			Unit
			Min.	Typ.	Max.	
<b>Static</b>						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS} = 0V, I_D = 250\mu A$	30	--	--	V
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS} = 30V, V_{GS} = 0V, T_J = 25^\circ\text{C}$	--	--	1	$\mu A$
		$V_{DS} = 30V, V_{GS} = 0V, T_J = 100^\circ\text{C}$	--	--	25	
Gate-Source Leakage	$I_{GSS}$	$V_{GS} = \pm 20V$	--	--	$\pm 100$	nA
Gate-Source Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250\mu A$	1.0	1.7	2.4	V
Static Drain-Source On-Resistance	$R_{DS(on)}$	$V_{GS} = 10V, I_D = 30A$	--	3.6	5.0	$m\Omega$
		$V_{GS} = 4.5V, I_D = 30A$	--	5	7.0	$m\Omega$
Forward Transconductance	$g_{fs}$	$V_{DS} = 10V, I_D = 20A$	17.3	--	--	S
<b>Dynamic</b>						
Input Capacitance	$C_{iss}$	$V_{GS} = 0V,$ $V_{DS} = 15V,$ $f = 1.0\text{MHz}$	--	1608	--	$pF$
Output Capacitance	$C_{oss}$		--	513	--	
Reverse Transfer Capacitance	$C_{rss}$		--	297	--	
Total Gate Charge	$Q_g$	$V_{DD} = 15V, I_D = 50A,$ $V_{GS} = 10V$	--	62	--	$nC$
Gate-Source Charge	$Q_{gs}$		--	7	--	
Gate-Drain Charge	$Q_{gd}$		--	13	--	
Turn-on Delay Time	$t_{d(on)}$	$V_{DD} = 15V, I_D = 50A,$ $R_G = 3\Omega$	--	13	--	$ns$
Turn-on Rise Time	$t_r$		--	17	--	
Turn-off Delay Time	$t_{d(off)}$		--	42	--	
Turn-off Fall Time	$t_f$		--	13	--	
<b>Drain-Source Body Diode Characteristics</b>						
Continuous Body Diode Current <sup>B</sup>	$I_S$	$T_C = 25^\circ\text{C}$	--	--	46	A
Pulsed Diode Forward Current <sup>A</sup>	$I_{SM}$		--	--	270	
Body Diode Voltage	$V_{SD}$	$T_J = 25^\circ\text{C}, I_{SD} = 30A, V_{GS} = 0V$	--	--	1.2	V
Reverse Recovery Time	$t_{rr}$	$I_F = 30A,$ $di_F/dt = 100A/\mu s$	--	40	--	ns
Reverse Recovery Charge	$Q_{rr}$		--	88	--	nC

**Notes**

1. Repetitive Rating: Pulse Width limited by maximum junction temperature
2.  $V_{DD} = 30V, R_G = 25\Omega, \text{Starting } T_J = 25^\circ\text{C}$
3. Pulse Test: Pulse Width  $\leq 300\mu s, \text{Duty Cycle } \leq 1\%$



Typical Characteristics  $T_J = 25^\circ\text{C}$ , unless otherwise noted

Figure 1. Output Characteristics

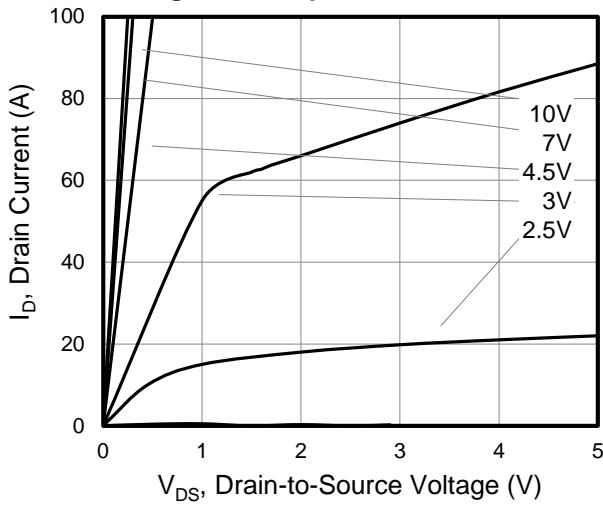


Figure 2. Transfer Characteristics

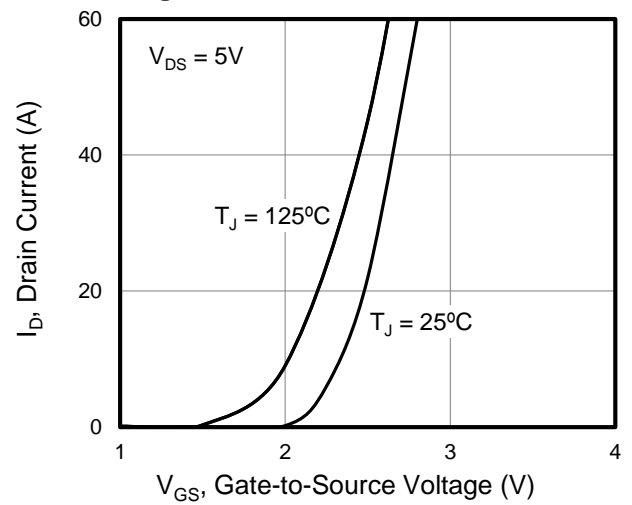


Figure 3. On-Resistance vs. Drain Current

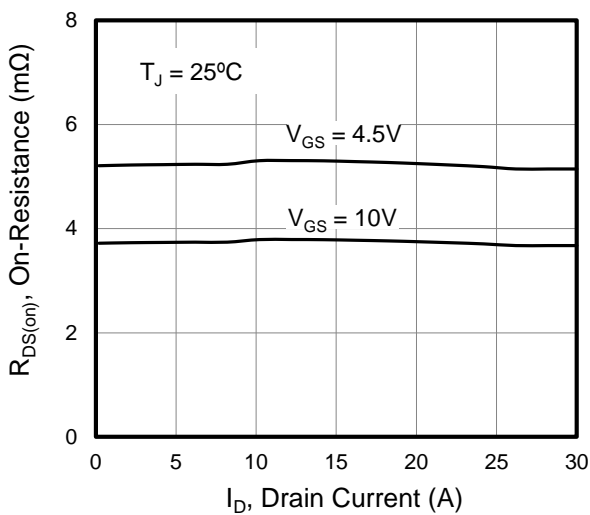


Figure 4. Capacitance

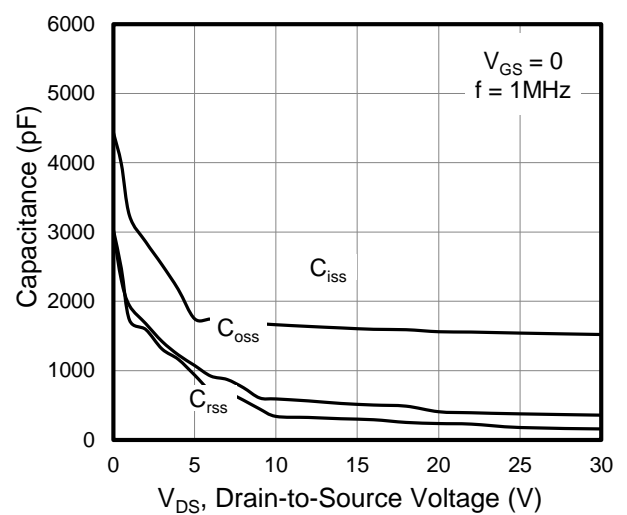


Figure 5. Gate Charge

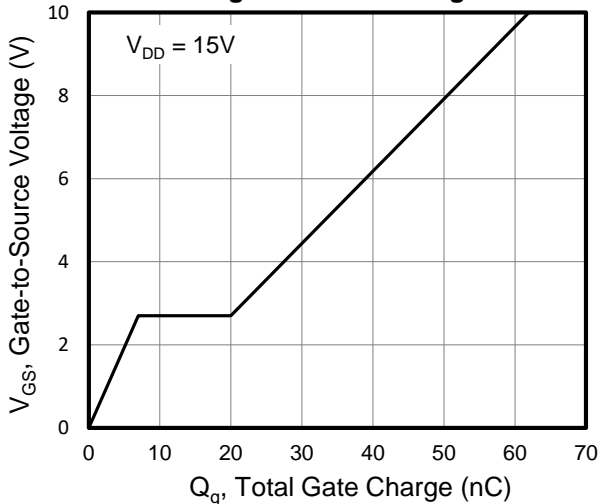
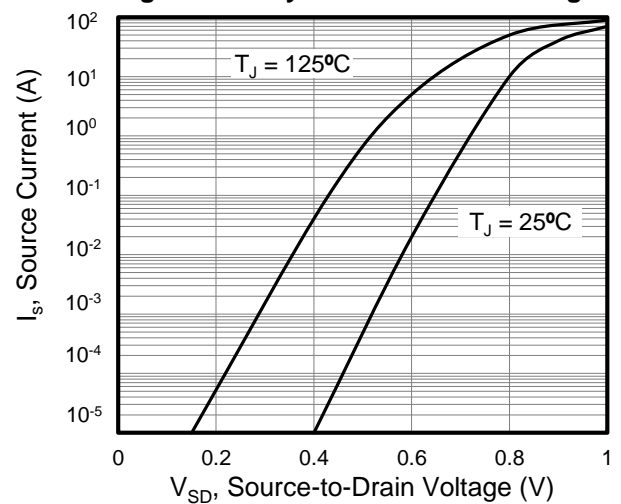


Figure 6. Body Diode Forward Voltage





Typical Characteristics  $T_J = 25^\circ\text{C}$ , unless otherwise noted

Figure 7. On-Resistance vs. Junction Temperature

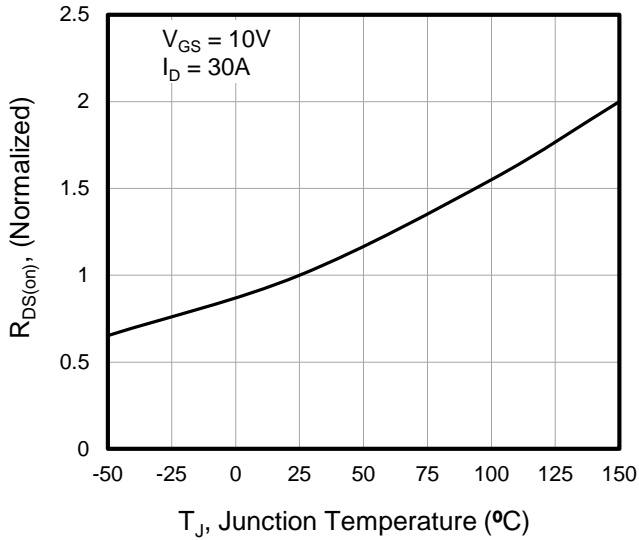


Figure 8. Threshold Voltage vs. Junction Temperature

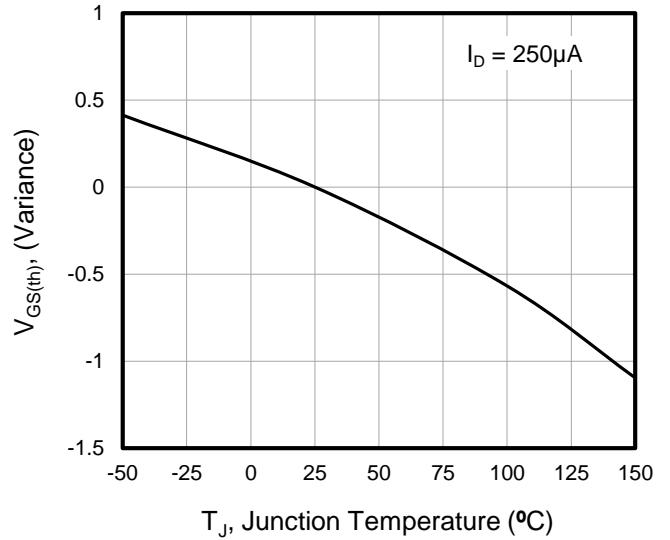


Figure 9. Transient Thermal Impedance

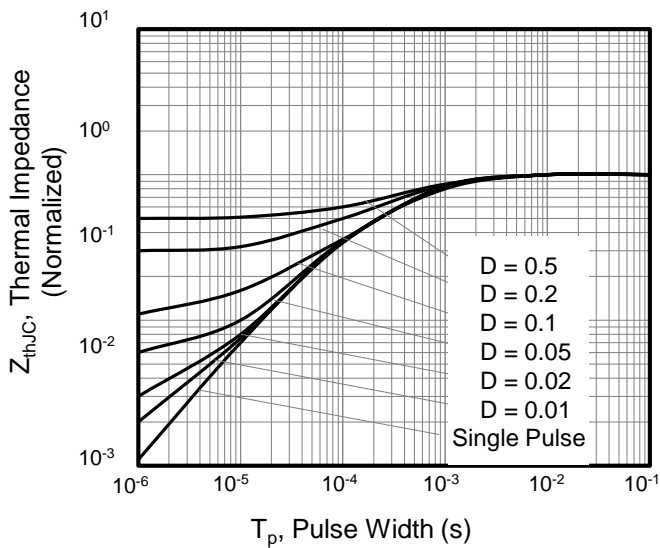


Figure 10. Safe operation area

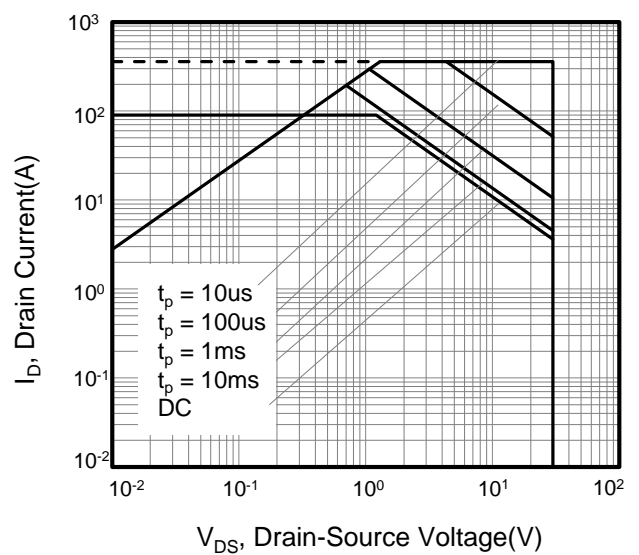




Figure A: Gate Charge Test Circuit and Waveform

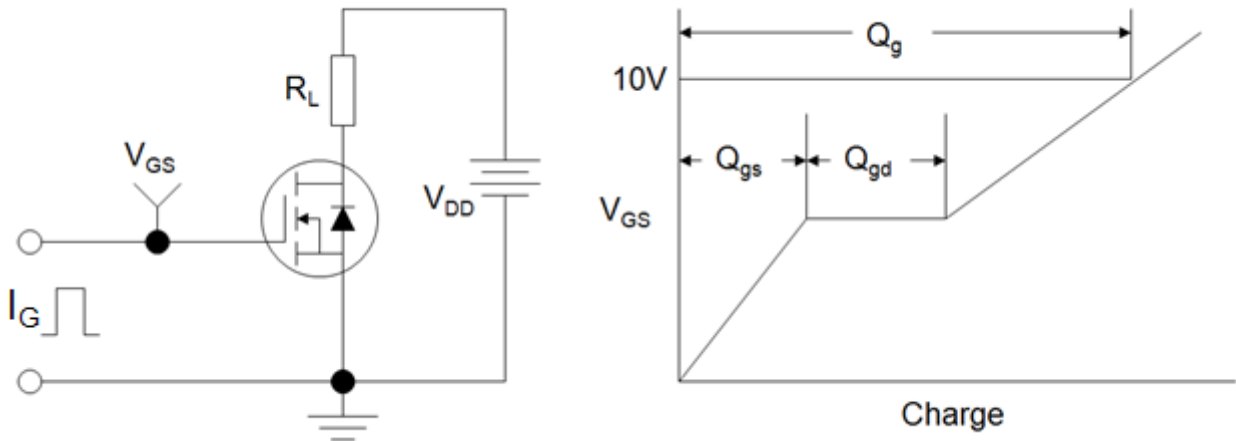


Figure B: Resistive Switching Test Circuit and Waveform

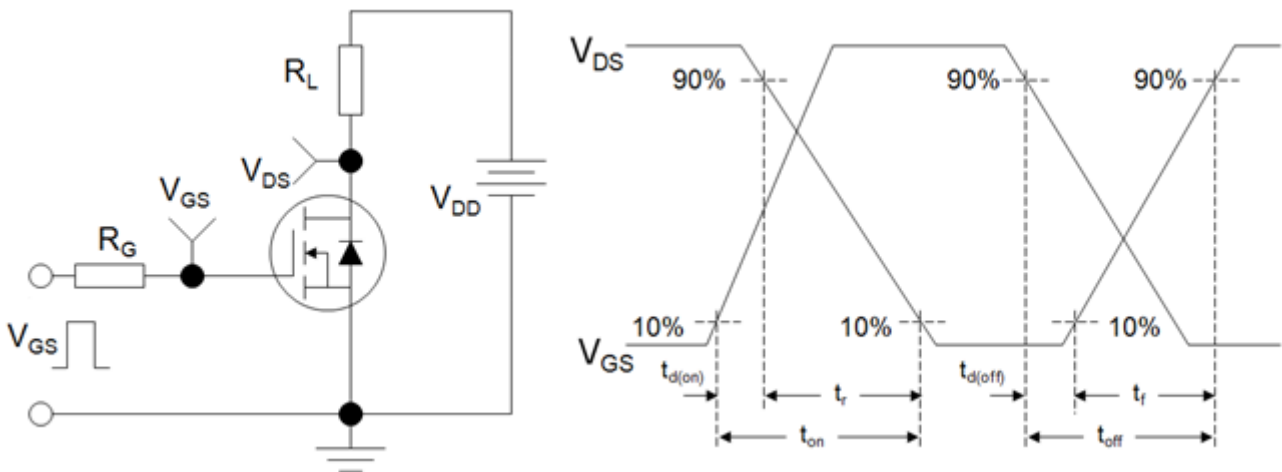
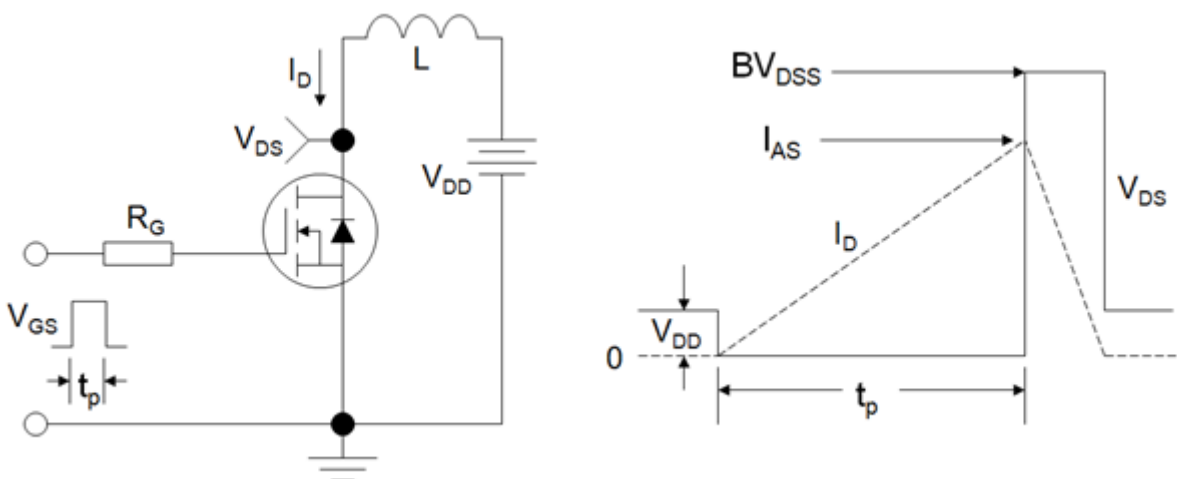
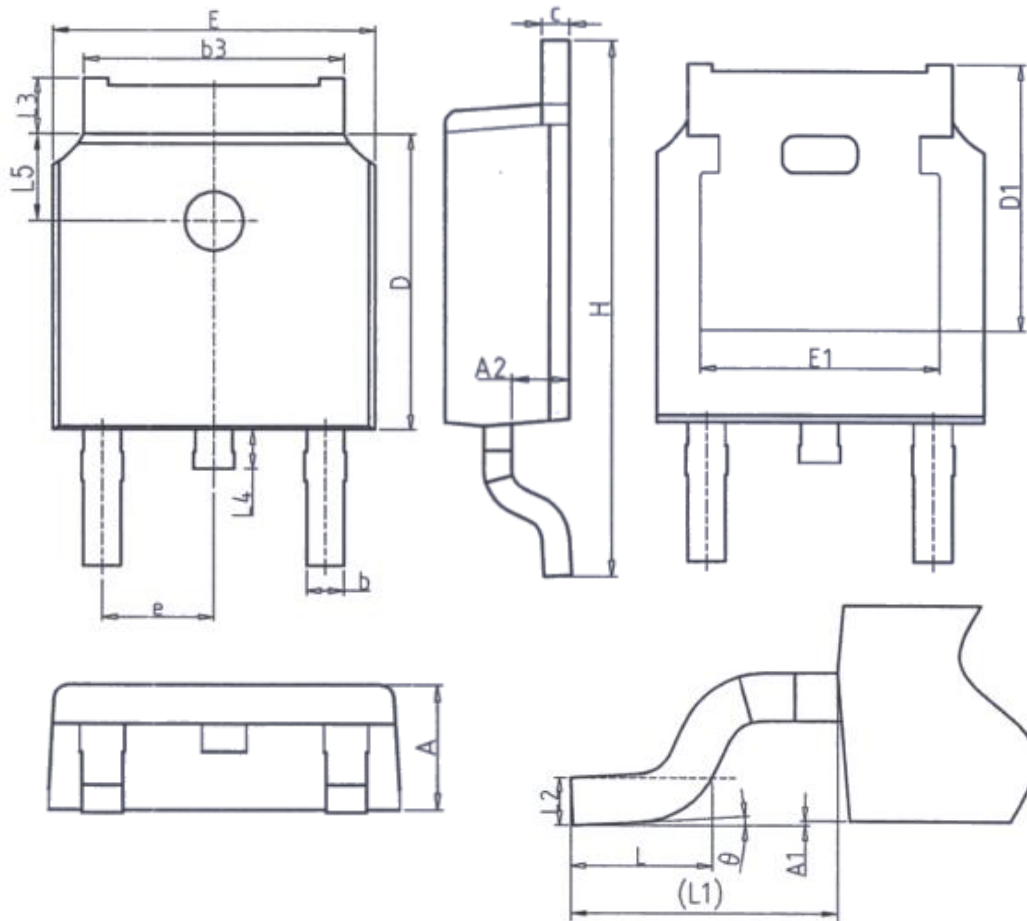


Figure C: Unclamped Inductive Switching Test Circuit and Waveform





### TO-252(H)



Unit: mm		
Symbol	Min.	Max.
A	2.20	2.40
A1	0.00	0.20
A2	0.97	1.17
b	0.68	0.90
b3	5.20	5.50
c	0.43	0.63
D	5.98	6.22
D1	5.30REF	
E	6.40	6.80
E1	4.63	-

Unit: mm		
Symbol	Min.	Max.
e	2.286BSC	
H	9.40	10.50
L	1.38	1.75
L1	2.90REF	
L2	0.51BSC	
L3	0.88	1.28
L4	-	1.00
L5	1.65	1.95
θ	0°	8°



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