
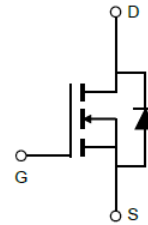


**30V N-Channel Trench MOSFET(Preliminary)**

<p><b>General Description</b></p> <ul style="list-style-type: none"> <li>● Trench Power technology</li> <li>● Low Capacitance</li> <li>● Ultra low Gate Charge</li> <li>● Optimized for fast-switching applications</li> </ul> <p><b>Applications</b></p> <ul style="list-style-type: none"> <li>● Synchronous Rectification in DC/DC and AC/DC Converters</li> <li>● Isolated DC/DC Converters in Telecom and Industrial</li> </ul>	<p><b>Product Summary</b></p> <table> <tr> <td><math>V_{DS}</math></td> <td>30V</td> </tr> <tr> <td><math>I_D</math> (at <math>V_{GS}=10V</math>)</td> <td>50A</td> </tr> <tr> <td><math>R_{DS(ON)}</math> (at <math>V_{GS}=10V</math>)</td> <td>&lt; 10m<math>\Omega</math></td> </tr> </table> <p>100% UIS Tested 100% DVDS Tested</p> 	$V_{DS}$	30V	$I_D$ (at $V_{GS}=10V$ )	50A	$R_{DS(ON)}$ (at $V_{GS}=10V$ )	< 10m $\Omega$
$V_{DS}$	30V						
$I_D$ (at $V_{GS}=10V$ )	50A						
$R_{DS(ON)}$ (at $V_{GS}=10V$ )	< 10m $\Omega$						

TO-252



Part Number	Package Type	Form	Marking
TTD50N03Q	TO-252	Tape & Reel	TTD50N03Q

**Absolute Maximum Ratings ( $T_A = 25^\circ\text{C}$  unless otherwise noted)**

Parameter	Symbol	Maximum	Units
Drain-Source Voltage	$V_{DS}$	30	V
Gate-Source Voltage	$V_{GS}$	$\pm 20$	V
Continuous Drain Current <sup>B</sup>	$I_D$	$T_C = 25^\circ\text{C}$	46
		$T_C = 100^\circ\text{C}$	37
Pulsed Drain Current <sup>A</sup>	$I_{DM}$	150	A
Avalanche Current <sup>A</sup>	$I_{AS}$	22	A
Single Pulse Avalanche Energy	$E_{AS}$	72.6	mJ
Power Dissipation <sup>C</sup>	$P_D$	$T_C = 25^\circ\text{C}$	46.8
		$T_C = 100^\circ\text{C}$	23.4
Junction and Storage Temperature Range	$T_J, T_{STG}$	-55 to 175	$^\circ\text{C}$

**Thermal Characteristics**

Parameter	Symbol	Maximum	Units
Maximum Junction-to-Case	$R_{\theta JC}$	3.2	$^\circ\text{C/W}$
Maximum Junction-to-Ambient	$R_{\theta JA}$	100	



Electrical Characteristics( $T_J = 25^\circ\text{C}$ unless otherwise noted)						
Symbol	Parameter	Conditions	Value			Units
			Min	Typ	Max	
<b>STATIC PARAMETERS</b>						
$BV_{DSS}$	Drain-Source Breakdown Voltage	$I_D = 250\mu\text{A}, V_{GS} = 0\text{V}$	30			V
$I_{DSS}$	Zero Gate Voltage Drain Current	$V_{DS} = 30\text{V}, V_{GS} = 0\text{V}$	$T_J = 25^\circ\text{C}$		1	$\mu\text{A}$
			$T_J = 125^\circ\text{C}$		100	
$I_{GSS}$	Gate-Body Leakage Current	$V_{DS} = 0\text{V}, V_{GS} = \pm 20\text{V}$			$\pm 100$	nA
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_D = 250\mu\text{A}$	3	4	5	V
$R_{DS(ON)}$	Static Drain-Source On-Resistance	$V_{GS} = 10\text{V}, I_D = 25\text{A}$		8.5	10	$\text{m}\Omega$
$g_{FS}$	Forward Transconductance	$V_{DS} = 5\text{V}, I_D = 20\text{A}$		13		S
$V_{SD}$	Diode Forward Voltage	$I_S = 25\text{A}, V_{GS} = 0\text{V}$			1	V
$I_S$	Maximum Body-Diode Continuous Current <sup>B</sup>				46	A
<b>DYNAMIC PARAMETERS</b>						
$C_{iss}$	Input Capacitance	$V_{GS} = 0\text{V}, V_{DS} = 15\text{V}, f = 1\text{MHz}$		722		$\text{pF}$
$C_{oss}$	Output Capacitance			223		
$C_{rss}$	Reverse Transfer Capacitance			80		
$R_g$	Gate Resistance	$f = 1\text{MHz}$		7.5		$\Omega$
<b>SWITCHING PARAMETERS</b>						
$Q_g$	Total Gate Charge	$V_{GS} = 10\text{V}, V_{DS} = 15\text{V}, I_D = 20\text{A}$		11.2		nC
$Q_{gs}$	Gate Source Charge			5.6		
$Q_{gd}$	Gate Drain Charge			3.7		
$t_{D(on)}$	Turn-On Delay Time	$V_{GS} = 10\text{V}, V_{DS} = 15\text{V}, I_D = 20\text{A}, R_G = 1.6\Omega$		36.1		ns
$t_r$	Turn-On Rise Time			4.1		
$T_{D(off)}$	Turn-Off Delay Time			37.1		
$t_f$	Turn-Off Fall Time			4.5		
$t_{rr}$	Body Diode Reverse Recovery Time	$I_F = 20\text{A}, di/dt = 100\text{A}/\mu\text{s}$		27		ns
$Q_{rr}$	Body Diode Reverse Recovery Charge			7.2		nC

A. Single pulse width limited by maximum junction temperature.

B. The maximum current rating is package limited.

C. The power dissipation  $P_D$  is based on  $T_{J(MAX)} = 175^\circ\text{C}$ , using junction-to-case thermal resistance, and is more useful in setting the upper dissipation limit for cases where additional heatsinking is used.



TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS

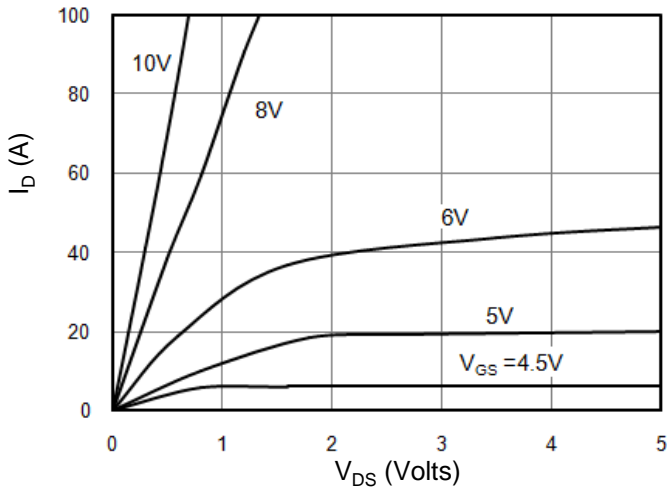


Figure 1: On-Region Characteristics

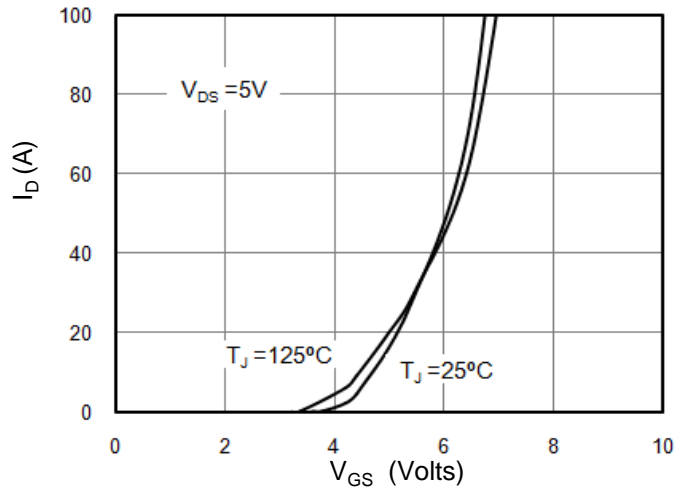


Figure 2: Transfer Characteristics

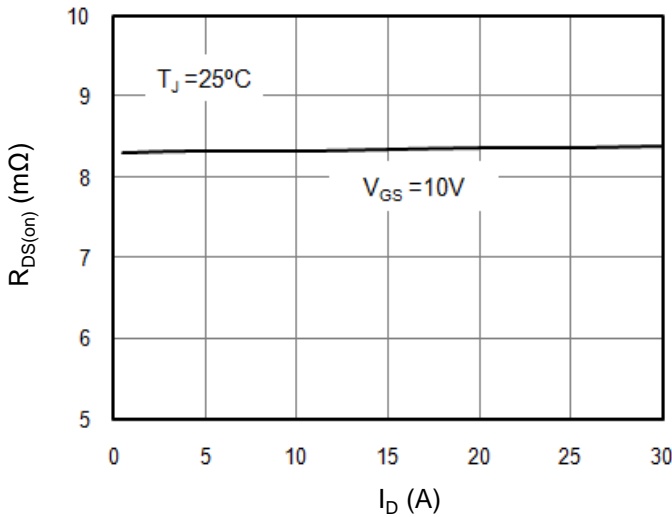


Figure 3: On-Resistance vs. Drain Current

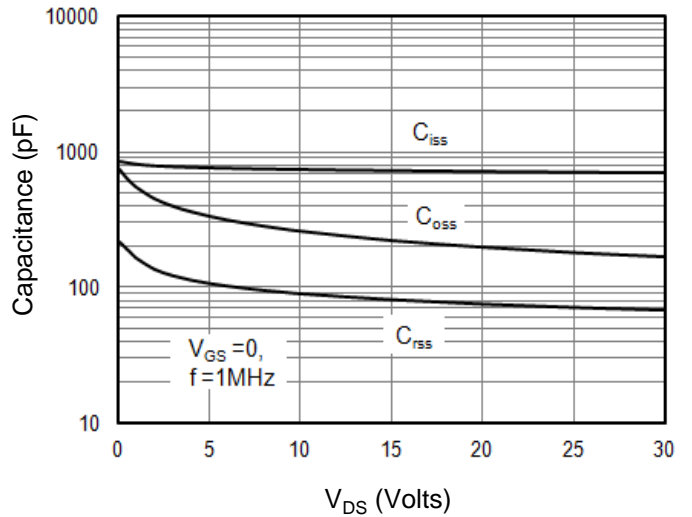


Figure 4: Capacitance Characteristics

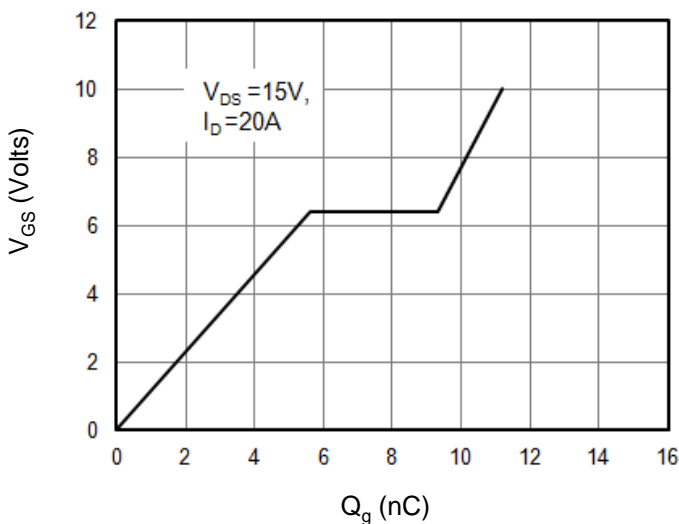


Figure 5: Gate Charge Characteristics

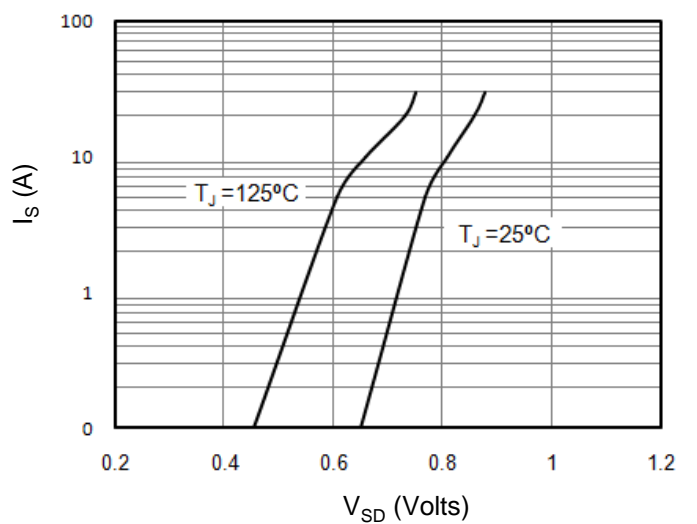


Figure 6: Body Diode Forward Voltage



### TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS

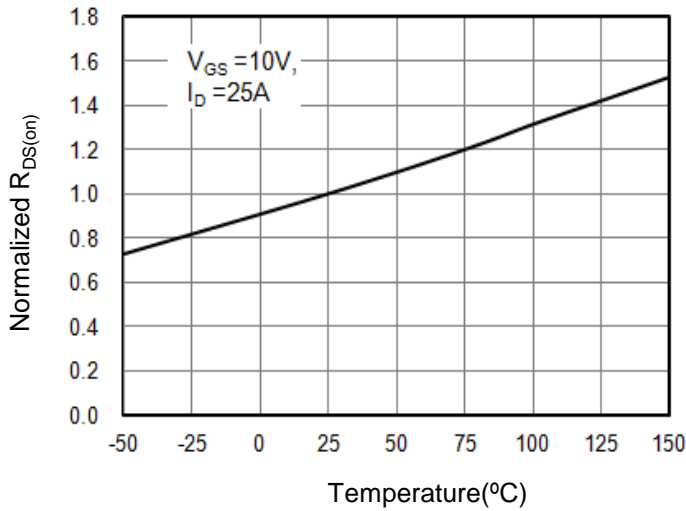


Figure 7: On-Resistance vs. Junction Temperature

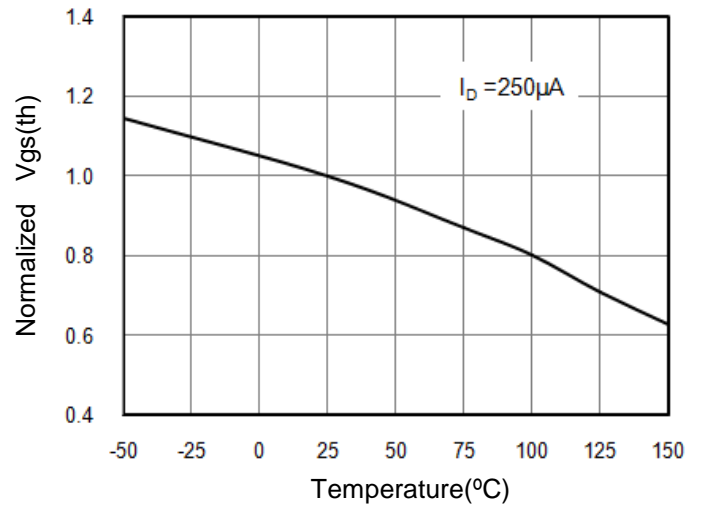


Figure 8:  $V_{GS(th)}$  vs. Junction Temperature

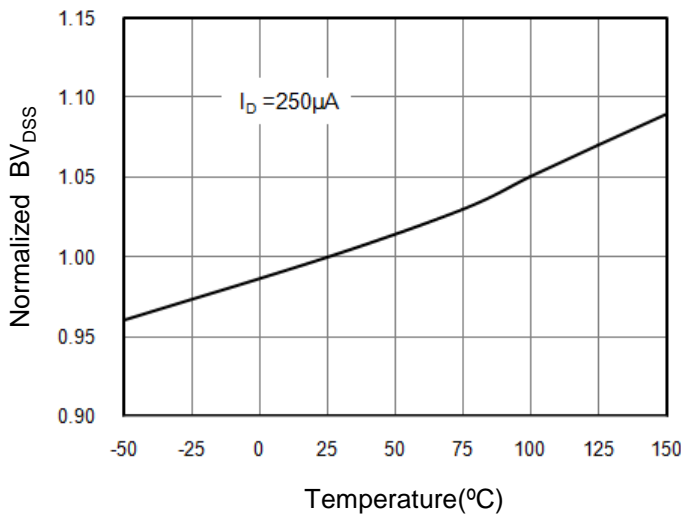


Figure 9:  $BV_{DS}$  vs. Junction Temperature

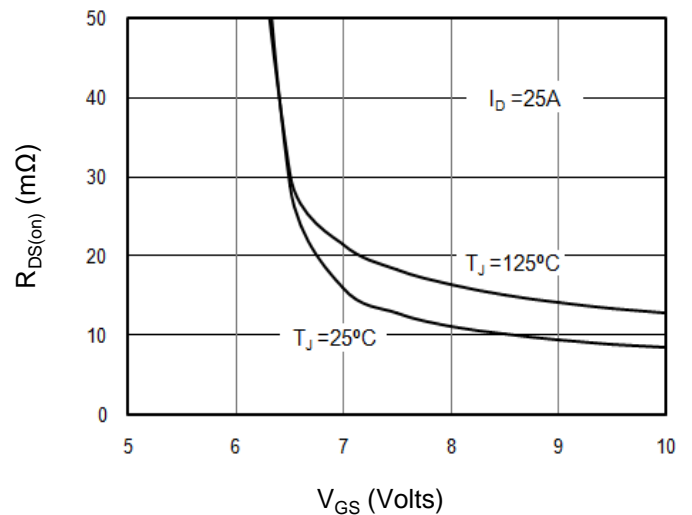


Figure 10: On-Resistance vs. Gate-Source Voltage

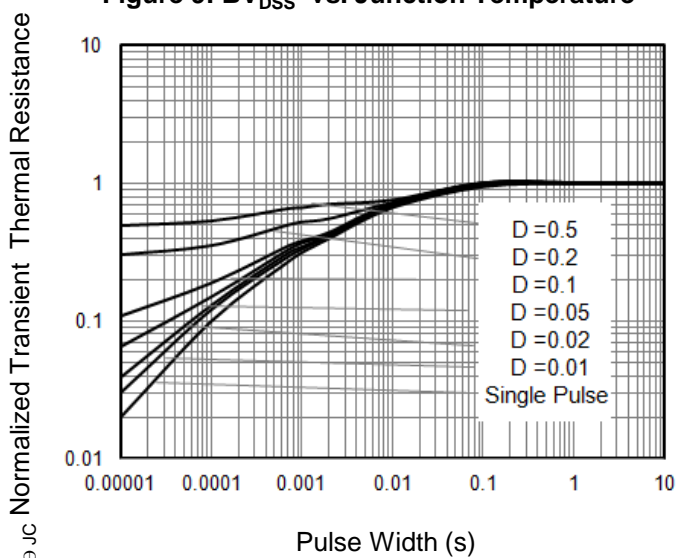


Figure 11: Normalized Transient Thermal Resistance

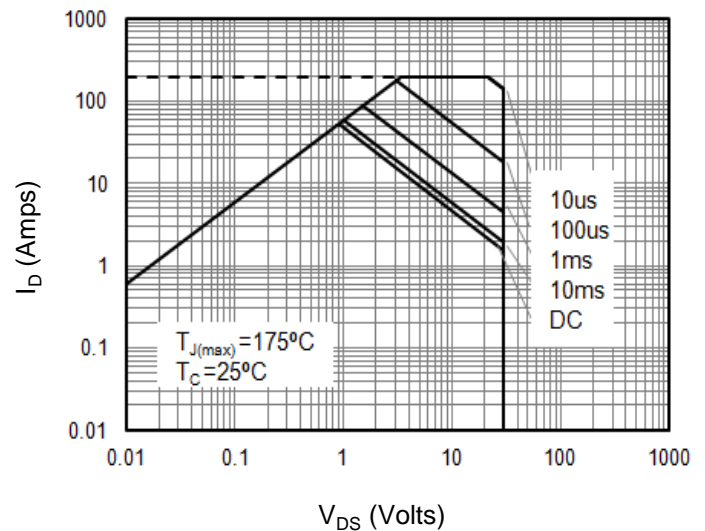


Figure 12: Safe Operating Area



Figure A: Gate Charge Test Circuit and Waveforms

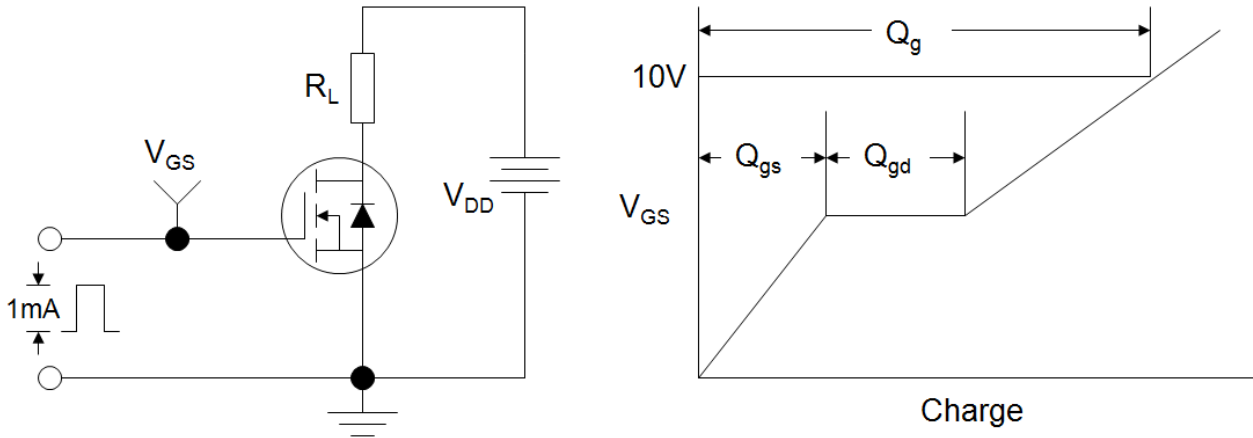


Figure B: Resistive Switching Test Circuit and Waveforms

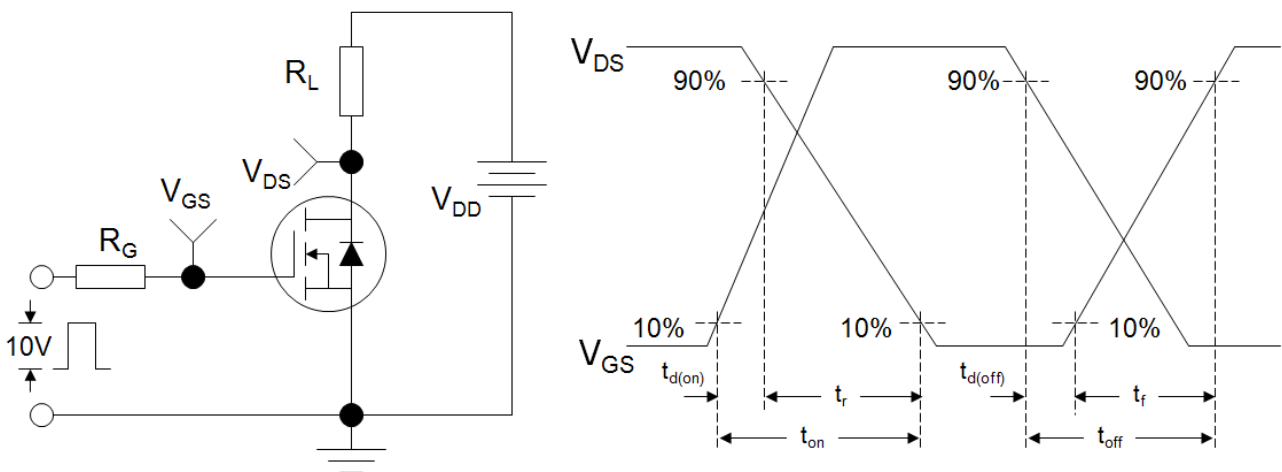
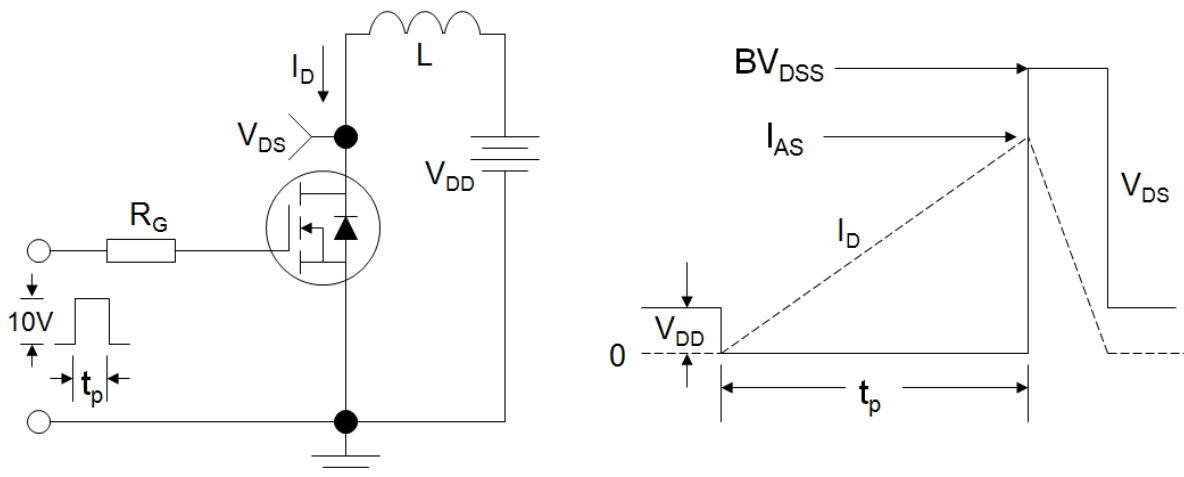
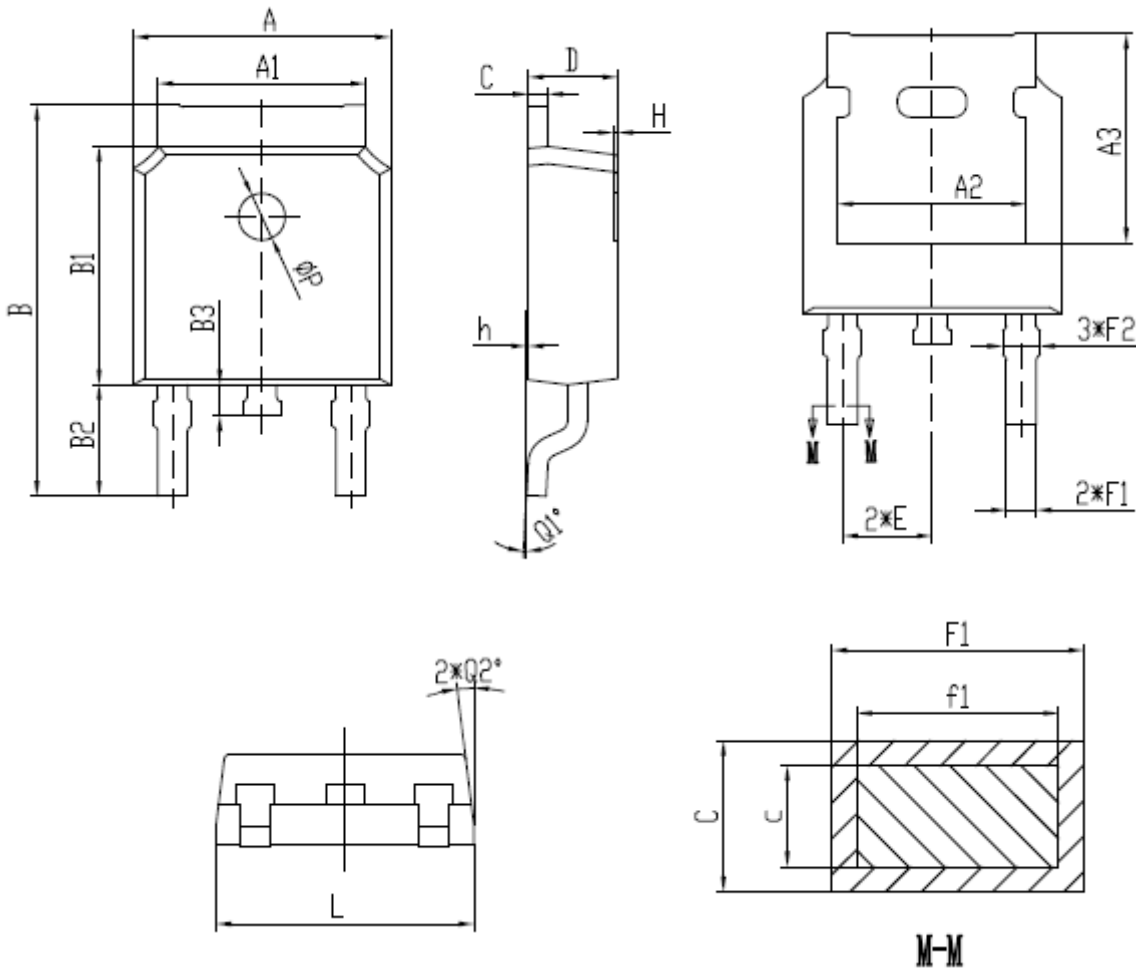


Figure C: Unclamped Inductive Switching (UIS) Test Circuit and Waveforms





### TO-252



SYMBOL	MIN	NOM	MAX
A	6.50	6.60	6.70
A1	5.16	5.31	5.46
A2	4.83 REF		
A3	5.30 REF		
B	9.77	9.97	10.17
B1	6.00	6.10	6.20
B2	2.60	2.80	3.00
B3	0.70	0.80	0.90
C	0.41	—	0.61
c	0.40	0.50	0.60

SYMBOL	MIN	NOM	MAX
D	2.20	2.30	2.40
E	2.186	2.286	2.386
F1	0.67	—	0.87
f1	0.66	0.76	0.86
F2	0.76	0.86	0.96
H	0.00	—	0.30
h	0.00	—	0.20
L	6.50	6.60	6.70
øP	1.10	1.20	1.30
Q1°	0°	—	8°
Q2°	6°	7°	8°



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