60V N-Channel SGT MOSFET

General Description

- **Trench Power SGT technology**
- Very low on-resistance $R_{DS(ON)}$
- Low Gate Charge
- Excellent Gate Charge x $R_{DS(ON)}$ Product

Applications

- High Frequency Switching and Synchronous Rectification

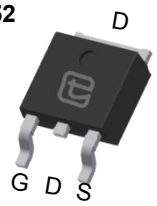
Product Summary

V_{DS}	60V
I_D (at V_{GS} =10V)	45A
$R_{DS(ON)}$ (at V_{GS} =10V)	< 15mΩ

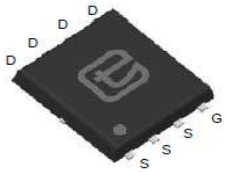
100% UIS Tested

RoHS

TO-252



DFN5x6



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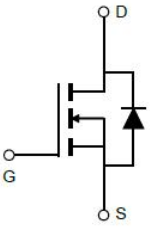


Part Number	Package Type	Form	Marking
TSD10N06A	TO-252	Tape & Reel	D10N06A
TSG10N06A	DFN5×6	Tape & Reel	G10N06A
TSU10N06A	TO-251	Tape & Reel	U10N06A

Absolute Maximum Ratings (T_A =25ºC unless otherwise noted)

Parameter		Symbol	Maximum	Units
Drain-Source Voltage		V_{DS}	60	V
Gate-Source Voltage		V_{GS}	±20	V
Continuous Drain Current [B]	T_C =25ºC	I_D	45	A
	T_C =100ºC		27	
Pulsed Drain Current [A]		I_{DM}	180	A
Avalanche Current [A]		I_{AS}	20	A
Single Pulse Avalanche Energy	L =0.3mH [A]	E_{AS}	20	mJ
Power Dissipation [C]	T_C =25ºC	P_D	56.5	W
	T_C =100ºC		35.7	W
Junction and Storage Temperature Range		T_J, T_{STG}	-55 to 175	ºC

Thermal Characteristics

Parameter		Symbol	Maximum	Units
Maximum Junction-to-Case	Steady-State	$R_{\theta JC}$	2.1	ºC/W
Maximum Junction-to-Ambient	Steady-State	$R_{\theta JA}$	50	

Electrical Characteristics(T_J =25ºC unless otherwise noted)							
Symbol	**Parameter**	**Conditions**		**Value**			**Units**
				Min	**Typ**	**Max**	
STATIC PARAMETERS							
BV_{DSS}	Drain-Source Breakdown Voltage	I_D =250μA,V_{GS} =0V		60	--	--	V
I_{DSS}	Zero Gate Voltage Drain Current	V_{DS} =60V, V_{GS} =0V	T_J =25ºC	--	--	1	μA
			T_J =125ºC	--	--	100	
I_{GSS}	Gate-Body Leakage Current	V_{DS} =0V, V_{GS} =±20V		--	--	±100	nA
$V_{GS(th)}$	Gate Threshold Voltage	V_{DS} =V_{GS}, I_D =250μA		2.5	--	4.0	V
$R_{DS(ON)}$	Static Drain-Source On-Resistance	V_{GS} =10V, I_D =20A		--	12	15	mΩ
g_{FS}	Forward Transconductance	V_{DS} =5V, I_D =20A		--	100	--	S
V_{SD}	Diode Forward Voltage	I_S =1A, V_{GS} =0V		--	--	1	V
I_S	Maximum Body-Diode Continuous Current [B]			--	--	30	A
DYNAMIC PARAMETERS							
C_{iss}	Input Capacitance	V_{GS} =0V, V_{DS} =30V, f =1MH_Z		--	1134	--	pF
C_{oss}	Output Capacitance			--	123	--	
C_{rss}	Reverse Transfer Capacitance			--	12	--	
SWITCHING PARAMETERS							
Q_g(10V)	Total Gate Charge	V_{GS} =10V,V_{DS} =30V, I_D =20A		--	21	--	nC
Q_{gs}	Gate Source Charge			--	3.1	--	
Q_{gd}	Gate Drain Charge			--	5.1	--	
$t_{D(on)}$	Turn-On Delay Time	V_{GS} =10V,V_{DS} =30V, I_D =20A, R_G =3Ω		--	7	--	ns
t_r	Turn-On Rise Time			--	3	--	
$t_{D(off)}$	Turn-Off Delay Time			--	20	--	
t_f	Turn-Off Fall Time			--	3	--	
t_{rr}	Body Diode Reverse Recovery Time	I_F =20A, di/dt =500A/μs		--	17	--	ns
Q_{rr}	Body Diode Reverse Recovery Charge			--	60	--	nC

A. Single pulse width limited by maximum junction temperature.

B. The maximum current rating is package limited.

C. The power dissipation P_D is based on $T_{J(MAX)}$ =175ºC, using junction-to-case thermal resistance, and is more useful in setting the upper dissipation limit for cases where additional heatsinking is used.

Typical Characteristics T_J = 25ºC, unless otherwise noted

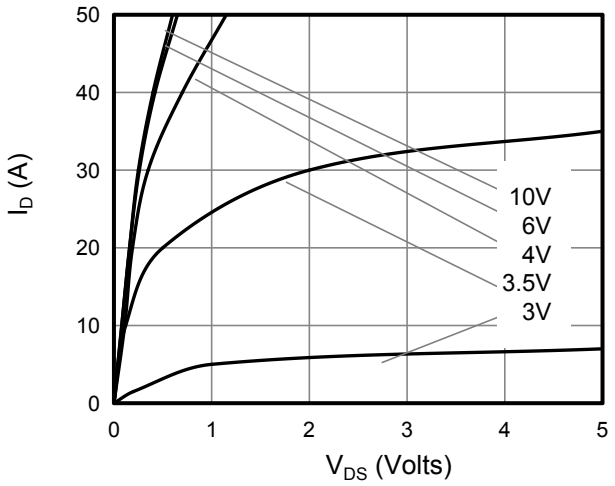


Figure 1: On-Region Characteristics

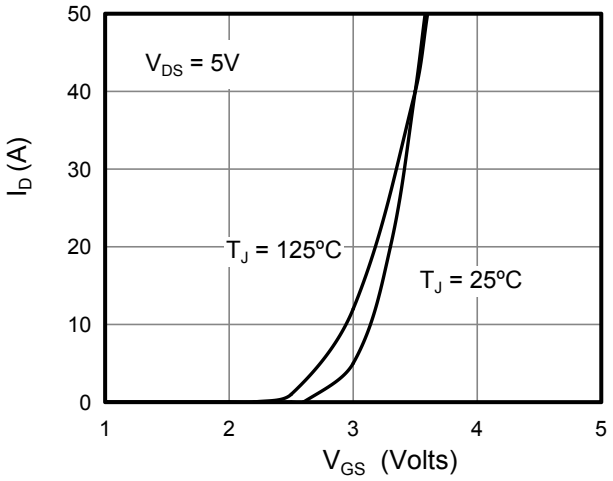


Figure 2: Transfer Characteristics

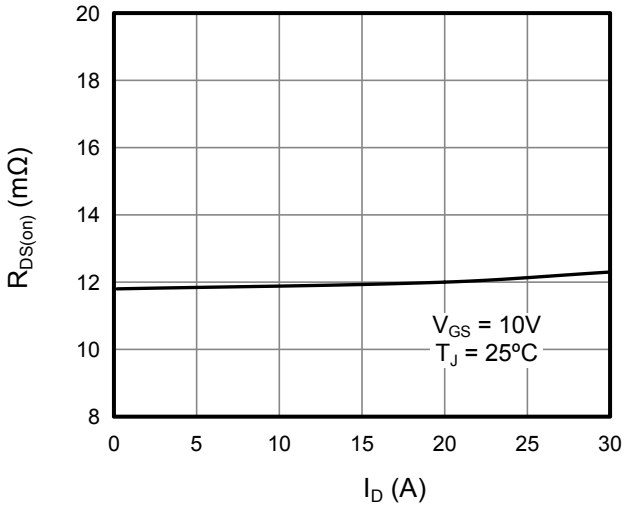


Figure 3: On-Resistance vs. Drain Current

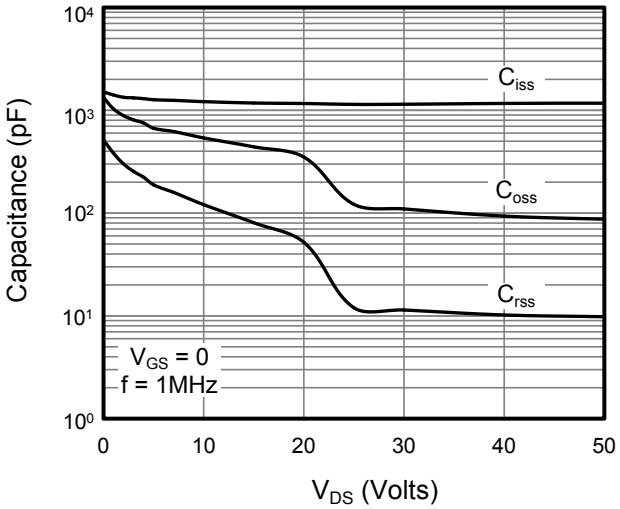


Figure 4: Capacitance Characteristics

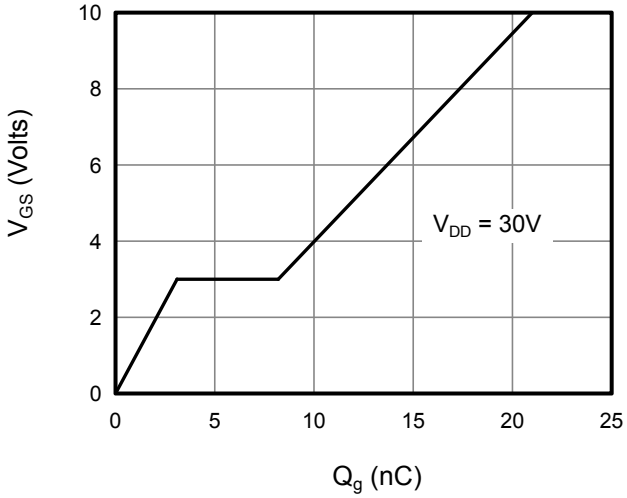


Figure 5: Gate Charge Characteristics

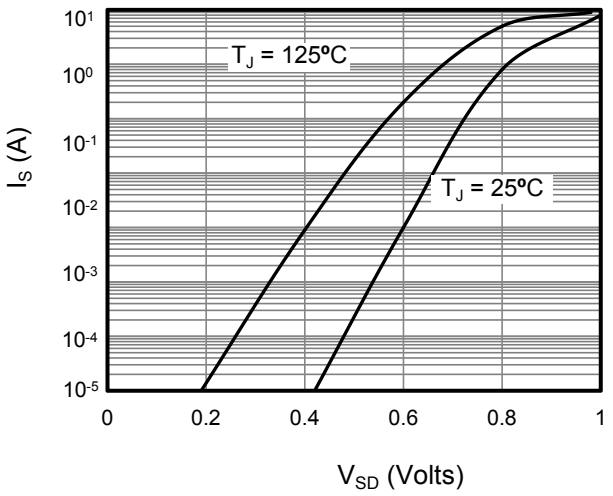


Figure 6: Body Diode Forward Voltage

Typical Characteristics T_J = 25ºC, unless otherwise noted

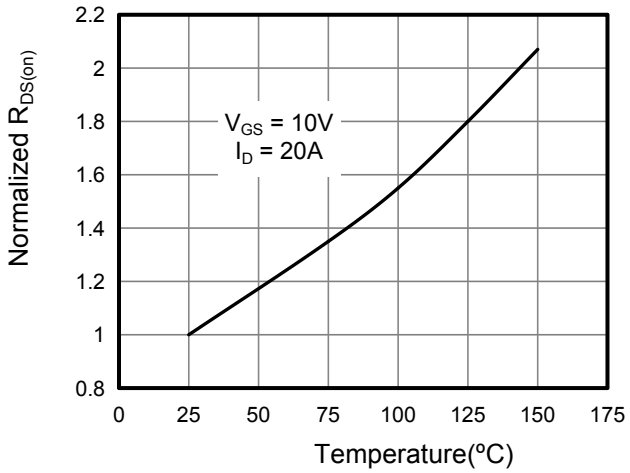


Figure 7: On-Resistance vs. Junction Temperature

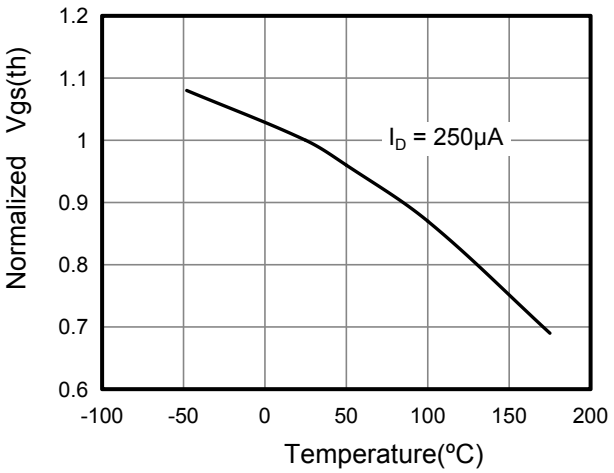


Figure 8: Vgs(th) vs. Junction Temperature

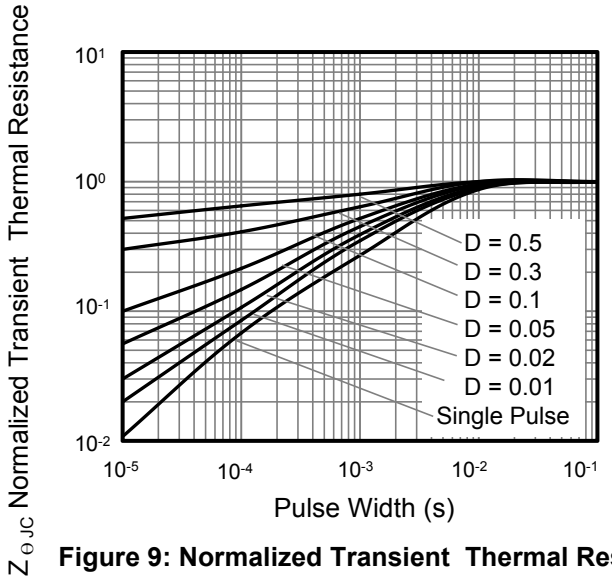


Figure 9: Normalized Transient Thermal Resistance

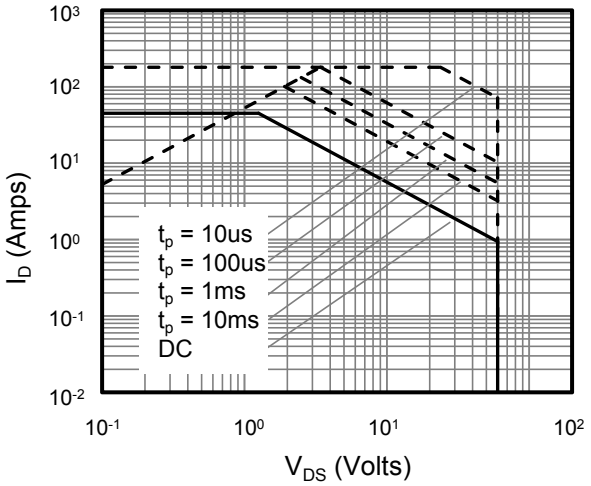


Figure 10: Safe Operating Area

Figure A：Gate Charge Test Circuit and Waveform

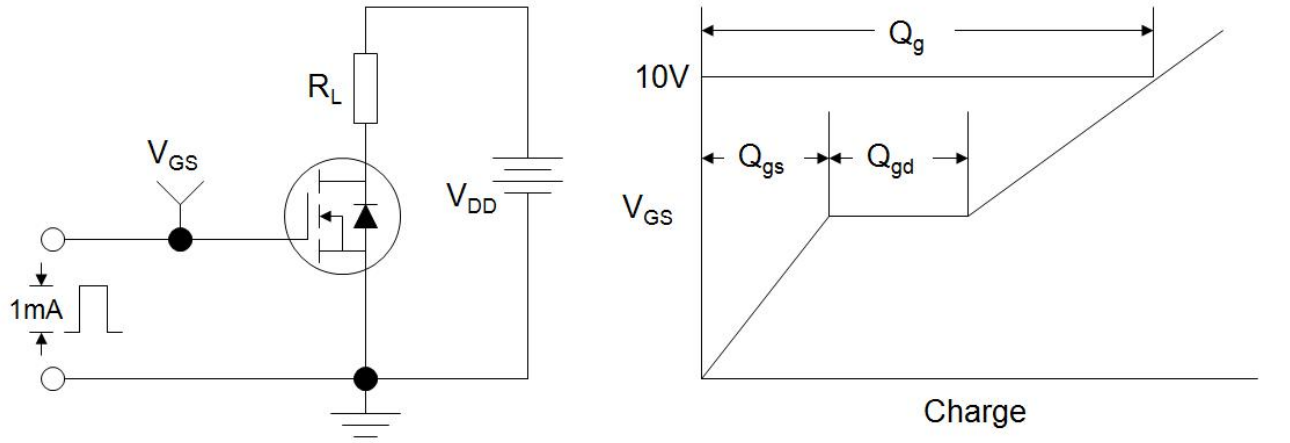


Figure B：Resistive Switching Test Circuit and Waveform

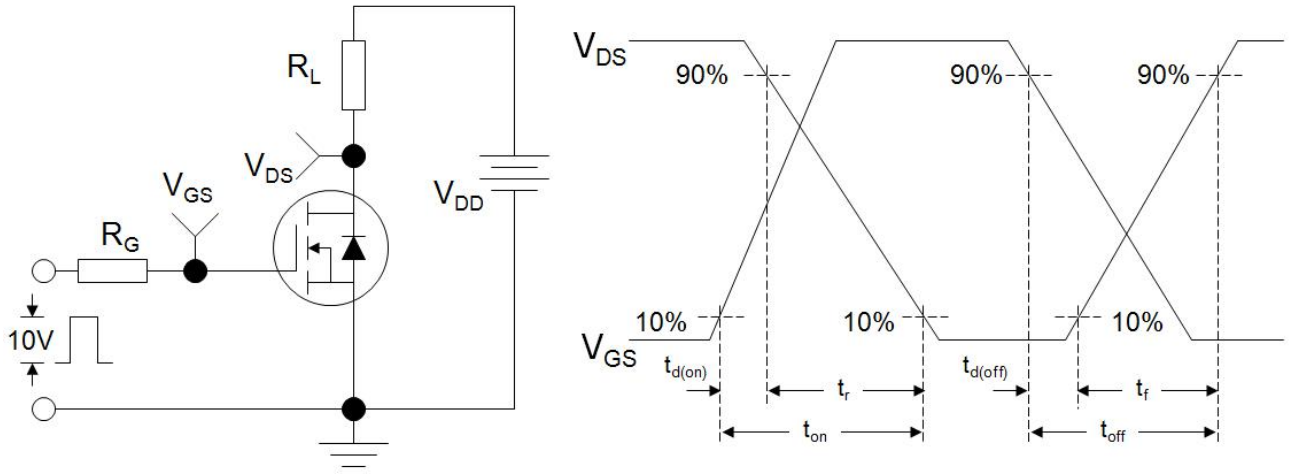
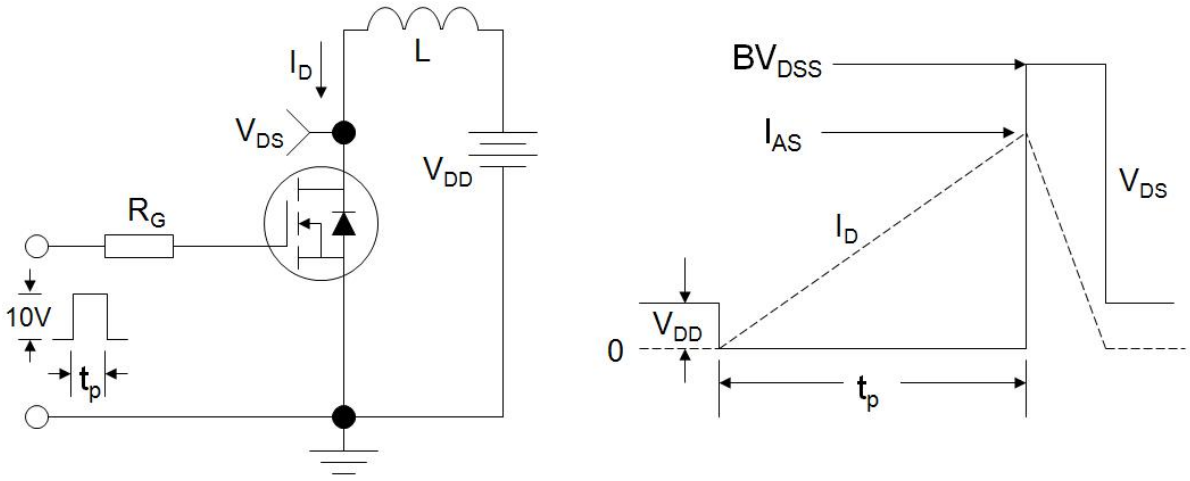
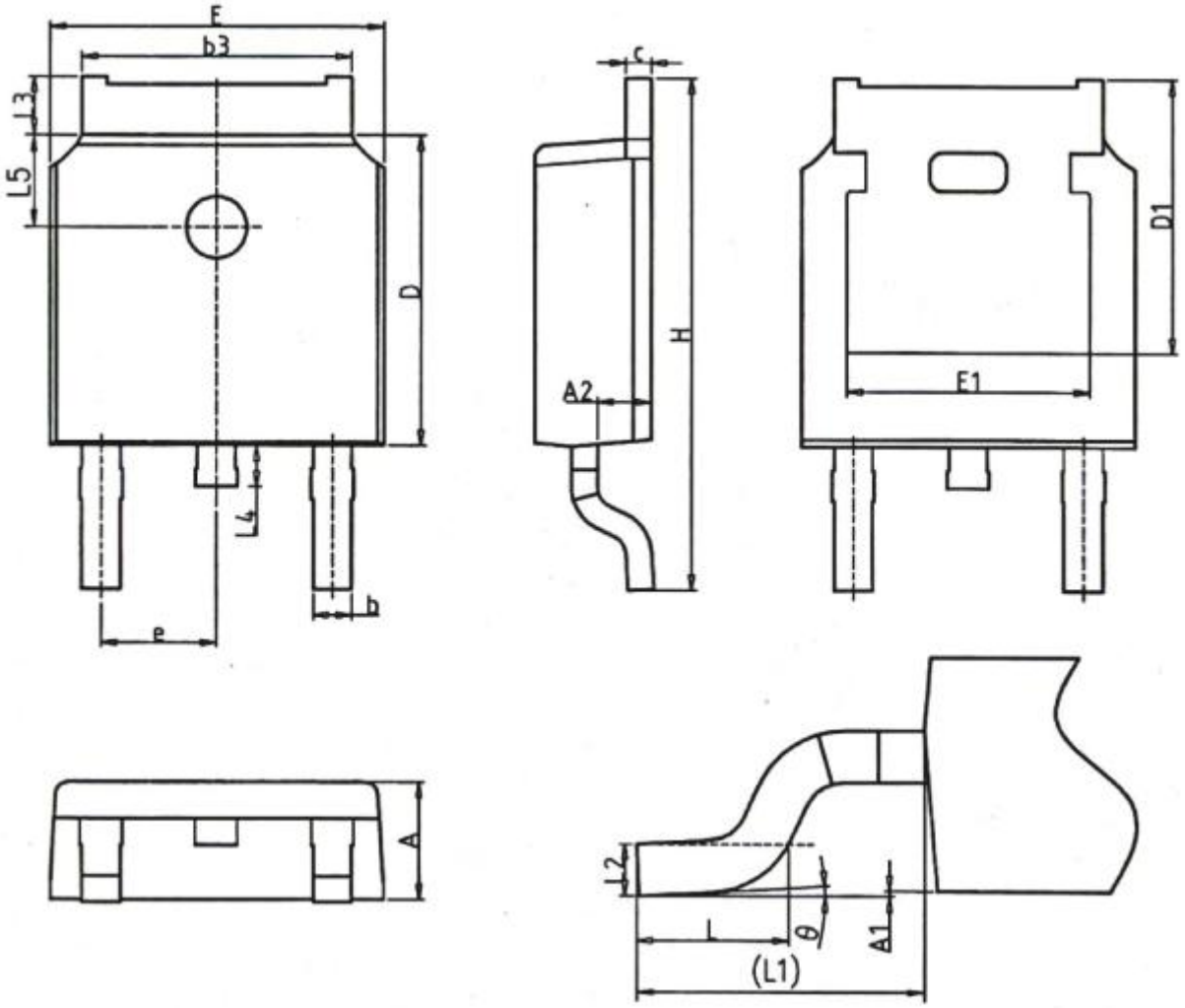


Figure C：Unclamped Inductive Switching Test Circuit and Waveform



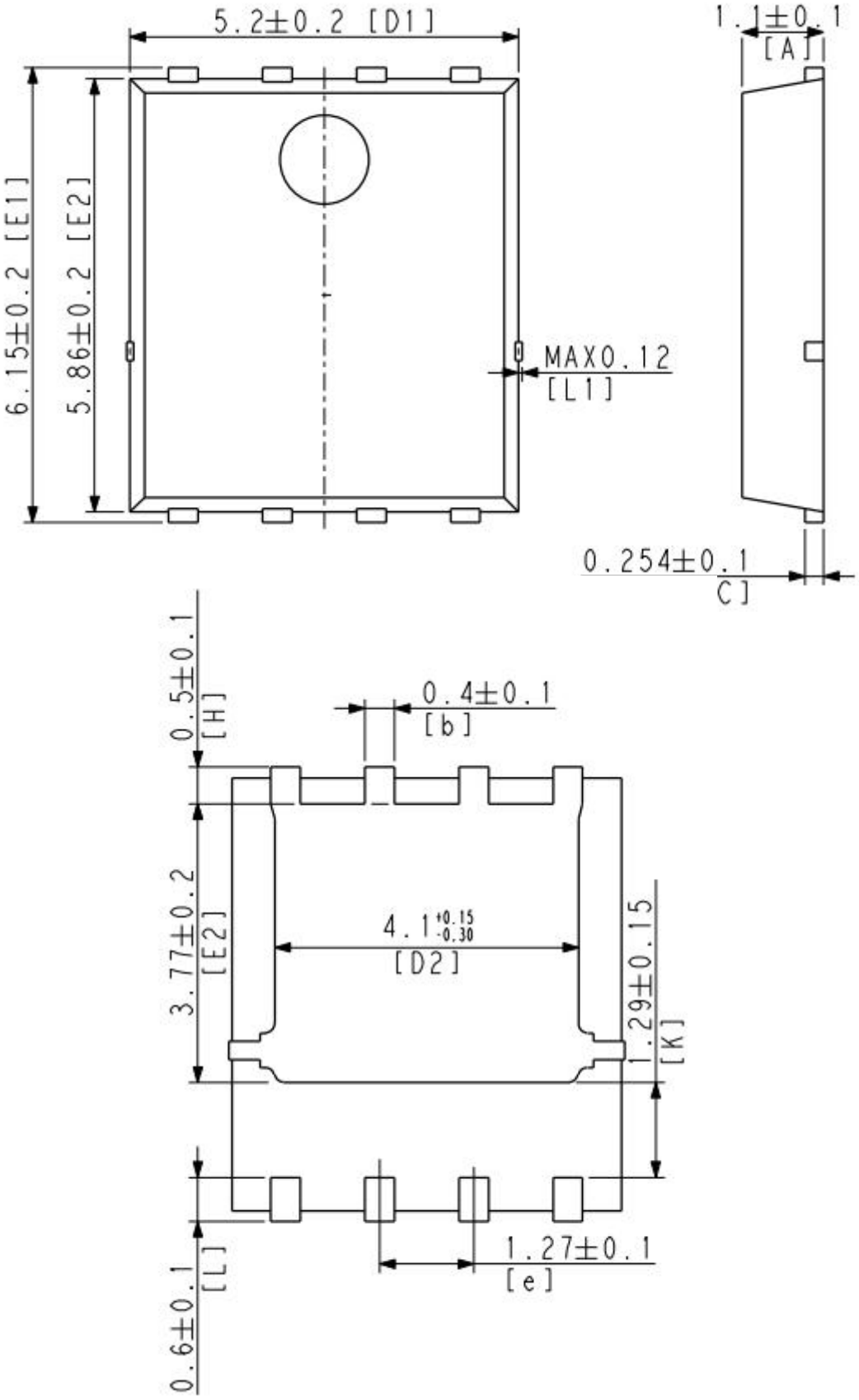
TO-252（华羿）



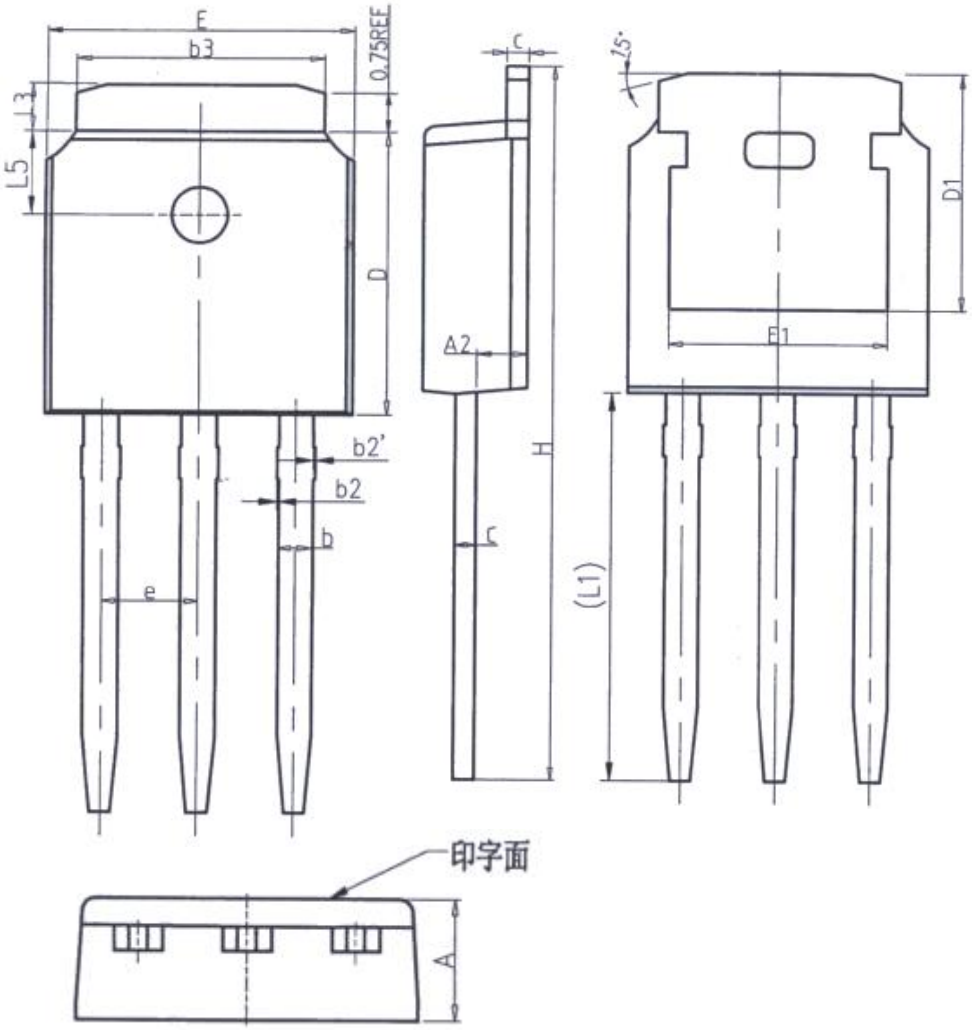
Unit:mm			
Symbol	Min.	Nom	Max.
A	2.20	2.30	2.38
A1	0.00	-	0.20
A2	0.97	1.07	1.17
b	0.68	0.78	0.90
b3	5.20	5.33	546
c	0.43	0.53	0.61
D	5.98	6.10	6.22
D1	5.30 REF		
E	6.40	6.60	6.73
E1	4.63	-	-

Unit:mm			
Symbol	Min.	Nom	Max.
e	2.286 BSC		
H	9.40	10.10	10.50
L	1.38	1.50	1.75
L1	2.90 REF		
L2	0.51 BSC		
L3	0.88	-	1.28
L4	0.50	-	1.00
L5	1.65	1.80	1.95
θ	0°	-	8°

DFN5x6（华天）


5.2±0.2 [D1]
1.1±0.1
[A]
6.15±0.2 [E1]
5.86±0.2 [E2]
MAX0.12
[L1]
0.254±0.1
[C]
0.5±0.1
[H]
0.4±0.1
[b]
3.77±0.2
[E2]
4.1 +0.15 -0.30
[D2]
1.29±0.15
[K]
1.27±0.1
[e]
0.6±0.1
[L]

TO-251（创立达）



Unit: mm		
Symbol	Min.	Max.
A	2.20	2.40
A2	0.97	1.17
b	0.68	0.90
b2	0.00	0.10
b2′	0.00	0.10
b3	5.20	5.50
c	0.43	0.63
D	5.98	6.22

Unit: mm		
Symbol	Min.	Max.
D1	5.30REF	
E	6.40	6.80
E1	4.63	-
e	2.286BSC	
H	16.22	16.82
L1	9.15	9.65
L3	0.88	1.28
L5	1.65	1.95

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