
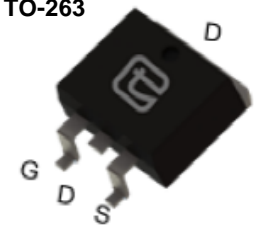
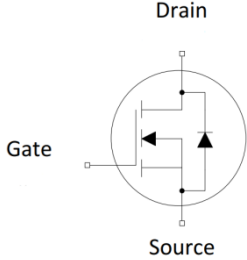


**60V N-Channel DTMOS**

<b>General Description</b> <ul style="list-style-type: none"> <li>● Trench Power SGT technology</li> <li>● Very low on-resistance <math>R_{DS(ON)}</math></li> <li>● Low Gate Charge</li> <li>● Excellent Gate Charge x <math>R_{DS(ON)}</math> Product</li> </ul> <b>Applications</b> <ul style="list-style-type: none"> <li>● High Frequency Switching and Synchronous Rectification</li> </ul>		<b>Product Summary</b> <table> <tr> <td><math>V_{DS}</math></td> <td>60V</td> </tr> <tr> <td>ID (at VGS=10V)</td> <td>180A</td> </tr> <tr> <td>RDS(ON) (at VGS=10V)</td> <td>&lt; 3mΩ</td> </tr> <tr> <td colspan="2">100% UIS Tested</td> </tr> </table> 		$V_{DS}$	60V	ID (at VGS=10V)	180A	RDS(ON) (at VGS=10V)	< 3mΩ	100% UIS Tested	
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ID (at VGS=10V)	180A										
RDS(ON) (at VGS=10V)	< 3mΩ										
100% UIS Tested											
											
<b>Device</b>	<b>Package</b>	<b>Form</b>	<b>Marking</b>								
TSB15N06A	TO-263	Tape&Reel	B15N06A								

Absolute Maximum Ratings ( $T_A = 25^\circ\text{C}$ unless otherwise noted)				
Parameter		Symbol	Maximum	Units
Drain-Source Voltage		$V_{DS}$	60	V
Gate-Source Voltage		$V_{GS}$	$\pm 20$	V
Continuous Drain Current	$T_C = 25^\circ\text{C}$	$I_D$	180	A
	$T_C = 100^\circ\text{C}$		108	
Pulsed Drain Current <sup>A</sup>		$I_{DM}$	720	A
Avalanche Current <sup>A</sup>		$I_{AS}$	28	A
Single Pulse Avalanche Energy $L = 0.3\text{mH}$ <sup>A</sup>		$E_{AS}$	609	mJ
Power Dissipation <sup>C</sup>	$T_C = 25^\circ\text{C}$	$P_D$	208	W
	$T_C = 100^\circ\text{C}$		125	W
Operating Junction and Storage Temperature Range		$T_J, T_{stg}$	-55 to 175	$^\circ\text{C}$

Thermal Resistance				
Parameter		Symbol	Maximum	Units
Maximum Junction-to-Case	Steady-State	$R_{thJC}$	0.6	$^\circ\text{C}/\text{W}$
Maximum Junction-to-Ambient	Steady-State	$R_{thJA}$	60	



Electrical Characteristics( $T_J = 25^\circ\text{C}$ unless otherwise noted)							
Symbol	Parameter	Conditions	Value			Units	
			Min	Typ	Max		
<b>STATIC PARAMETERS</b>							
$BV_{DSS}$	Drain-Source Breakdown Voltage	$I_D = 250\mu\text{A}, V_{GS} = 0\text{V}$	60	--	--	V	
$I_{DSS}$	Zero Gate Voltage Drain Current	$V_{DS} = 60\text{V}, V_{GS} = 0\text{V}$	$T_J = 25^\circ\text{C}$	--	--	1	$\mu\text{A}$
			$T_J = 100^\circ\text{C}$	--	--	100	
$I_{GSS}$	Gate-Body Leakage Current	$V_{DS} = 0\text{V}, V_{GS} = \pm 20\text{V}$	--		$\pm 100$	nA	
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_D = 250\mu\text{A}$	2	3	4	V	
$R_{DS(ON)}$	Static Drain-Source On-Resistance	$V_{GS} = 10\text{V}, I_D = 50\text{A}$	--	2.5	3	$\text{m}\Omega$	
$g_{FS}$	Forward Transconductance	$V_{DS} = 10\text{V}, I_D = 50\text{A}$	--	140	--	S	
$V_{SD}$	Diode Forward Voltage	$I_S = 50\text{A}, V_{GS} = 0\text{V}$	--	--	1	V	
$I_S$	Maximum Body-Diode Continuous Current <sup>B</sup>		--	--	50	A	
<b>DYNAMIC PARAMETERS</b>							
$C_{iss}$	Input Capacitance	$V_{GS} = 0\text{V}, V_{DS} = 30\text{V}, f = 1\text{MHz}$	--	7700	--	$\text{pF}$	
$C_{oss}$	Output Capacitance		--	667	--		
$C_{rss}$	Reverse Transfer Capacitance		--	66	--		
<b>SWITCHING PARAMETERS</b>							
$Q_g(10\text{V})$	Total Gate Charge	$V_{GS} = 10\text{V}, V_{DS} = 30\text{V}, I_D = 50\text{A}$	--	138	--	nC	
$Q_{gs}$	Gate Source Charge		--	37	--		
$Q_{gd}$	Gate Drain Charge		--	35.5	--		
$t_{D(on)}$	Turn-On Delay Time	$V_{GS} = 10\text{V}, V_{DS} = 30\text{V}, I_D = 50\text{A}, R_G = 3\Omega$	--	35	--	ns	
$t_r$	Turn-On Rise Time		--	22	--		
$T_{D(off)}$	Turn-Off Delay Time		--	105	--		
$t_f$	Turn-Off Fall Time		--	45	--		
$t_{rr}$	Body Diode Reverse Recovery Time	$I_F = 50\text{A}, di/dt = 500\text{A}/\mu\text{s}$	--	50	--	ns	
$Q_{rr}$	Body Diode Reverse Recovery Charge		--	110	--	nC	

A. Single pulse width limited by maximum junction temperature.

B. The maximum current rating is package limited.

C. The power dissipation  $P_D$  is based on  $T_{J(MAX)} = 175^\circ\text{C}$ , using junction-to-case thermal resistance, and is more useful in setting the upper dissipation limit for cases where additional heatsinking is used.



Typical Characteristics  $T_J = 25^\circ\text{C}$ , unless otherwise noted

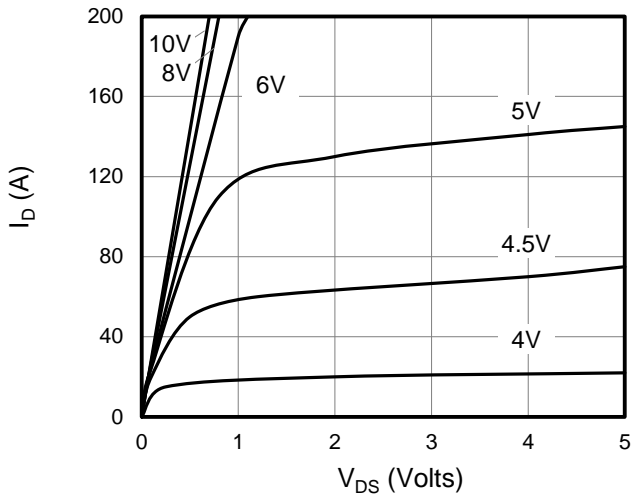


Figure 1: On-Region Characteristics

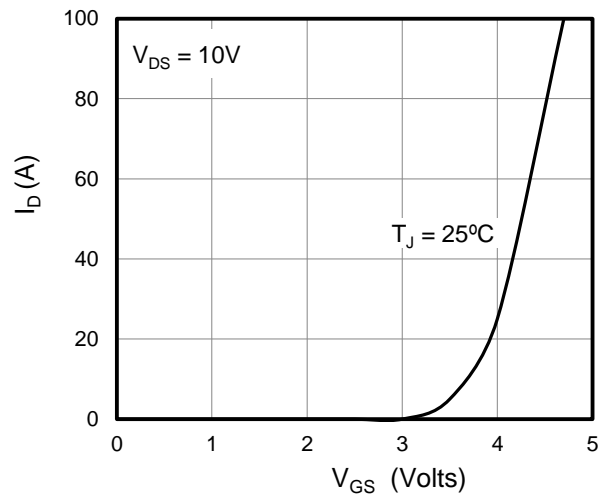


Figure 2: Transfer Characteristics

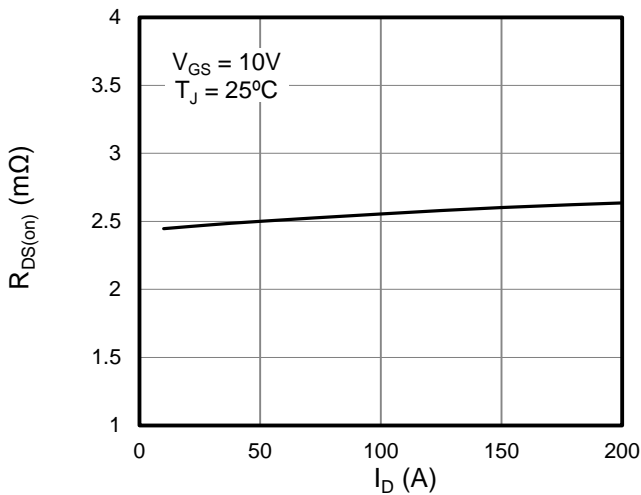


Figure 3: On-Resistance vs. Drain Current

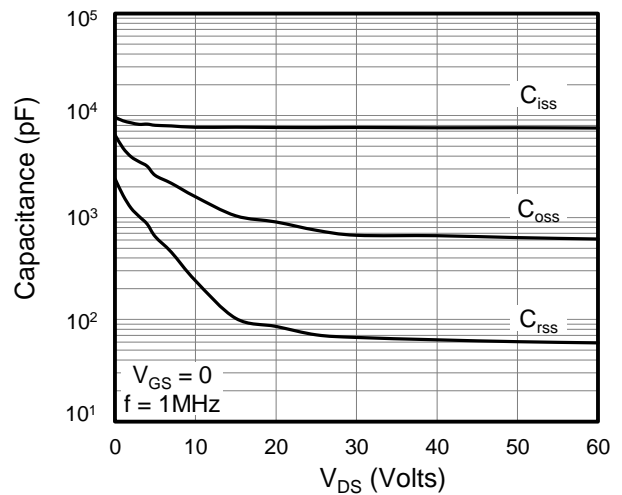


Figure 4: Capacitance Characteristics

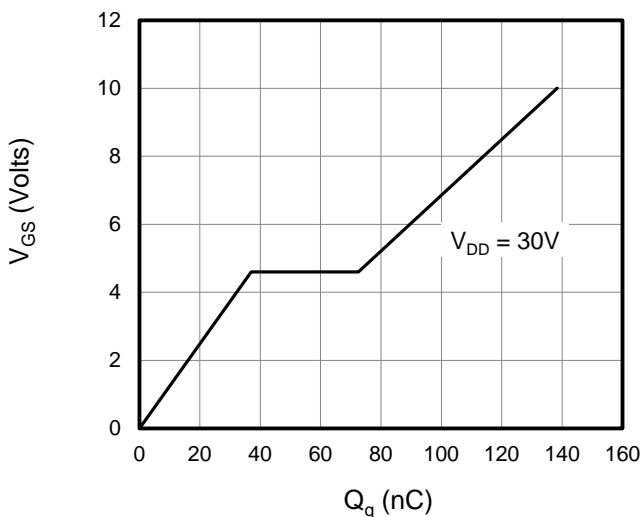


Figure 5: Gate Charge Characteristics

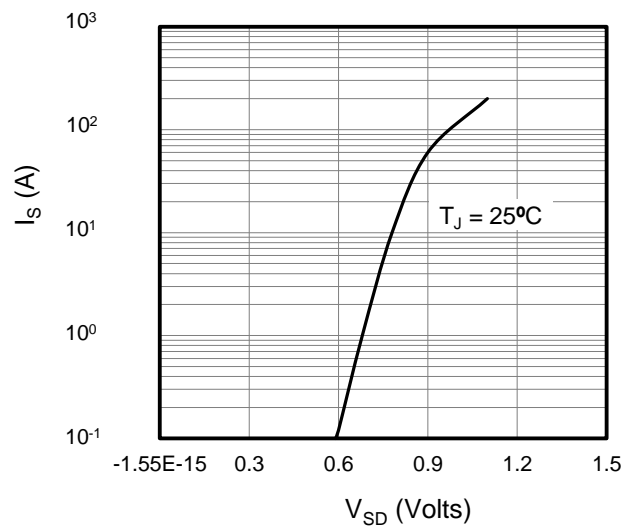


Figure 6: Body Diode Forward Voltage



Typical Characteristics  $T_J = 25^\circ\text{C}$ , unless otherwise noted

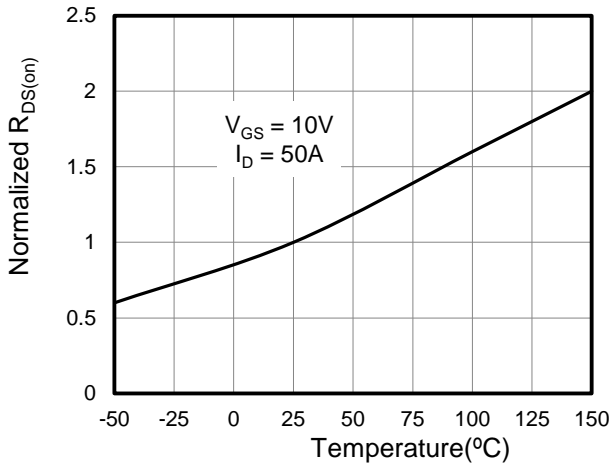


Figure 7: On-Resistance vs. Junction Temperature

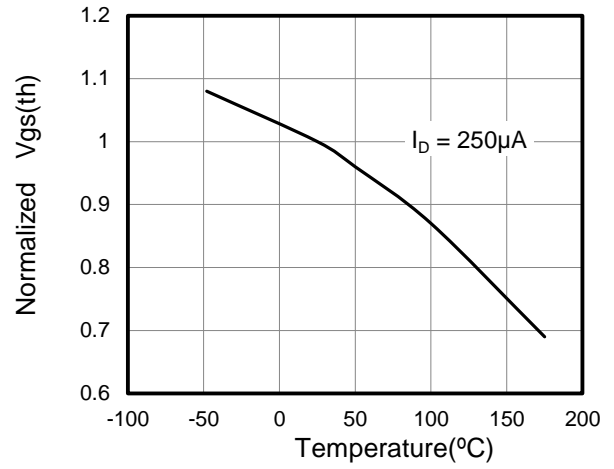


Figure 8:  $V_{gs(th)}$  vs. Junction Temperature

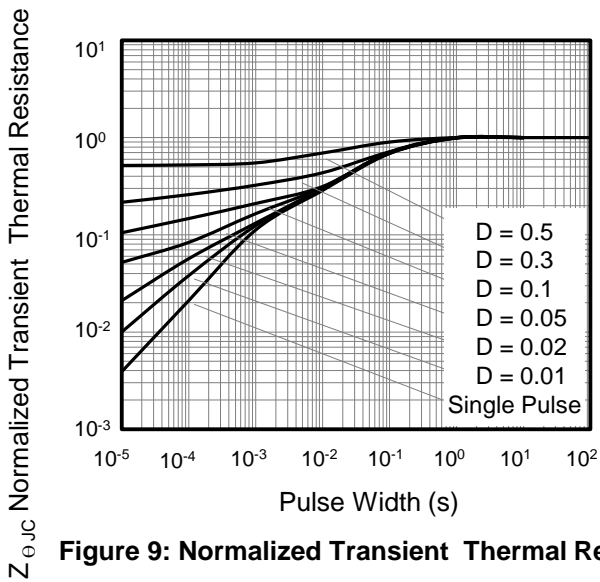


Figure 9: Normalized Transient Thermal Resistance

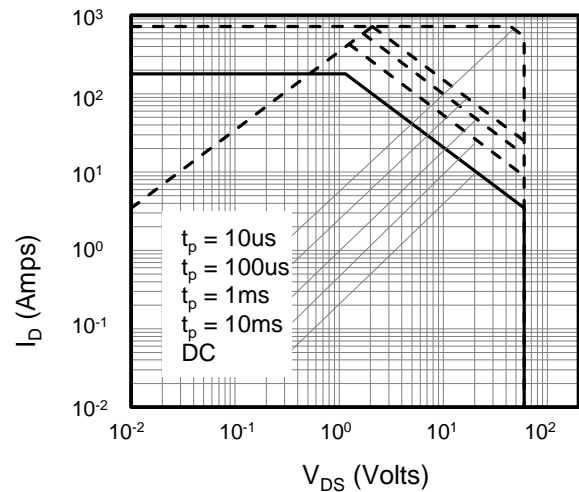


Figure 10: Safe Operating Area



Figure A: Gate Charge Test Circuit and Waveform



Figure B: Resistive Switching Test Circuit and Waveform

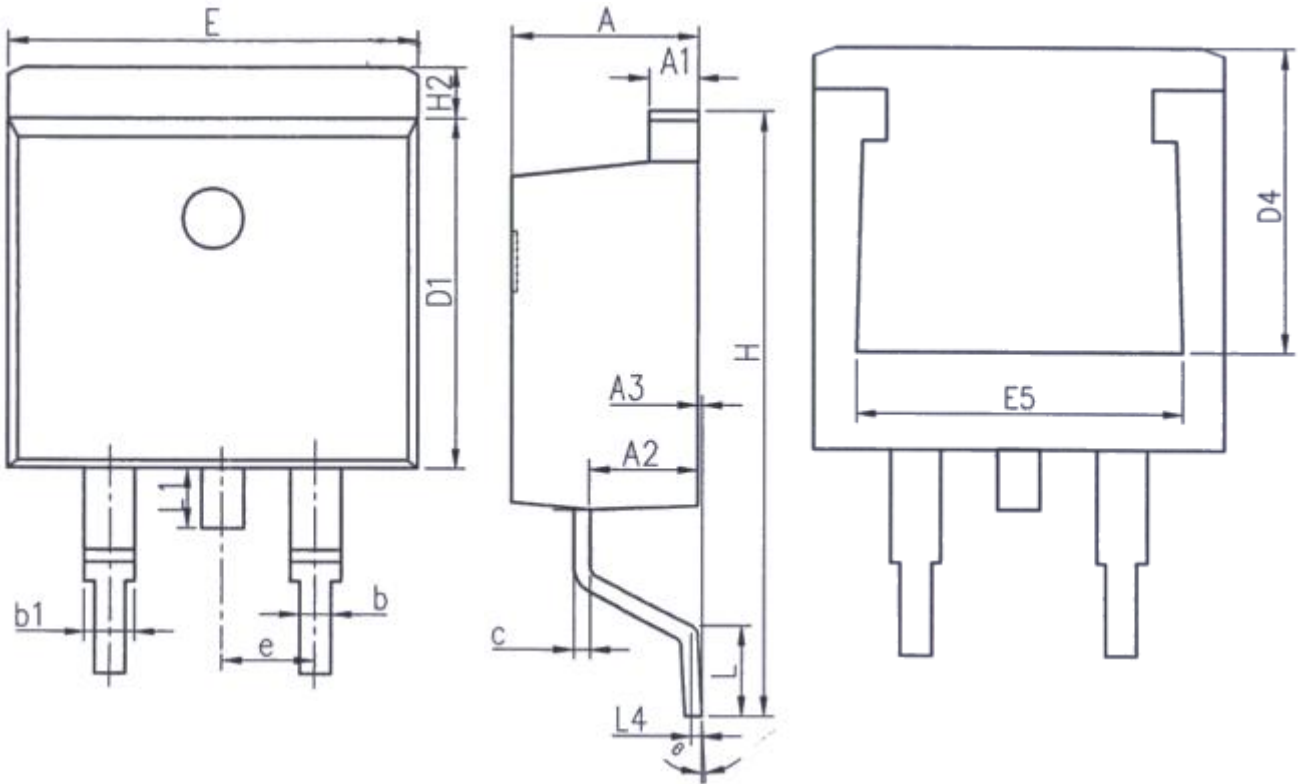


Figure C: Unclamped Inductive Switching Test Circuit and Waveform





### TO-263



Unit: mm		
Symbol	Min.	Max.
A	4.37	4.77
A1	1.22	1.42
A2	2.49	2.89
A3	0.00	0.25
b	0.70	0.96
b1	1.17	1.47
c	0.30	0.53
D1	8.50	8.90
D4	6.60	-

Unit: mm		
Symbol	Min.	Max.
E	9.86	10.36
E5	7.06	-
e	2.54BSC	
H	14.70	15.50
H2	1.07	1.47
L	2.00	2.60
L1	1.40	1.70
L4	0.25BSC	
θ	0°	9°



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