

600V Super-junction Power MOSFET

Description

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Super-junction power MOSFET is a revolutionary technology for high voltage power MOSFETs, designed according to the SJ principle and pioneered. The Multi-EPI SJ MOSFET provide an extremely fast and robust body diode. Also provide an extremely low switching, communication and conduction losses device with highest robustness make especially resonant switching applications more reliable, more efficient, lighter and cooler, also fits the industrial grade applications, like AC-DC SMPS requirements for PFC, AC/DC power conversion, designed by Wuxi Unigroup Microelectronics Company.

Footuroo		Applications		
 Features Ultra-fast body diode Very low FOM R_{DS(on)}×Q_g Easy to use/drive 100% avalanche tested RoHS compliant 	Gate	Applications Switch Mode Pow Uninterruptible Poil Power Factor Comilian LLC Half-bridge Charger Drain	wer Supply (UPS)	
Device Marking and Pack		Source		
Device	Package		Marking	
TPW60R040MFD	TO-247		60R040MFD	
Key Performance Paran	neters			
Parameter	Value		Unit	
V _{DS} @ T _{j,max}	650		V	
R _{DS(on),max}	0.04		Ω	
Q _{g,typ}	169		nC	
I _D	72		A	
I _{D,pulse}	216		A	
E _{OSS} @ 400V	24.15		μJ	
Body Diode di _F /dt	900		A/µs	
t _{rr}	225		ns	
Q _{rr}	1.4		μC	
I _{rrm}	12.5		A	



Absolute Maximum Ratings T _C = 25°C, unless otherwise noted						
Parameter			Symbol	Value	Unit	
Continuous Drain Current	T _C = 25°C			72		
	T _C = 100°C		Ι _D	43.2	A	
Pulsed Drain Current (note1)		note1)	I _{D,pulse}	216	А	
Gate-Source Voltage			V _{GSS}	±30	V	
Single Pulse Avalanche Energy (note2)		note2)	E _{AS}	2185	mJ	
Repetitive Avalanche Energy (note2)		note2)	E _{AR}	3.31	mJ	
Avalanche Current			I _{AR}	13.7	А	
MOSFET dv/dt Ruggedness, V _{DS} = 0480V			dv/dt	50	V/ns	
Power Dissipation For TO-247			P _D	500	W	
Continuous Diode Forward Current			۱ _s	72	Α	
Diode Pulsed Current (note1)		note1)	I _{S,pulse}	216		
Reverse Diode dv/dt (note3)		note3)	dv/dt	50	V/ns	
Maximum Diode Commutation Speed (note3)		note3)	di _f /dt	900	A/µs	
Operating Junction and Storage Temperature Range			T _J , T _{stg}	-55~+150	°C	

Thermal Resistance For TO-247			
Parameter	Symbol	Value	Unit
Thermal Resistance, Junction-to-Case	R _{thJC}	0.25	°C/W
Thermal Resistance, Junction-to-Ambient	R _{thJA}	62	-0/00



Electrical Characteristics	T _J = 25°C,	unless otherwise noted					
Parameter	Symbol	Test Conditions	Value			Unit	
Turumeter	Cymbol		Min.	Тур.	Max.	Onic	
Static Characteristics							
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V_{GS} = 0V, I _D = 250µA	600			V	
Zero Gate Voltage Drain Current	I _{DSS}	V_{DS} = 600V, V_{GS} = 0V, T_{J} = 25°C			10	μA	
Gate-Source Leakage	I _{GSS}	$V_{GS} = \pm 30V$			±100	nA	
Gate-Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}$, $I_D = 250 \mu A$	3		5	V	
Drain-Source On-State-Resistance	R _{DS(on)}	V _{GS} = 10V, I _D = 36A		0.035	0.04	Ω	
Gate resistance	R _G	f = 1.0MHz open drain		0.3		Ω	
Dynamic Characteristics					-		
Input Capacitance	C _{iss}	\/ − 0\/		8043		pF	
Output Capacitance	C _{oss}	$V_{GS} = 0V,$ $V_{DS} = 100V,$		283			
Reverse Transfer Capacitance	C _{rss}	f = 1.0MHz		3.6			
Total Gate Charge	Qg			169		nC	
Gate-Source Charge	Q _{gs}	$V_{DD} = 480V, I_D = 50A, V_{GS} = 10V$		50			
Gate-Drain Charge	Q _{gd}			65			
Turn-on Delay Time	t _{d(on)}			91			
Turn-on Rise Time	t _r	V _{DD} = 400V, I _D = 50A,		90			
Turn-off Delay Time	t _{d(off)}	$R_{\rm G} = 25\Omega$		583		ns	
Turn-off Fall Time	t _f			133			
Drain-Source Body Diode Characteristics							
Body Diode Voltage	V _{SD}	T _J = 25°C, I _{SD} = 36A, V _{GS} = 0V		1.0	1.5	V	
Reverse Recovery Time	t _{rr}			225		ns	
Reverse Recovery Charge	Q _{rr}	V _R = 400V, I _F = 50A, di _F /dt = 100A/µs		1.4		μC	
Peak Reverse Recovery Current	I _{rrm}	a.t. a		12.5		А	

Notes

- 1. Repetitive Rating: Pulse width limited by maximum junction temperature
- 2. I_{AS} = 13.7A, V_{DD} = 50V, R_G = 25 Ω , Starting T_J = 25°C
- 3. Identical low side and high side switch with identical R_G



8

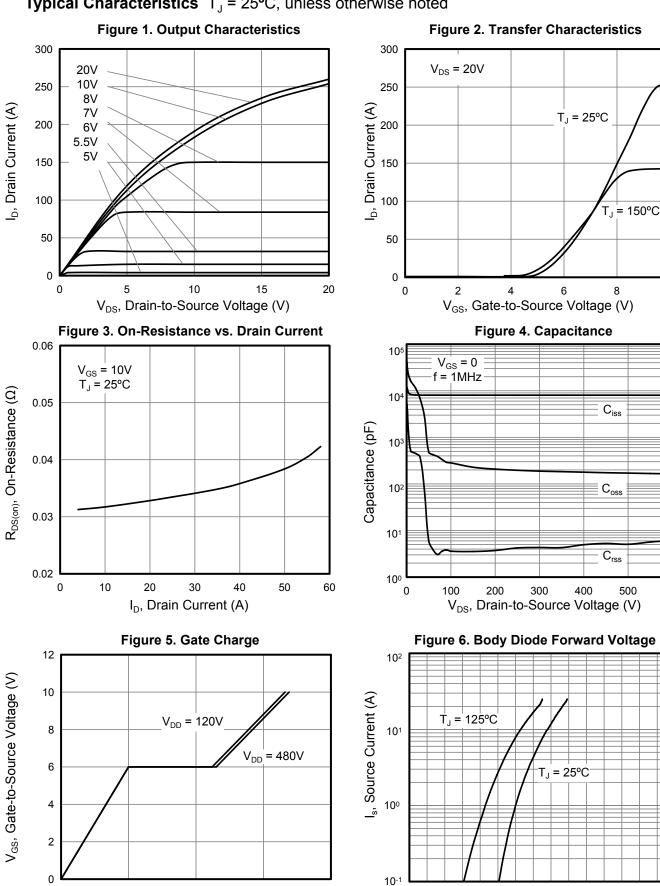
500

600

1.5

10

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Typical Characteristics $T_J = 25^{\circ}C$, unless otherwise noted

0

50

100

Q_g, Total Gate Charge (nC)

150

1.0

0.0

0.5

V_{SD}, Source-to-Drain Voltage (V)

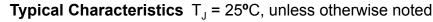
200

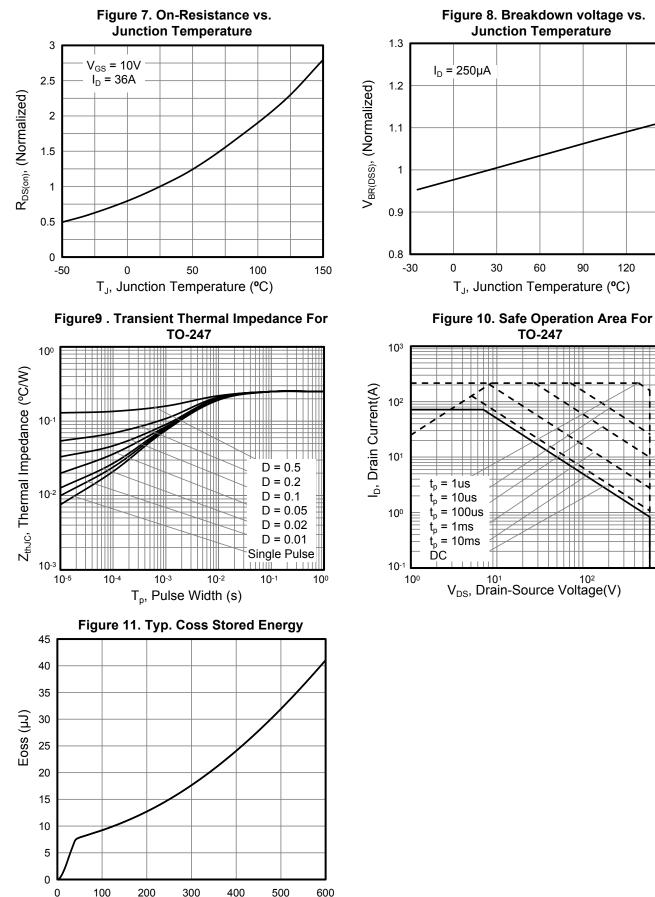


150

10³

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V_{DS}, Drain-Source Voltage(V)



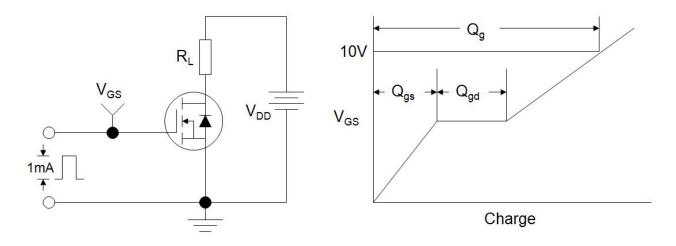


Figure B: Resistive Switching Test Circuit and Waveform

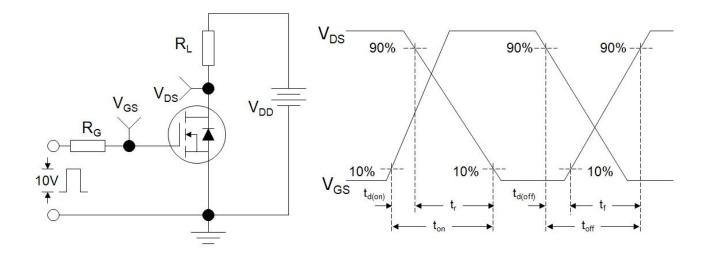
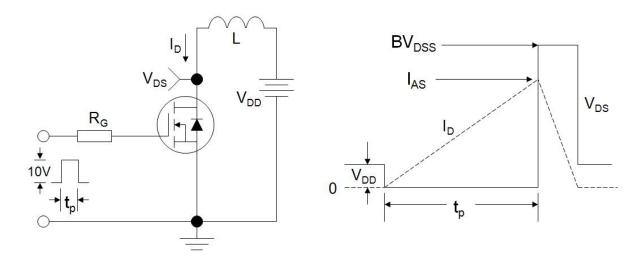


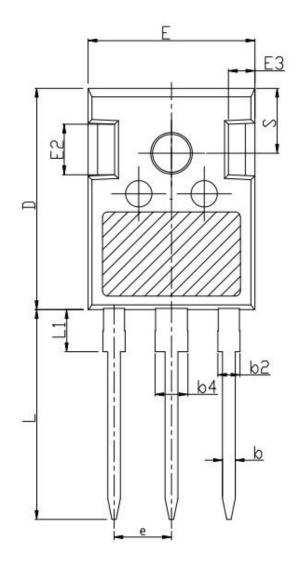
Figure C: Unclamped Inductive Switching Test Circuit and Waveform

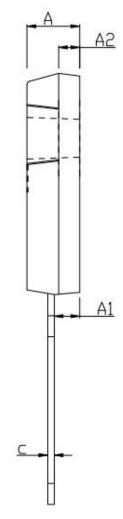


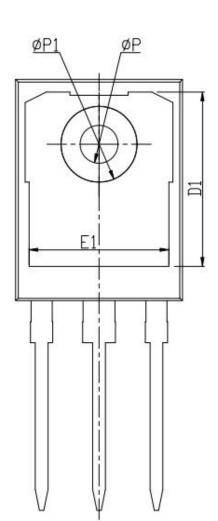
E

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TO-247







Unit:mm					
Symbol	Min.	Nom	Max.		
А	4.80	5.00	5.20		
A1	2.21	2.41	2.61		
A2	1.85	2.00	2.15		
b	1.11	1.21	1.36		
b2	1.91	2.01	2.21		
b4	2.91	3.01	3.21		
с	0.51	0.61	0.75		
D	20.70	21.00	21.30		
D1	16.25	16.55	16.85		

Unit:mm						
Symbol	Min.	Max.				
E	15.50	15.80	16.10			
E1	13.00	13.30	13.60			
E2	4.80	5.00	5.20			
E3	2.30	2.50	2.70			
е	5.44BSC					
L	19.62	19.92	20.22			
L1	-	-	4.30			
ΦΡ	3.40	3.60	3.80			
ΦP1	-	-	7.30			
S	6.15BSC					



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