

# **650V Super-Junction Power MOSFET**

## FEATURES

- Very low FOM  $R_{DS(on)} \times Q_g$
- 100% avalanche tested
- RoHS compliant

## APPLICATIONS

- Switch Mode Power Supply (SMPS)
- Uninterruptible Power Supply (UPS)
- Power Factor Correction (PFC)

TO-220 GDS	TO-220F GDS	T0-251 G D S	T0-252
RoHS	TO-262 GDS	TO-263 G <sup>D</sup> S	

Device Marking and Package Information						
Device	TPP65R1K2C	TPA65R1K2C	TPU65R1K2C	TPD65R1K2C	TPC65R1K2C	TPB65R1K2C
Package	TO-220	TO-220F	TO-251	TO-252	TO-262	TO-263
Marking	65R1K2C	65R1K2C	65R1K2C	65R1K2C	65R1K2C	65R1K2C

<b>Absolute Maximum Ratings</b> $T_c = 25^{\circ}C$ , unless otherwise noted					
		Value			
Parameter	Symbol	TO-220, TO-251, TO-252 TO-262, TO-263	TO-220F	Unit	
Drain-Source Voltage (V <sub>GS</sub> = 0V)	V <sub>DSS</sub>	650		V	
Continuous Drain Current	I <sub>D</sub>	4		А	
Pulsed Drain Current (note1)	I <sub>DM</sub>	12		А	
Gate-Source Voltage	V <sub>GSS</sub>	±30		V	
Single Pulse Avalanche Energy (note2)	E <sub>AS</sub>	52.8 m		mJ	
Avalanche Current (note1)	I <sub>AR</sub>	0.8 A		А	
Repetitive Avalanche Energy (note1)	E <sub>AR</sub>	0.09 n		mJ	
Power Dissipation ( $T_c = 25^{\circ}C$ )	P <sub>D</sub>	28 23		W	
Operating Junction and Storage Temperature Range	T <sub>J</sub> , T <sub>stg</sub>	-55~+150		°C	

Thermal Resistance					
		Value			
Parameter	Symbol	TO-220, TO-251, TO-252 TO-262, TO-263	TO-220F	Unit	
Thermal Resistance, Junction-to-Case	R <sub>thJC</sub>	4.4	5.5	⁰C/W	
Thermal Resistance, Junction-to-Ambient	R <sub>thJA</sub>	62	80	°C/VV	



<b>Specifications</b> $T_J = 25^{\circ}C$ , unless otherwise noted						
Parameter	Symbol	Symbol Test Conditions –		Value		Unit
		Test conditions	Min.	Тур.	Max.	
Static						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS} = 0V, I_{D} = 250\mu A$	650			V
Zero Gate Voltage Drain Current		$V_{DS} = 650V, V_{GS} = 0V, T_{J} = 25^{\circ}C$			1	
	I <sub>DSS</sub>	$V_{DS} = 650V, V_{GS} = 0V, T_{J} = 150^{\circ}C$			100	μA
Gate-Source Leakage	I <sub>GSS</sub>	$V_{GS}$ = $\pm 30V$			±100	nA
Gate-Source Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_D = 250 \mu A$	2.5		4.0	V
Drain-Source On-Resistance (Note3)	$R_{DS(on)}$	V <sub>GS</sub> = 10V, I <sub>D</sub> = 1A		1.1	1.2	Ω
Forward Transconductance (Note3)	9 <sub>fs</sub>	$V_{DS} = 10V, I_{D} = 1A$		3		S
Dynamic						
Input Capacitance	C <sub>iss</sub>	- V <sub>GS</sub> = 0V,		207		
Output Capacitance	C <sub>oss</sub>	$V_{GS} = 0V,$ $V_{DS} = 50V,$ f = 1.0MHz		8.2		pF
Reverse Transfer Capacitance	C <sub>rss</sub>			1.1		
Total Gate Charge	$Q_g$			8.9		
Gate-Source Charge	Q <sub>gs</sub>	$V_{DD} = 520$ V, $I_D = 4$ A, $V_{GS} = 10$ V		1.8		nC
Gate-Drain Charge	$Q_{gd}$			4		
Turn-on Delay Time	t <sub>d(on)</sub>			32		
Turn-on Rise Time	t <sub>r</sub>	V <sub>DD</sub> = 400V, I <sub>D</sub> = 4A,		30		
Turn-off Delay Time	t <sub>d(off)</sub>	$R_G = 25\Omega$		70		ns
Turn-off Fall Time	t <sub>f</sub>			32		
Drain-Source Body Diode Characteri	stics					
Continuous Body Diode Current	I <sub>s</sub>	T 2500			4	^
Pulsed Diode Forward Current	I <sub>SM</sub>	$T_{\rm C} = 25^{\circ}{\rm C}$			12	A
Body Diode Voltage	V <sub>SD</sub>	$T_J = 25^{o}C, I_{SD} = 4A, V_{GS} = 0V$		0.9	1.2	V
Reverse Recovery Time	t <sub>rr</sub>			196		ns
Reverse Recovery Charge	Q <sub>rr</sub>	V <sub>R</sub> = 520V, I <sub>F</sub> = I <sub>S</sub> , di <sub>F</sub> /dt = 100A/µs		0.8		μC
Peak Reverse Recovery Current	l <sub>rrm</sub>			7		А

#### Notes

- 1. Repetitive Rating: Pulse Width limited by maximum junction temperature
- 2.  $I_{AS} = 0.8A, V_{DD} = 50V, R_G = 25\Omega$ , Starting  $T_J = 25^{\circ}C$
- 3. Pulse Test: Pulse Width  $\leq$  300µs, Duty Cycle  $\leq$  1%

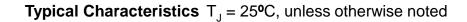


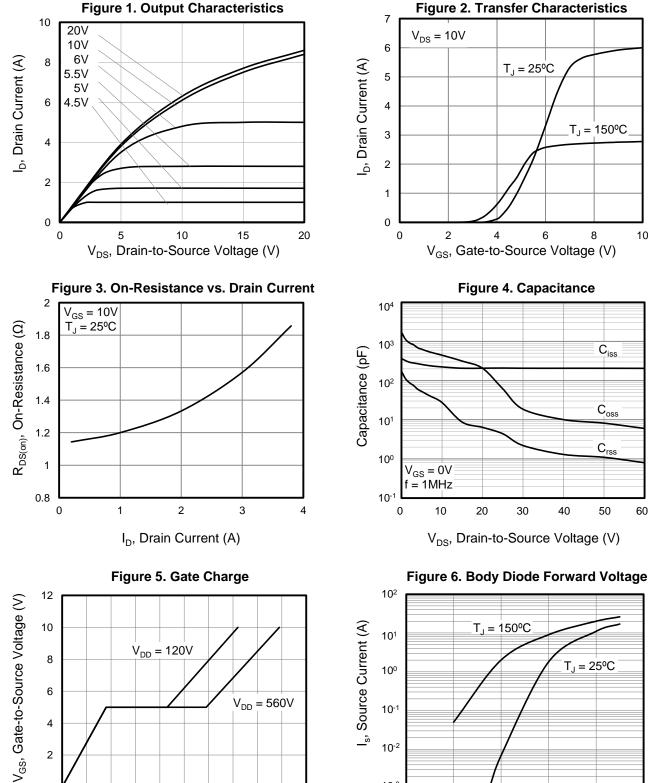
 $T_{\rm J} = 150^{\circ}C$ 

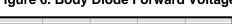
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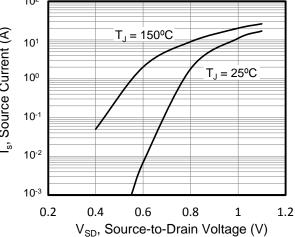
Coss

C<sub>rss</sub>



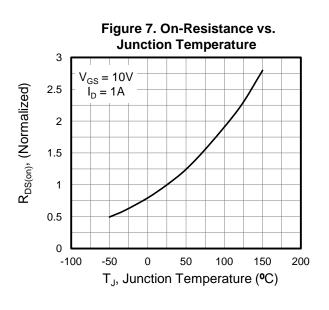




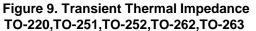


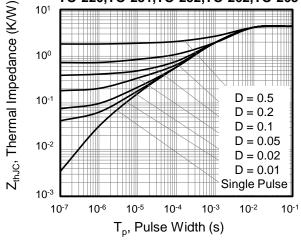
 Q<sub>q</sub>, Total Gate Charge (nC)





## **Typical Characteristics** $T_J = 25^{\circ}C$ , unless otherwise noted





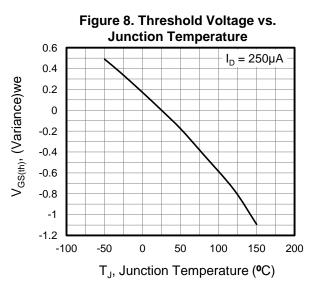


Figure 10. Transient Thermal Impedance TO-220F

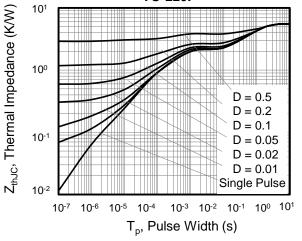
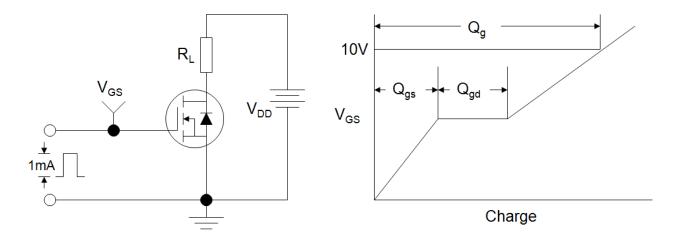
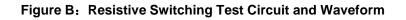


Figure A: Gate Charge Test Circuit and Waveform





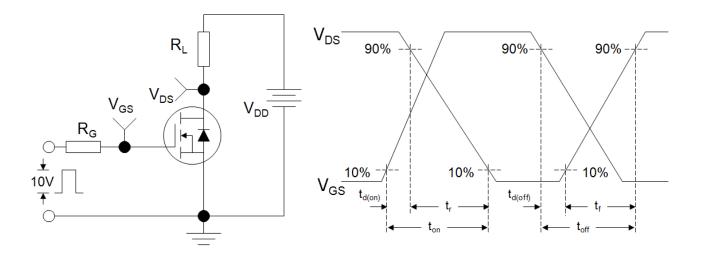
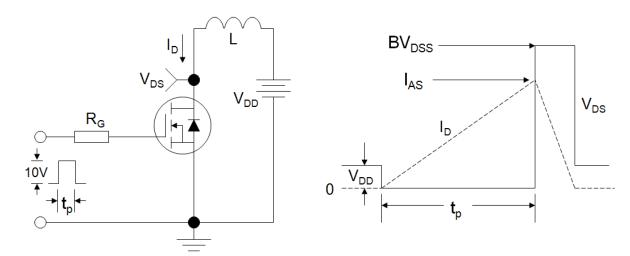
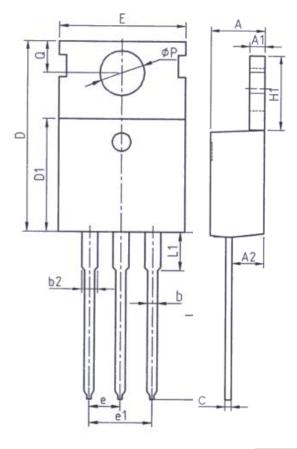
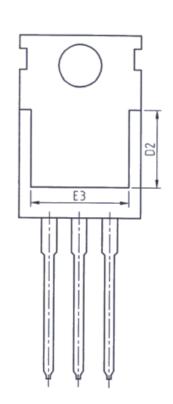


Figure C: Unclamped Inductive Switching Test Circuit and Waveform





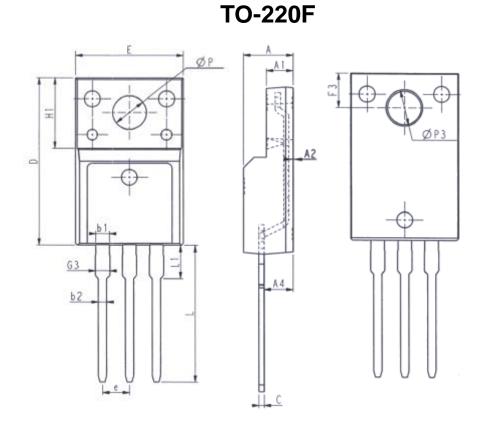




Unit: mm				
Symbol	Min.	Max.		
Α	4.37	4.77		
A1	1.25	1.45		
A2	2.20	2.60		
b	0.70	0.95		
b2	1.17	1.47		
c	0.40	0.65		
D	15. 10	16. 10		
D1	8.80	9.40		
D2	5.50	_		

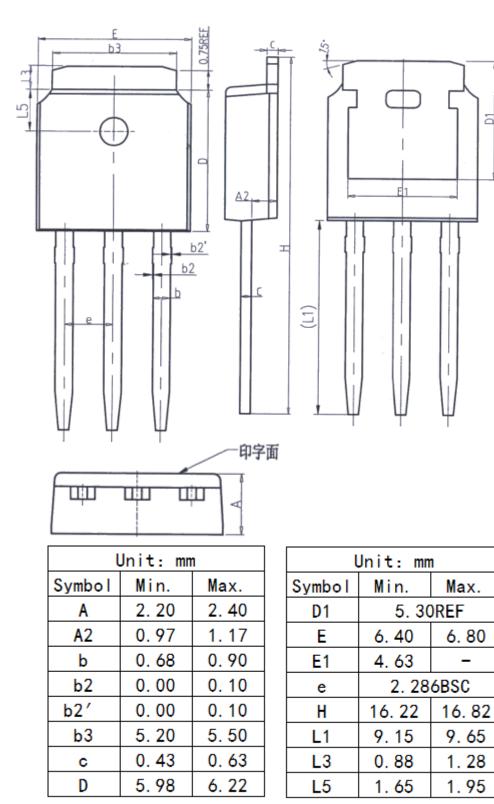
Unit: mm				
Symbol	Min.	Max.		
E	9.70	10. 30		
E3	7.00	-		
e	2.54BSC			
e1	5. 08	BSC		
H1	6. 25	6.85		
L	12.75	13.80		
L1	-	3. 40		
Р	3. 40	3.80		
Q	2.60	3.00		





Unit: mm			l	Jnit: mn	n
Symbol	Min.	Max.	Symbol	Min.	Max.
E	9.96	10.36	L	12.68	13.28
Α	4.50	4. 90	L1	2.93	3.13
A1	2.34	2.74	Р	3.03	3. 38
A2	0.30	0.60	P3	3.15	3.65
A4	2.56	2.96	F3	3.15	3.45
с	0.40	0.65	G3	1.25	1.55
D	15. 57	16.17	b1	1.18	1.43
H1	6. 70	OREF	b2	0.70	0.95
e	2.54BSC			-	



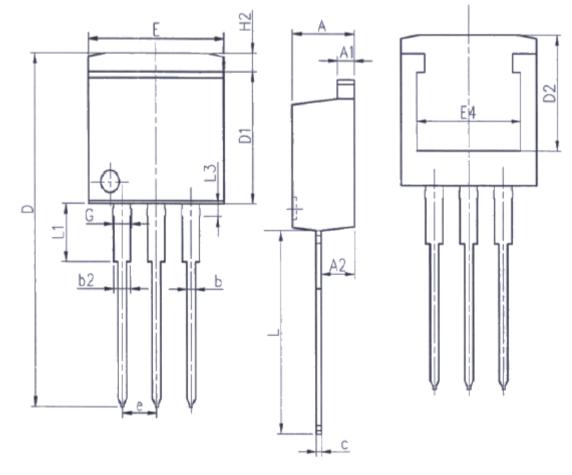




Unit: mm				
Symbol	Min.	Max.		
A	2.20	2.40		
A1	0.00	0.20		
A2	0.97	1.17		
b	0.68	0.90		
b3	5.20	5.50		
с	0.43	0.63		
D	5.98	6. 22		
D1	D1 5. 30REF			
E	6.40	6.80		
E1	4.63	_		

Unit: mm				
Symbol	Min.	Max.		
е	2. 28	6BSC		
Н	9.40	10.50		
L	1.38	1.75		
L1	2.90REF			
L2	0. 51	BSC		
L3	0.88	1.28		
L4	_	1.00		
L5	1.65	1.95		
θ	0°	8°		

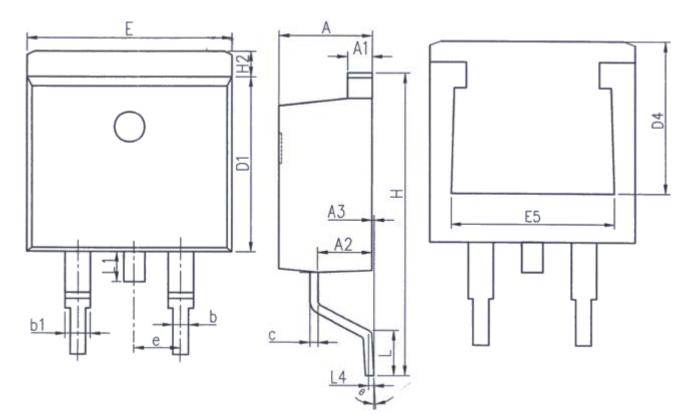




Unit: mm				
Symbol	Min.	Max.		
Α	4. 37	4. 77		
<b>A</b> 1	1. 22	1.42		
A2	2.47	2.87		
b	0.70	0.97		
b2	1.17	1.42		
c	0. 28	0.53		
D	23. 20	24. 02		
D1	8.38	8.90		
D2	6.00	-		

Unit: mm					
Symbol	Min.	Max.			
E	9.90	10.39			
E4	7.30	-			
е	2.54BSC				
G	1. 25	1.50			
H2	-	1.31			
L	13. 34	14. 10			
L1	3.30	4.06			
L3	0.95	1.15			





Unit: mm			Unit: mm		
Symbol	Min.	Max.	Symbol	Min.	Max.
Α	4. 37	4. 77	E	9.86	10.36
A1	1.22	1.42	E5	7.06	-
A2	2.49	2.89	e	2. 54BSC	
A3	0.00	0. 25	Н	14. 70	15. 50
b	0.70	0.96	H2	1.07	1.47
b1	1.17	1.47	L	2.00	2.60
с	0.30	0.53	L1	1.40	1.70
D1	8.50	8.90	L4	0. 25BSC	
D4	6. 60	-	θ	0°	<b>9</b> °



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