

# 700V Super-Junction Power MOSFET

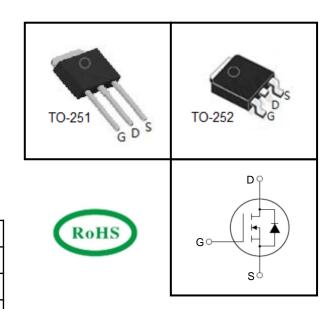
#### **FEATURES**

- $\qquad \text{Very low FOM } \mathsf{R}_{\mathsf{DS}(\mathsf{on})} \!\! \times \! \mathsf{Q}_{\mathsf{g}} \\$
- 100% avalanche tested
- RoHS compliant

#### **APPLICATIONS**

- Switch Mode Power Supply (SMPS)
- Uninterruptible Power Supply (UPS)
- Power Factor Correction (PFC)

Device Marking and Package Information			
Device	Package	Marking	
TPU70R2K8C	TO-251	70R2K8C	
TPD70R2K8C	TO-252	70R2K8C	



<b>Absolute Maximum Ratings</b> $T_C = 25^{\circ}C$ , unless otherwise noted				
Parameter	Symbol	Value		1114
Parameter		TO-251	TO-252	Unit
Drain-Source Voltage (V <sub>GS</sub> = 0V)	$V_{\mathrm{DSS}}$	70	00	V
Continuous Drain Current	I <sub>D</sub>	2	2	А
Pulsed Drain Current (note1)	I <sub>DM</sub>	6	5	А
Gate-Source Voltage	$V_{GSS}$	±;	30	V
Single Pulse Avalanche Energy (note2)	E <sub>AS</sub>	2	0	mJ
Avalanche Current (note1)	I <sub>AR</sub>	0.	5	А
Repetitive Avalanche Energy (note1)	E <sub>AR</sub>	0.0	05	mJ
Power Dissipation (T <sub>C</sub> = 25°C)	$P_{D}$	2	4	W
Operating Junction and Storage Temperature Range	$T_J,T_stg$	-55~-	+150	°C

Thermal Resistance					
Baramatan	Complete al	Va	lue	l lmit	
Parameter	Symbol	TO-251	TO-252	Unit	
Thermal Resistance, Junction-to-Case	R <sub>thJC</sub>	5.	.2	12/\\\	
Thermal Resistance, Junction-to-Ambient	R <sub>thJA</sub>	6	2	K/W	



Parameter	Symbol	Took Countities	Value			
		Test Conditions	Min.	Тур.	Max.	Unit
Static		•				
Drain-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	$V_{GS} = 0V, I_{D} = 250\mu A$	700			V
Zava Cata Valtana Dvain Cumant		$V_{DS} = 700V, V_{GS} = 0V, T_{J} = 25^{\circ}C$			1	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = 700V, V <sub>GS</sub> = 0V, T <sub>J</sub> = 150°C			100	μΑ
Gate-Source Leakage	I <sub>GSS</sub>	$V_{GS} = \pm 30V$			±100	nA
Gate-Source Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	2.5		4.0	V
Drain-Source On-Resistance (Note3)	R <sub>DS(on)</sub>	V <sub>GS</sub> = 10V, I <sub>D</sub> = 1A		2.5	2.8	Ω
Forward Transconductance (Note3)	g <sub>fs</sub>	V <sub>DS</sub> = 10V, I <sub>D</sub> = 1A		2		S
Dynamic		•				
Input Capacitance	C <sub>iss</sub>	V - 0V		182		pF
Output Capacitance	C <sub>oss</sub>	$V_{GS} = 0V,$ $V_{DS} = 50V,$		11		
Reverse Transfer Capacitance	C <sub>rss</sub>	f = 1.0MHz		1.5		
Total Gate Charge	$Q_g$	$V_{DD} = 560V, I_{D} = 2A,$ $V_{GS} = 10V$		5.1		
Gate-Source Charge	$Q_{gs}$			0.9		nC
Gate-Drain Charge	$Q_{gd}$	GS -		2.1		
Turn-on Delay Time	t <sub>d(on)</sub>			30		
Turn-on Rise Time	t <sub>r</sub>	$V_{DD} = 400V, I_{D} = 2A,$		29		
Turn-off Delay Time	t <sub>d(off)</sub>	$R_G = 25\Omega$		54		ns
Turn-off Fall Time	t <sub>f</sub>			31		
Drain-Source Body Diode Characteris	stics					
Continuous Body Diode Current	Is	T 0500			2	
Pulsed Diode Forward Current	I <sub>SM</sub>	T <sub>C</sub> = 25°C			8	Α
Body Diode Voltage	V <sub>SD</sub>	$T_J = 25^{\circ}C$ , $I_{SD} = 2A$ , $V_{GS} = 0V$		0.9	1.2	V
Reverse Recovery Time	t <sub>rr</sub>			150		ns
Reverse Recovery Charge	Q <sub>rr</sub>	$V_R = 560V, I_F = I_S,$ $di_F/dt = 100A/\mu s$		0.5		μC
Peak Reverse Recovery Current	I <sub>rrm</sub>	1,		6		Α

#### **Notes**

- 1. Repetitive Rating: Pulse Width limited by maximum junction temperature
- 2.  $I_{AS}$  = 0.5A,  $V_{DD}$  = 50V,  $R_{G}$  = 25 $\Omega$ , Starting  $T_{J}$  = 25 $^{\circ}$ C
- 3. Pulse Test: Pulse Width ≤ 300µs, Duty Cycle ≤ 1%



### **Typical Characteristics** $T_J = 25^{\circ}\text{C}$ , unless otherwise noted

**Figure 1. Output Characteristics** 

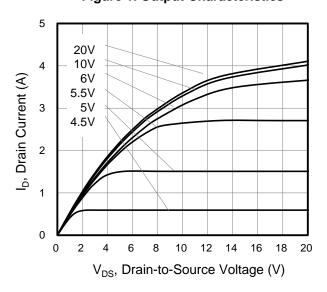


Figure 2. Transfer Characteristics

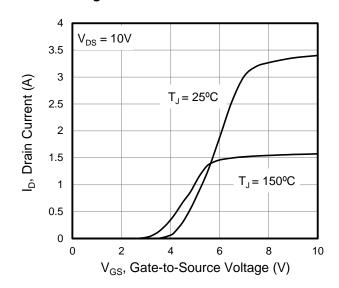


Figure 3. On-Resistance vs. Drain Current

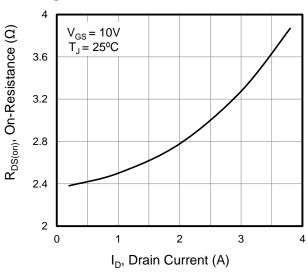


Figure 4. Capacitance

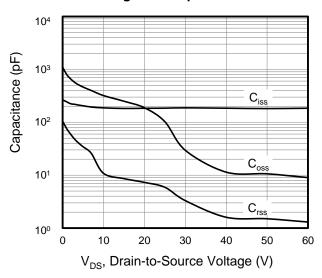


Figure 5. Gate Charge

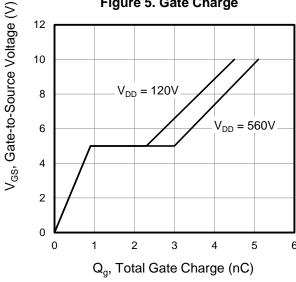
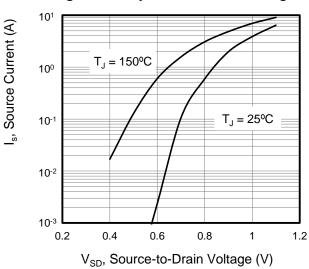


Figure 6. Body Diode Forward Voltage





### **Typical Characteristics** $T_J = 25^{\circ}\text{C}$ , unless otherwise noted

Figure 7. On-Resistance vs.

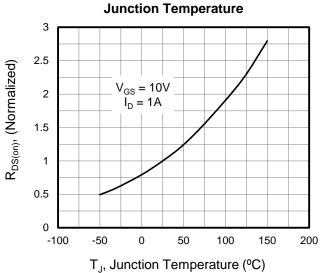


Figure 9. Transient Thermal Impedance TO-251/TO-252

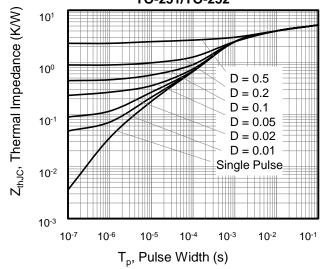
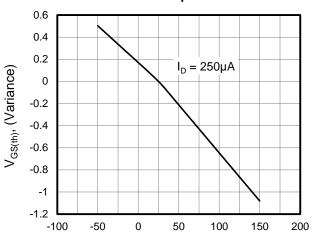


Figure 8. Threshold Voltage vs. Junction Temperature



T<sub>J</sub>, Junction Temperature (°C)



Figure A: Gate Charge Test Circuit and Waveform

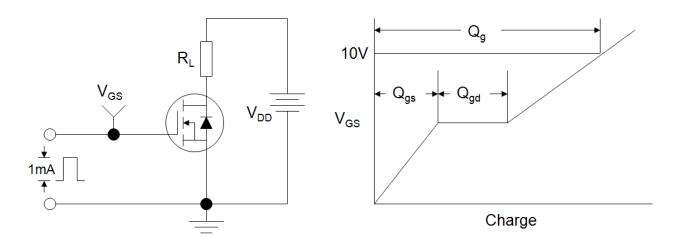


Figure B: Resistive Switching Test Circuit and Waveform

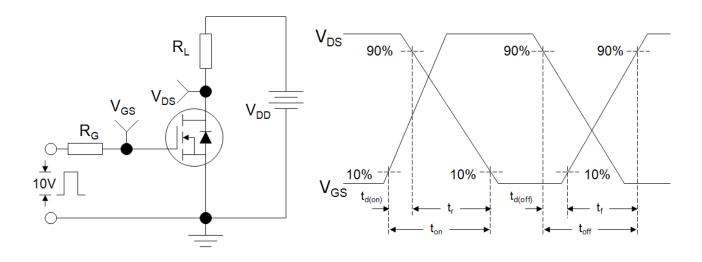
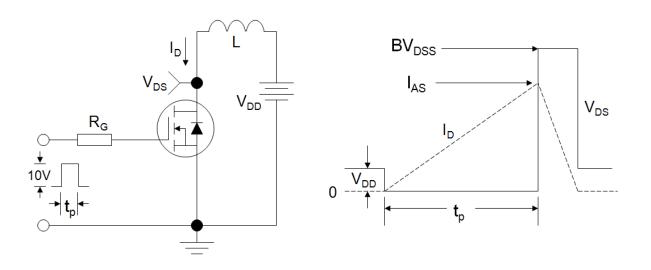
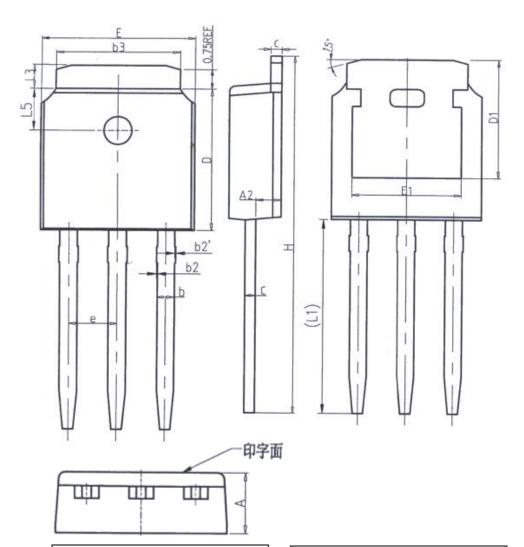


Figure C: Unclamped Inductive Switching Test Circuit and Waveform



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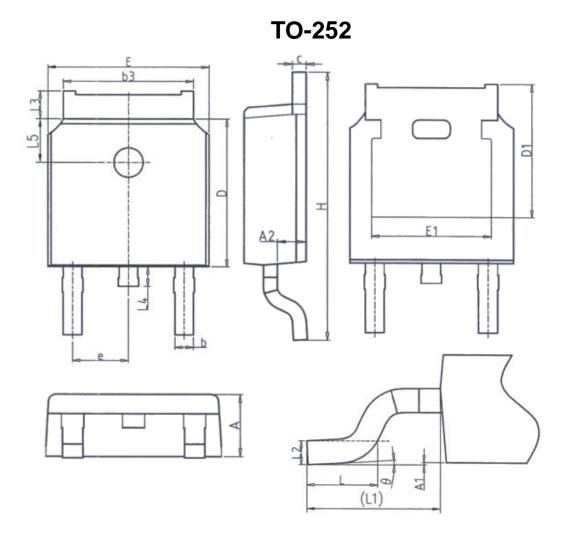
## TO-251



Unit: mm			
Symbol	Min.	Max.	
Α	2. 20	2. 40	
A2	0. 97	1. 17	
b	0. 68	0. 90	
b2	0.00	0.10	
b2′	0.00	0.10	
b3	5. 20	5. 50	
С	0. 43	0. 63	
D	5. 98	6. 22	

Unit: mm			
Symbol	Min.	Max.	
D1	5. 30	REF	
E	6. 40	6. 80	
E1	4. 63	-	
е	2. 286BSC		
Н	16. 22	16. 82	
L1	9. 15	9. 65	
L3	0.88	1. 28	
L5	1. 65	1. 95	





Unit: mm			
Symbol	Min.	Max.	
Α	2. 20	2. 40	
A1	0.00	0. 20	
A2	0. 97	1. 17	
b	0. 68	0.90	
b3	5. 20	5. 50	
С	0. 43	0. 63	
D	5. 98	6. 22	
D1	D1 5. 30REF		
E	6. 40	6. 80	
E1	4. 63	_	

Unit: mm			
Symbol	Min.	Max.	
е	2. 28	6BSC	
Н	9. 40	10.50	
L	1. 38	1. 75	
L1	2. 90REF		
L2	0. 51BSC		
L3	0.88	1. 28	
L4	_	1.00	
L5	1. 65	1. 95	
θ	0°	8°	



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