

# **650V Super-junction Power MOSFET**

#### **Description**

#### 650V Super-junction Power MOSFET

Super-junction power MOSFET is a revolutionary technology for high voltage power MOSFETs, designed according to the SJ principle. The deep trench SJ MOSFET provide an extremely low switching, communication and conduction losses device with highest robustness make especially resonant switching applications more reliable, more efficient, lighter and cooler, designed by Wuxi Unigroup Microelectronics Company.

#### **Features**

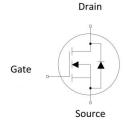
- Very low FOM  $R_{DS(on)} \times Q_g$
- 100% avalanche tested
- Easy to use/drive
- RoHS compliant

### **Applications**

- Switch Mode Power Supply (SMPS)
- Uninterruptible Power Supply (UPS)
- Power Factor Correction (PFC)
- Charger

TO-220F







### **Device Marking and Package Information**

Device	Package	Marking
TPA65R620CFD	TO-220F	65R620CFD

### **Key Performance Parameters**

Parameter	Value	Unit
V <sub>DS</sub> @ T <sub>j,max</sub>	700	V
R <sub>DS(on),max</sub>	0.65	Ω
$Q_{g,typ}$	14.5	nC
I <sub>D</sub>	7	A
I <sub>D,pulse</sub>	21	A



Absolute Maximum R	Absolute Maximum Ratings T <sub>C</sub> = 25°C, unless otherwise noted				
Parameter		Symbol	Values	Unit	
Continuous Drain Current	T <sub>C</sub> = 25°C			7	_
Continuous Drain Current	T <sub>C</sub> = 100°C		l <sub>D</sub>	4.2	- A
Pulsed Drain Current	•	(note1)	I <sub>D,pulse</sub>	21	А
Gate-Source Voltage			$V_{GSS}$	±30V	V
Single Pulse Avalanche Energy	/	(note2)	E <sub>AS</sub>	162	mJ
Repetitive Avalanche Energy (note2)		E <sub>AR</sub>	0.2	mJ	
Avalanche Current		I <sub>AR</sub>	1.4	А	
MOSFET dv/dt Ruggedness, V <sub>DS</sub> = 0480V		dv/dt	50	V/ns	
Power Dissipation For TO-220	=		$P_{D}$	28	W
Continuous Diode Forward Cui	rent		I <sub>S</sub>	7	
Diode Pulsed Current		(note1)	I <sub>S,pulse</sub>	21	A
Reverse Diode dv/dt (note3)		dv/dt	15	V/ns	
Operating Junction and Storage Temperature Range		$T_J,T_stg$	-55~+150	°C	

Thermal Resistance For TO-220F			
Parameter	Symbol	Value	Unit
Thermal Resistance, Junction-to-Case	R <sub>thJC</sub>	4.5	°C/W
Thermal Resistance, Junction-to-Ambient	R <sub>thJA</sub>	80	-0/00



	Value	Value					
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static Characteristics							
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS} = 0V, I_D = 250\mu A$	650			٧	
Zoro Coto Voltago Drain Current		$V_{DS} = 650V, V_{GS} = 0V, T_{J} = 25^{\circ}C$			1	μA	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = 650V, V <sub>GS</sub> = 0V, T <sub>J</sub> = 150°C			100		
Gate-Source Leakage Current	I <sub>GSS</sub>	$V_{GS}$ = $\pm 30V$			±100	nA	
Gate-Source Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	2.5		4.0	٧	
Drain-Source On-State-Resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> = 10V, I <sub>D</sub> = 3A		0.59	0.65	Ω	
Dynamic Characteristics	-			I			
Input Capacitance	C <sub>iss</sub>	\/ - 0\/		513			
Output Capacitance	C <sub>oss</sub>	$V_{GS} = 0V,$ $V_{DS} = 50V,$		24		pF	
Reverse Transfer Capacitance	C <sub>rss</sub>	f = 1.0MHz		1.1			
Total Gate Charge	$Q_g$			14.5			
Gate-Source Charge	$Q_{gs}$	$V_{DD} = 520V, I_D = 7A,$ $V_{GS} = 10V$ 5.2			nC		
Gate-Drain Charge	$Q_{gd}$						
Turn-on Delay Time	t <sub>d(on)</sub>			39			
Turn-on Rise Time	t <sub>r</sub>	V <sub>DD</sub> = 400V, I <sub>D</sub> = 7A,		25			
Turn-off Delay Time	t <sub>d(off)</sub>	$R_G = 25\Omega$		100		ns	
Turn-off Fall Time	t <sub>f</sub>			18			
Drain-Source Body Diode Character	ristics						
Body Diode Forward Voltage	$V_{SD}$	$T_J = 25$ °C, $I_{SD} = 7A$ , $V_{GS} = 0V$		0.9	1.2	٧	
Reverse Recovery Time	t <sub>rr</sub>			122		ns	
Reverse Recovery Charge	Q <sub>rr</sub>	$V_R = 480V, I_F = I_S,$ $di_F/dt = 100A/\mu s$		0.61		μC	
Peak Reverse Recovery Current	I <sub>rrm</sub>	, ' ' ' ' ' ' ' ' ' ' ' ' ' ' ' ' ' ' '		10		Α	

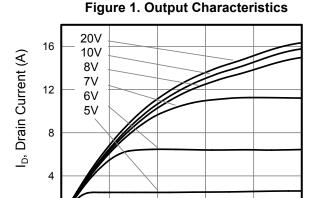
#### Notes

- 1. Repetitive Rating: Pulse width limited by maximum junction temperature
- 2.  $I_{AS} = 1.4A$ ,  $V_{DD} = 50V$ ,  $R_{G} = 25\Omega$ , Starting  $T_{J} = 25^{\circ}C$
- 3. Identical low side and high side switch with identical  ${\sf R}_{\sf G}$



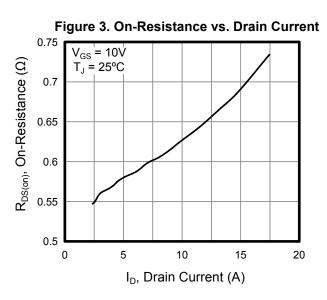


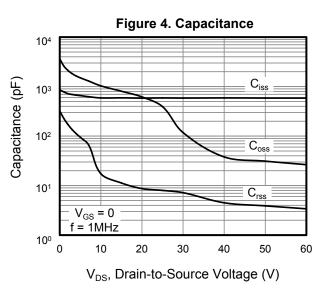
## **Typical Characteristics** $T_J = 25^{\circ}\text{C}$ , unless otherwise noted

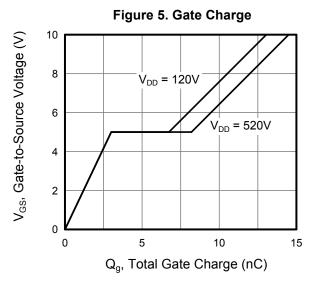


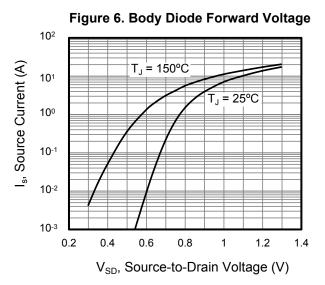
V<sub>DS</sub>, Drain-to-Source Voltage (V)

Figure 2. Transfer Characteristics  $V_{DS} = 10V$ ID, Drain Current (A)  $T_{J}^{'} = 25^{\circ}C$  $T_{J} = 150^{\circ}C$ V<sub>GS</sub>, Gate-to-Source Voltage (V)











# **Typical Characteristics** $T_J = 25^{\circ}\text{C}$ , unless otherwise noted

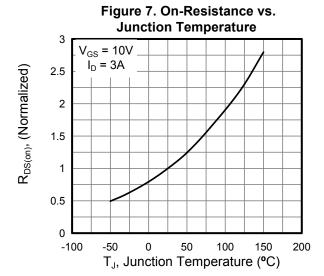


Figure 9. Transient Thermal Impedance For TO-220F

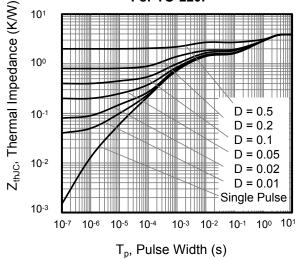


Figure 8. Threshold Voltage vs. Junction Temperature

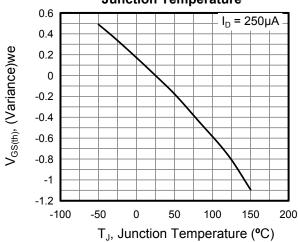
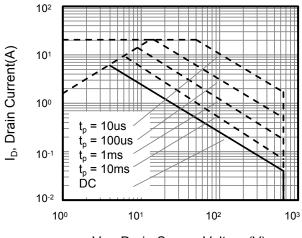


Figure 10. Safe Operation Area For TO-220F



V<sub>DS</sub>, Drain-Source Voltage(V)



Figure A: Gate Charge Test Circuit and Waveform

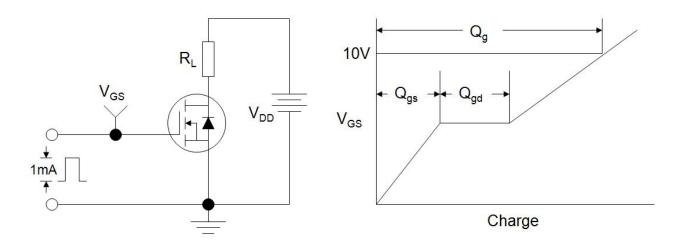


Figure B: Resistive Switching Test Circuit and Waveform

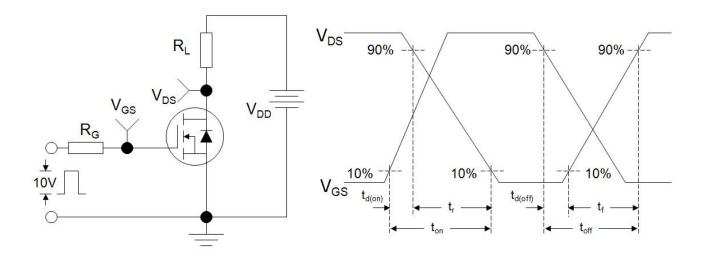
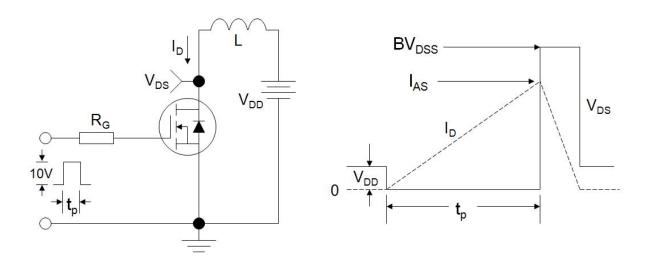


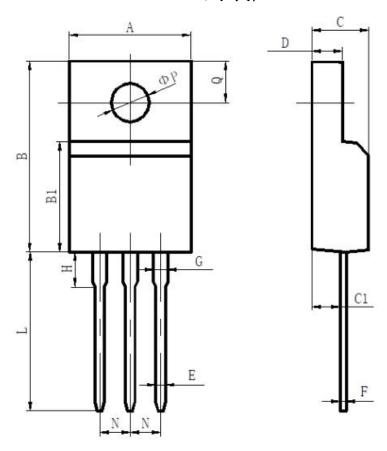
Figure C: Unclamped Inductive Switching Test Circuit and Waveform







# TO-220F (封装厂 T)



项目	规范(mm)		
	MIN	MAX	
A	9.70	10.30	
В	15.50	16.10	
B1	8.99	9.39	
C	4.40	4.80	
C1	2.15	2.55	
D	2.50	2.90	
E	0.70	0.90	
F	0.40	0.60	
G	1.12	1.42	
Н	3.40	3.80	
L	12.6	13.6	
N	2.34	2.74	
Q	3.15	3.55	
φР	3.00	3.30	



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