Wuxi Unigroup Microelectronics Company

500V Super-junction Power MOSFET

Description

500V Super-junction Power MOSFET

Super-junction power MOSFET is a revolutionary technology for high voltage power MOSFETs，designed according to the SJ principle. The deep trench SJ MOSFET provide an extremely low switching, communication and conduction losses device with highest robustness make especially resonant switching applications more reliable, more efficient, lighter and cooler, designed by Wuxi Unigroup Microelectronics Company.

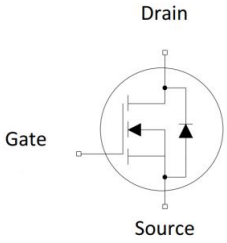
Features

- Very low FOM $R_{DS(on)} \times Q_g$
- 100% avalanche tested
- Easy to use/drive
- RoHS compliant

Applications

- Switch Mode Power Supply (SMPS)
- Uninterruptible Power Supply (UPS)
- Power Factor Correction (PFC)
- Charger

TO-220F



RoHS

Device Marking and Package Information

Device	Package	Marking
TPA50R1K6C	TO-220F	50R1K6C

Key Performance Parameters

Parameter	Value	Unit
V_{DS} @ $T_{j,max}$	550	V
$R_{DS(on),max}$	1.8	Ω
$Q_{g,typ}$	5.1	nC
I_D	2	A
$I_{D,pulse}$	6	A

Absolute Maximum Ratings T_C = 25ºC, unless otherwise noted				
Parameter		**Symbol**	**Values**	**Unit**
Continuous Drain Current	T_C = 25ºC	I_D	2	A
Pulsed Drain Current	(note1)	$I_{D,pulse}$	6	A
Gate-Source Voltage		V_{GSS}	±30V	V
Single Pulse Avalanche Energy	(note2)	E_{AS}	20	mJ
Repetitive Avalanche Energy	(note2)	E_{AR}	0.05	mJ
Avalanche Current		I_{AR}	0.5	A
Power Dissipation For TO-220F		P_D	8.1	W
Continuous Diode Forward Current		I_S	2	A
Diode Pulsed Current	(note1)	$I_{S,pulse}$	6	
Operating Junction and Storage Temperature Range		T_J, T_{stg}	-55~+150	ºC

Thermal Resistance For TO-220F			
Parameter	**Symbol**	**Value**	**Unit**
Thermal Resistance, Junction-to-Case	R_{thJC}	15.4	ºC/W
Thermal Resistance, Junction-to-Ambient	R_{thJA}	80	

Specifications T_J = 25ºC, unless otherwise noted

Parameter	Symbol	Test Conditions	Value			Unit
			Min.	Typ.	Max.	
Static						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	V_{GS} = 0V, I_D = 250μA	600	--	--	V
Zero Gate Voltage Drain Current	I_{DSS}	V_{DS} = 500V, V_{GS} = 0V, T_J = 25ºC	--	--	1	μA
		V_{DS} = 500V, V_{GS} = 0V, T_J = 150ºC	--	--	100	
Gate-Source Leakage	I_{GSS}	V_{GS} = ±30V	--	--	±100	nA
Gate-Source Threshold Voltage	$V_{GS(th)}$	V_{DS} = V_{GS}, I_D = 250μA	2.5	--	4.0	V
Drain-Source On-Resistance (Note3)	$R_{DS(on)}$	V_{GS} = 10V, I_D = 1A	--	1.6	1.8	Ω
Forward Transconductance (Note3)	g_{fs}	V_{DS} = 10V, I_D = 1A	--	2	--	S
Dynamic						
Input Capacitance	C_{iss}	V_{GS} = 0V, V_{DS} = 50V, f = 1.0MHz	--	182	--	pF
Output Capacitance	C_{oss}		--	11	--	
Reverse Transfer Capacitance	C_{rss}		--	1.5	--	
Total Gate Charge	Q_g	V_{DD} = 400V, I_D = 2A, V_{GS} = 10V	--	5.1	--	nC
Gate-Source Charge	Q_{gs}		--	0.9	--	
Gate-Drain Charge	Q_{gd}		--	2.1	--	
Turn-on Delay Time	$t_{d(on)}$	V_{DD} = 400V, I_D = 2A, R_G = 25Ω	--	30	--	ns
Turn-on Rise Time	t_r		--	29	--	
Turn-off Delay Time	$t_{d(off)}$		--	54	--	
Turn-off Fall Time	t_f		--	31	--	
Drain-Source Body Diode Characteristics						
Body Diode Voltage	V_{SD}	T_J = 25ºC, I_{SD} = 2A, V_{GS} = 0V	--	0.9	1.2	V
Reverse Recovery Time	t_{rr}	V_R = 400V, I_F = I_S, di_F/dt = 100A/μs	--	150	--	ns
Reverse Recovery Charge	Q_{rr}		--	0.5	--	μC
Peak Reverse Recovery Current	I_{rrm}		--	6.0	--	A

Notes

1. Repetitive Rating: Pulse Width limited by maximum junction temperature
2. I_{AS} = 0.5A, V_{DD} = 50V, R_G = 25Ω, Starting T_J = 25°C
3. Pulse Test: Pulse Width ≤ 300μs, Duty Cycle ≤ 1%

Typical Characteristics T_J = 25ºC, unless otherwise noted

Figure 1. Output Characteristics

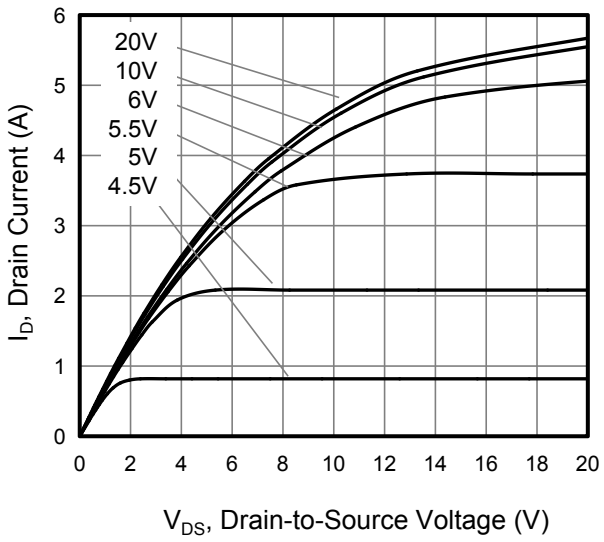


Figure 2. Transfer Characteristics

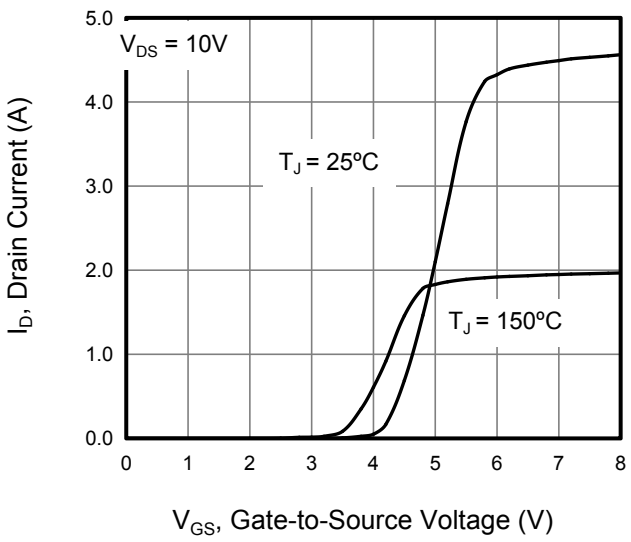


Figure 3. On-Resistance vs. Drain Current

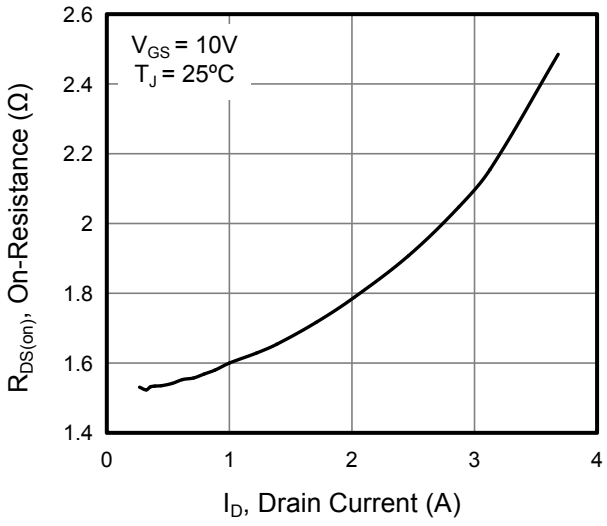


Figure 4. Capacitance

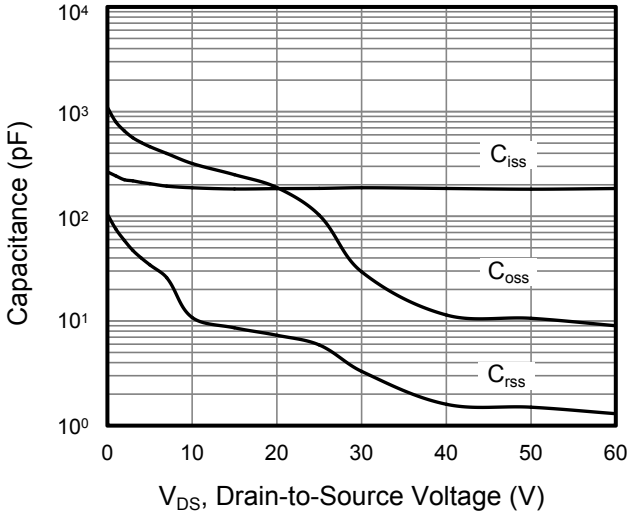


Figure 5. Gate Charge

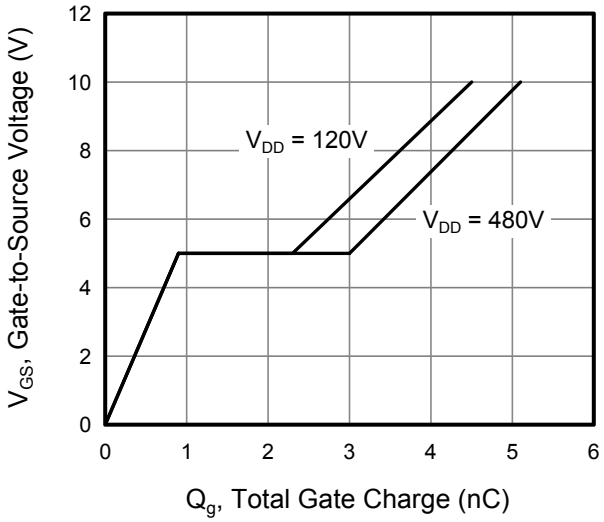
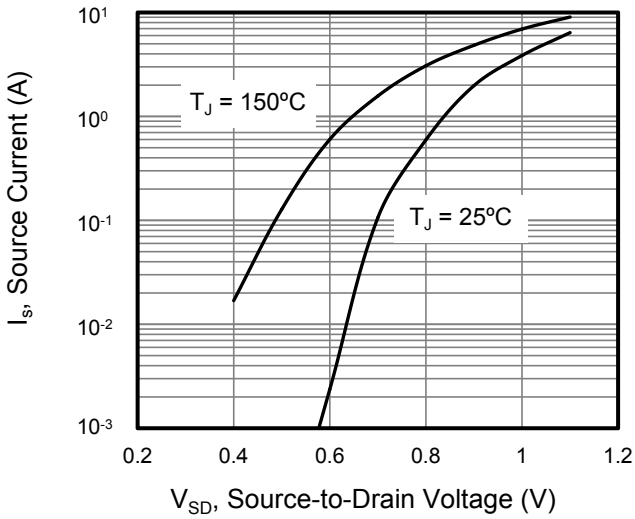


Figure 6. Body Diode Forward Voltage



Typical Characteristics T_J = 25ºC, unless otherwise noted

Figure 7. On-Resistance vs. Junction Temperature

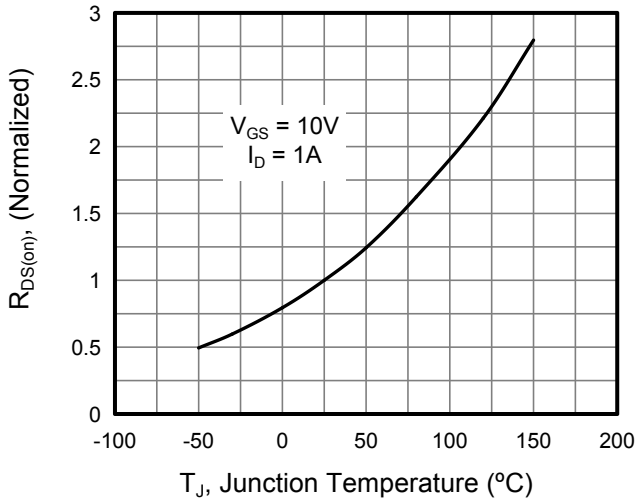


Figure 8. Threshold Voltage vs. Junction Temperature

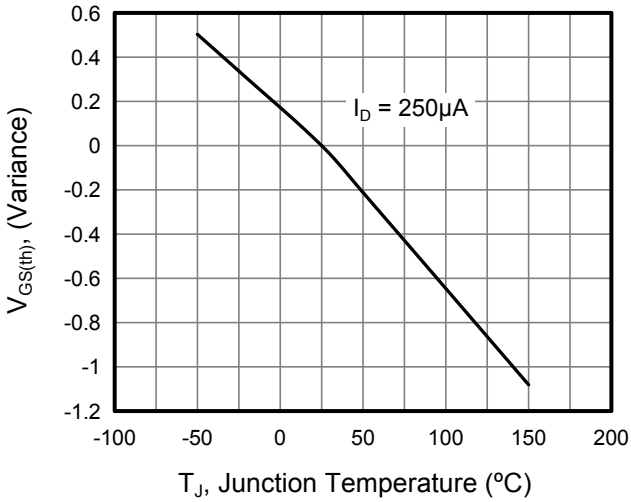


Figure 9. Transient Thermal Impedance TO-220F

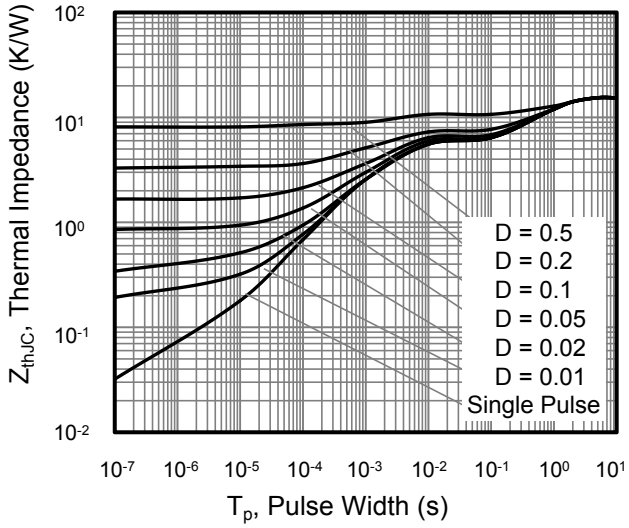


Figure 10. Safe Operation Area For TO-220F

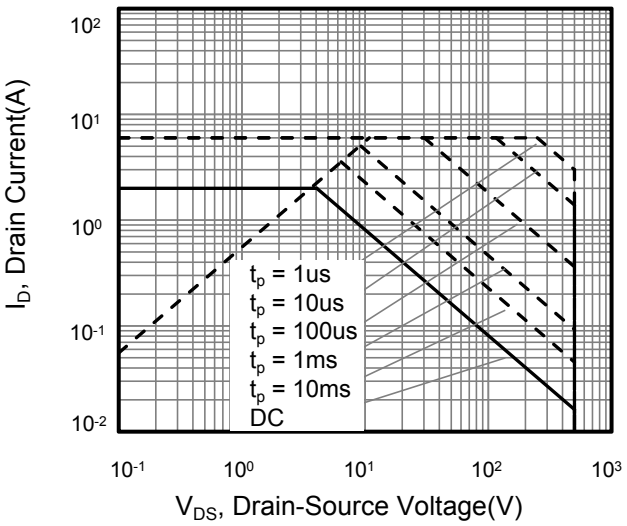


Figure A：Gate Charge Test Circuit and Waveform

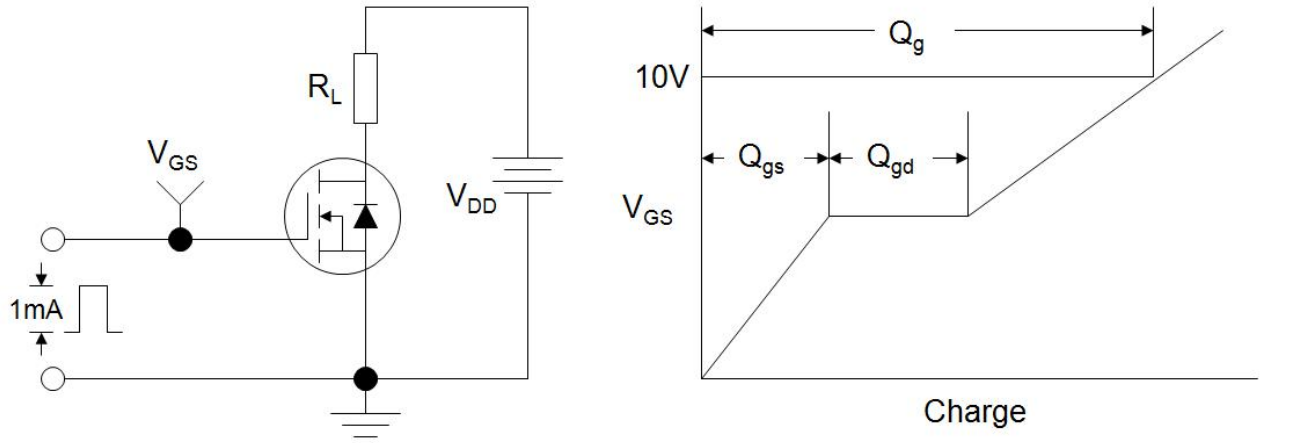


Figure B：Resistive Switching Test Circuit and Waveform

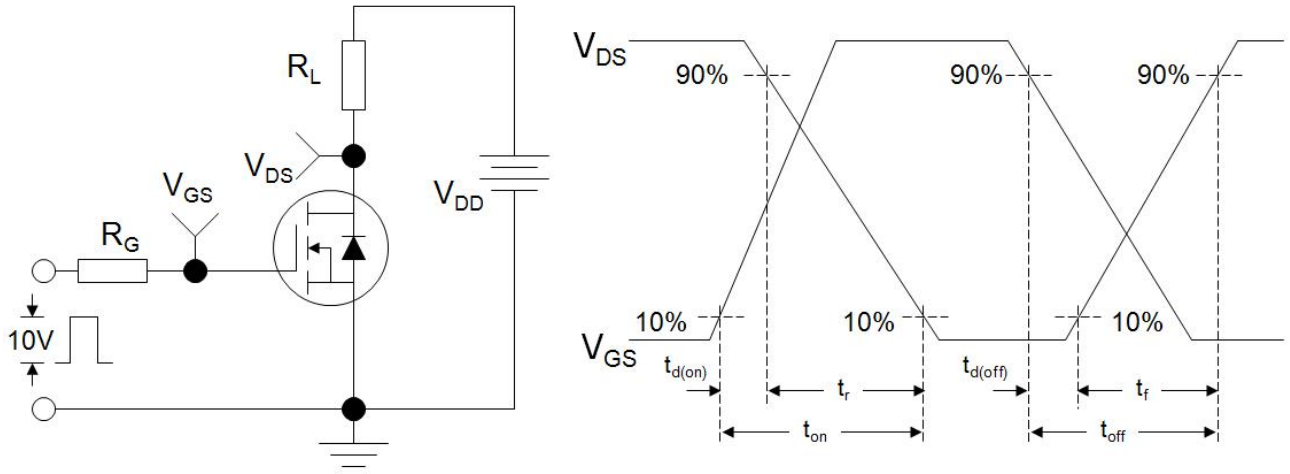
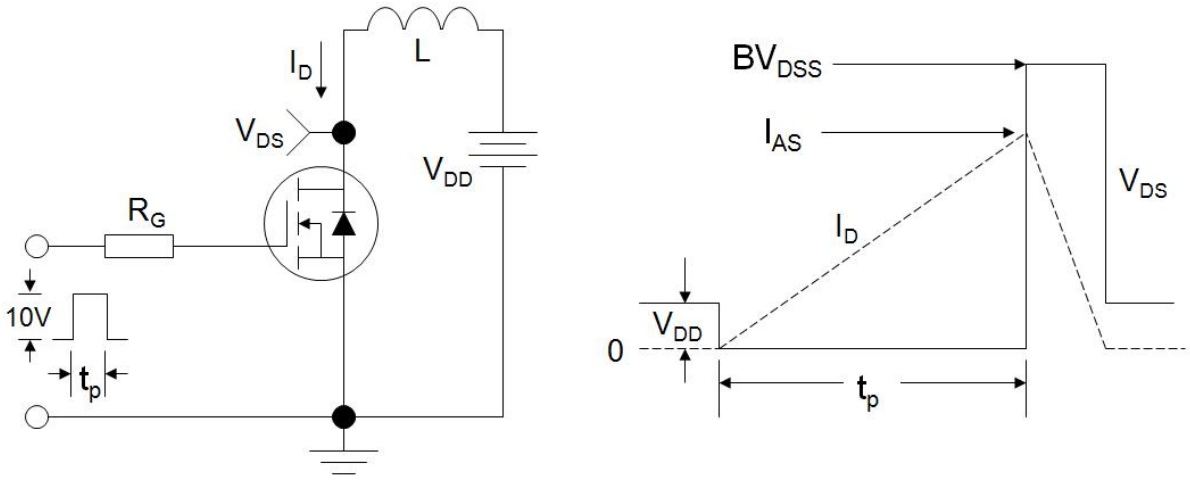
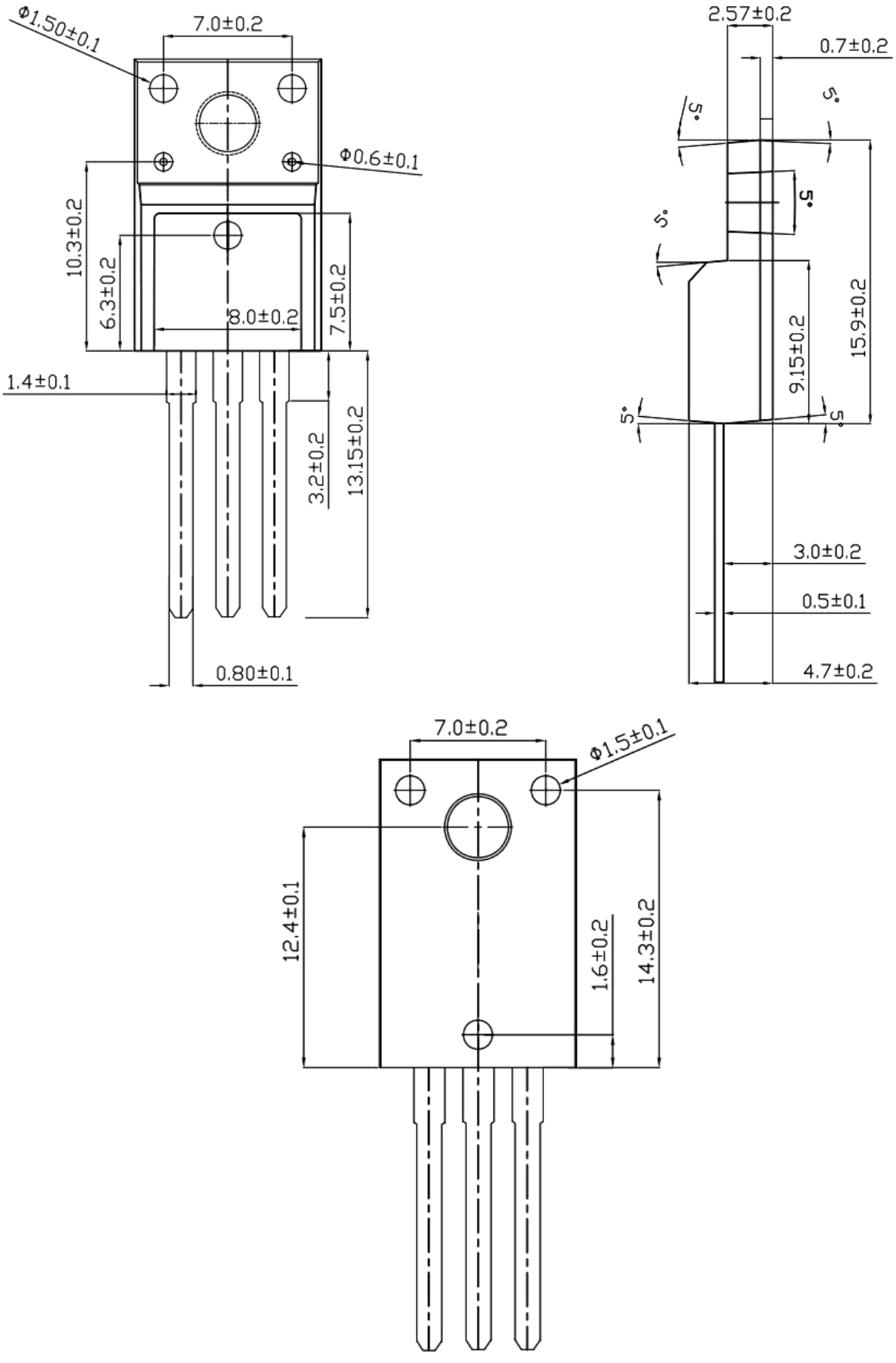


Figure C：Unclamped Inductive Switching Test Circuit and Waveform



TO-220F（封装厂 C）


Φ1.50±0.1
7.0±0.2
Φ0.6±0.1
10.3±0.2
6.3±0.2
8.0±0.2
7.5±0.2
1.4±0.1
3.2±0.2
13.15±0.2
0.80±0.1
2.57±0.2
0.7±0.2
5°
5°
5°
5°
5°
5°
15.9±0.2
9.15±0.2
3.0±0.2
0.5±0.1
4.7±0.2
7.0±0.2
Φ1.5±0.1
12.4±0.1
1.6±0.2
14.3±0.2

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