



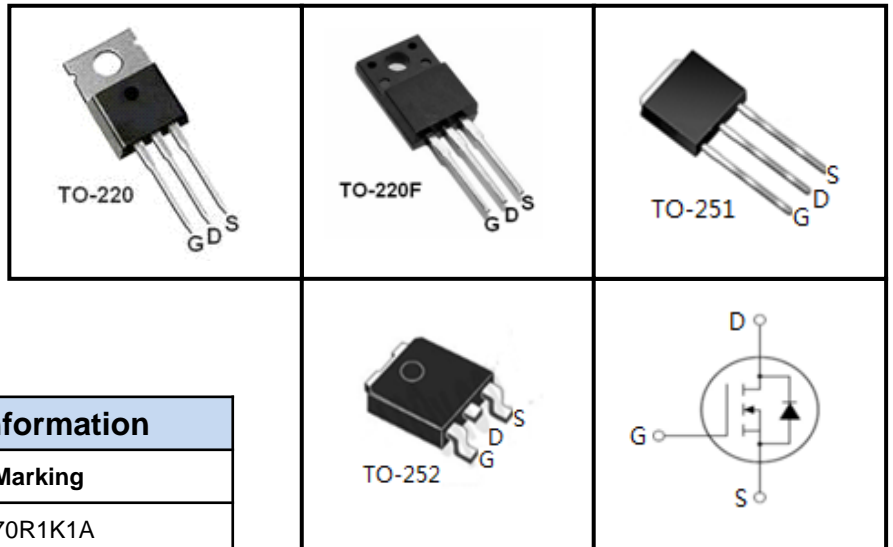
700V Super-Junction Power MOSFET

FEATURES

- Very low FOM $R_{DS(on)} \times Q_g$
- 100% avalanche tested
- RoHS compliant

APPLICATIONS

- Switch Mode Power Supply (SMPS)
- Uninterruptible Power Supply (UPS)
- Power Factor Correction (PFC)



Device Marking and Package Information		
Device	Package	Marking
TPP70R1K1A	TO-220	70R1K1A
TPA70R1K1A	TO-220F	70R1K1A
TPU70R1K1A	TO-251	70R1K1A
TPD70R1K1A	TO-252	70R1K1A

Absolute Maximum Ratings $T_C = 25^\circ\text{C}$, unless otherwise noted						
Parameter	Symbol	Value				Unit
		TO-220	TO-251	TO-252	TO-220F	
Drain-Source Voltage ($V_{GS} = 0\text{V}$)	V_{DSS}	700				V
Continuous Drain Current	I_D	5				A
Pulsed Drain Current (note1)	I_{DM}	15				A
Gate-Source Voltage	V_{GSS}	± 30				V
Single Pulse Avalanche Energy (note2)	E_{AS}	50				mJ
Avalanche Current (note1)	I_{AR}	5				A
Repetitive Avalanche Energy (note1)	E_{AR}	0.4				mJ
Power Dissipation ($T_C = 25^\circ\text{C}$)	P_D	73		32		W
Operating Junction and Storage Temperature Range	T_J, T_{stg}	-55~+150				$^\circ\text{C}$

Thermal Resistance						
Parameter	Symbol	Value				Unit
		TO-220	TO-251	TO-252	TO-220F	
Thermal Resistance, Junction-to-Case	R_{thJC}	1.7		3.9		$^\circ\text{C/W}$
Thermal Resistance, Junction-to-Ambient	R_{thJA}	75		80		



Specifications $T_J = 25^\circ\text{C}$, unless otherwise noted						
Parameter	Symbol	Test Conditions	Value			Unit
			Min.	Typ.	Max.	
Static						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS} = 0V, I_D = 250\mu A$	700	--	--	V
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = 700V, V_{GS} = 0V, T_J = 25^\circ\text{C}$	--	--	1	μA
		$V_{DS} = 700V, V_{GS} = 0V, T_J = 150^\circ\text{C}$	--	--	100	
Gate-Source Leakage	I_{GSS}	$V_{GS} = \pm 30V$	--	--	± 100	nA
Gate-Source Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250\mu A$	2.5	--	3.5	V
Drain-Source On-Resistance (Note3)	$R_{DS(on)}$	$V_{GS} = 10V, I_D = 3A$	--	0.93	1.05	Ω
Forward Transconductance (Note3)	g_{fs}	$V_{DS} = 10V, I_D = 5A$	--	5.5	--	S
Dynamic						
Input Capacitance	C_{iss}	$V_{GS} = 0V,$ $V_{DS} = 50V,$ $f = 1.0\text{MHz}$	--	671	--	μF
Output Capacitance	C_{oss}		--	142	--	
Reverse Transfer Capacitance	C_{rss}		--	5.5	--	
Total Gate Charge	Q_g	$V_{DD} = 480V, I_D = 5A,$ $V_{GS} = 10V$	--	15	--	nC
Gate-Source Charge	Q_{gs}		--	3	--	
Gate-Drain Charge	Q_{gd}		--	6	--	
Turn-on Delay Time	$t_{d(on)}$	$V_{DD} = 300V, I_D = 5A,$ $R_G = 25\Omega$	--	25	57	ns
Turn-on Rise Time	t_r		--	39	85	
Turn-off Delay Time	$t_{d(off)}$		--	53	114	
Turn-off Fall Time	t_f		--	22	53	
Drain-Source Body Diode Characteristics						
Continuous Body Diode Current	I_S	$T_C = 25^\circ\text{C}$	--	--	5	A
Pulsed Diode Forward Current	I_{SM}		--	--	15	
Body Diode Voltage	V_{SD}	$T_J = 25^\circ\text{C}, I_{SD} = 5A, V_{GS} = 0V$	--	0.95	1.2	V
Reverse Recovery Time	t_{rr}	$V_R = 480V, I_F = I_S,$ $di_F/dt = 100A/\mu s$	--	300	--	ns
Reverse Recovery Charge	Q_{rr}		--	2.4	--	μC
Peak Reverse Recovery Current	I_{rrm}		--	16	--	A

Notes

1. Repetitive Rating: Pulse width limited by maximum junction temperature
2. $I_{AS} = 3A, V_{DD} = 50V, R_G = 25\Omega, \text{Starting } T_J = 25^\circ\text{C}$
3. Pulse Test: Pulse width $\leq 300\mu s, \text{Duty Cycle } \leq 1\%$



Typical Characteristics $T_J = 25^\circ\text{C}$, unless otherwise noted

Figure 1. Output Characteristics

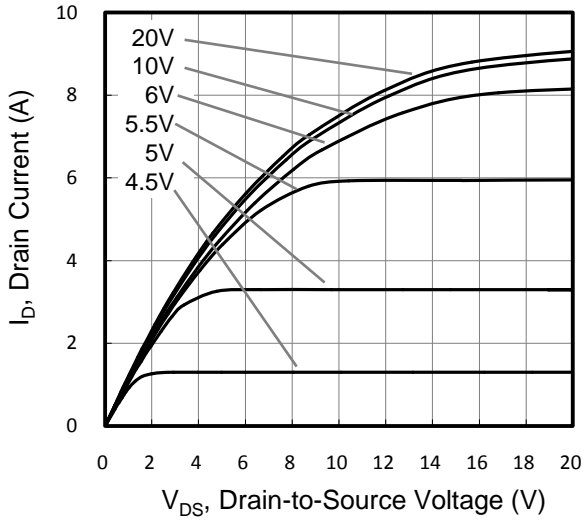


Figure 2. Transfer Characteristics

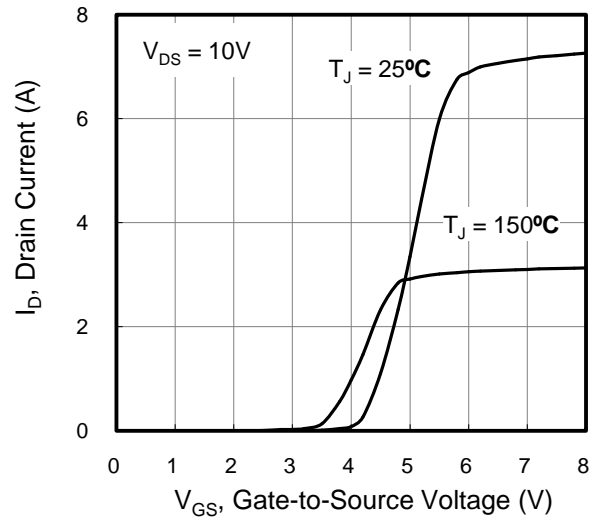


Figure 3. On-Resistance vs. Drain Current

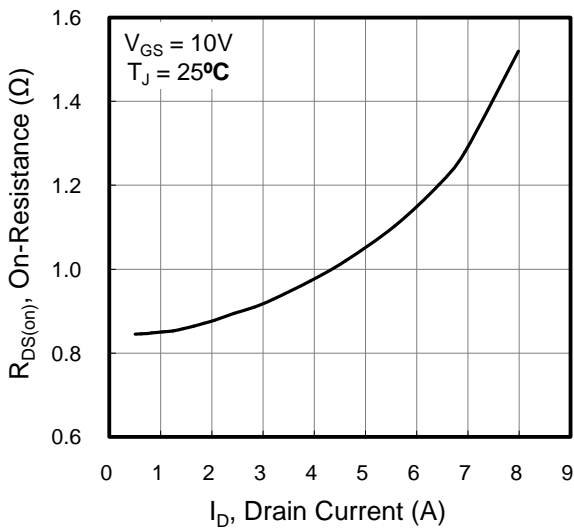


Figure 4. Capacitance

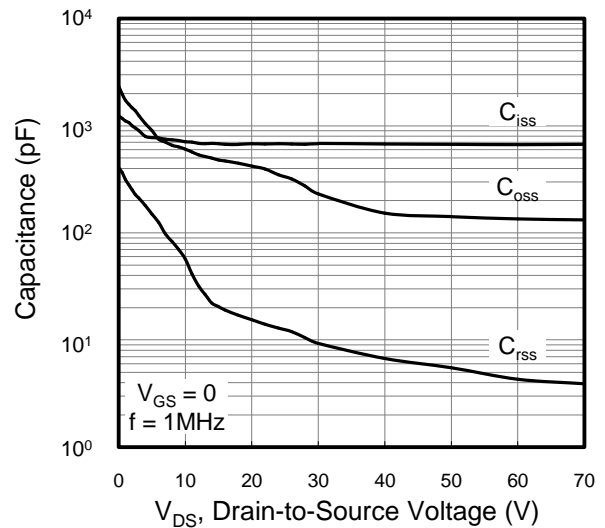


Figure 5. Gate Charge

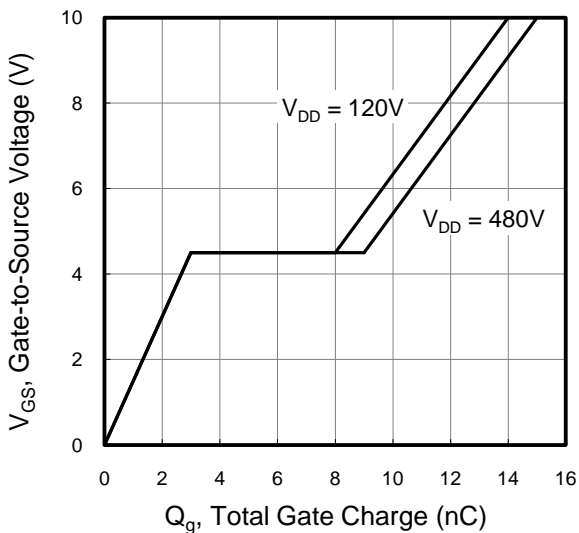
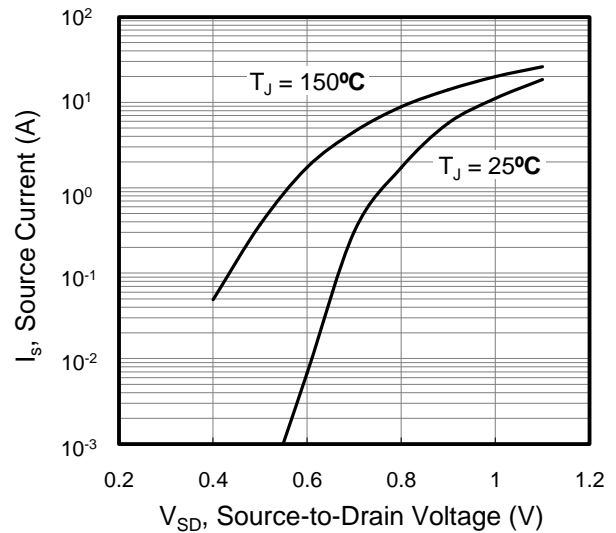


Figure 6. Body Diode Forward Voltage





Typical Characteristics $T_J = 25^\circ\text{C}$, unless otherwise noted

Figure 7. On-Resistance vs. Junction Temperature

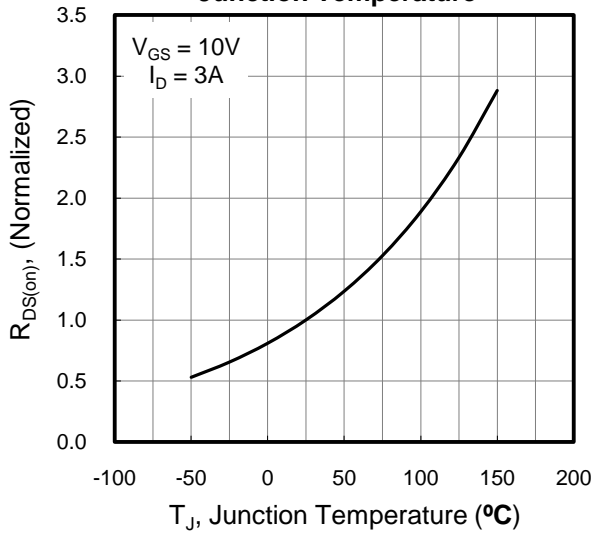


Figure 8. Threshold Voltage vs. Junction Temperature

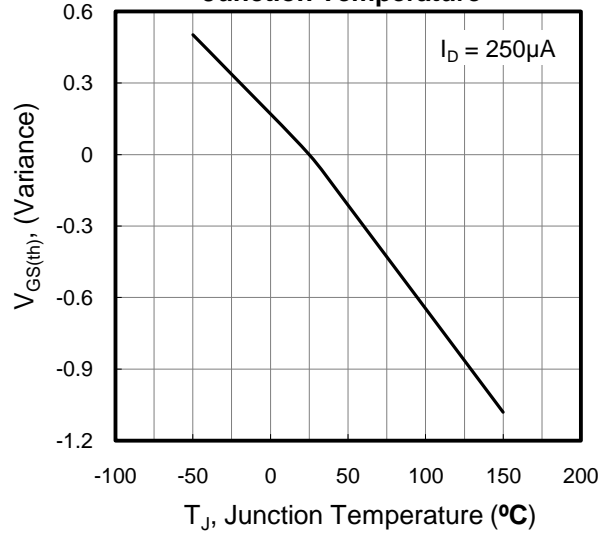


Figure 9. Transient Thermal Impedance TO-220/TO-251/TO-252

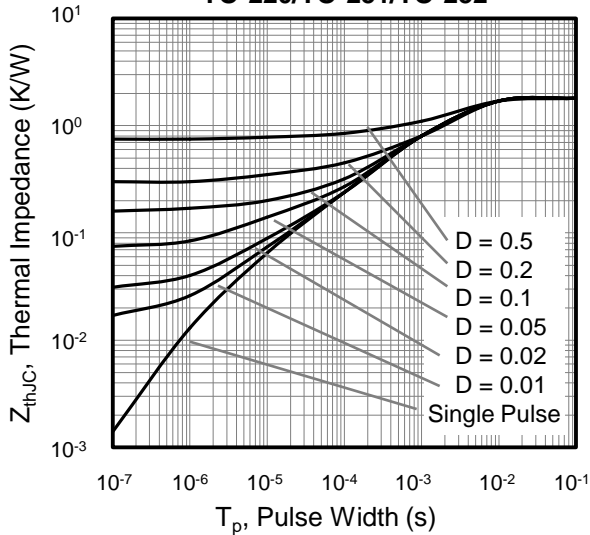


Figure 10. Transient Thermal Impedance TO-220F

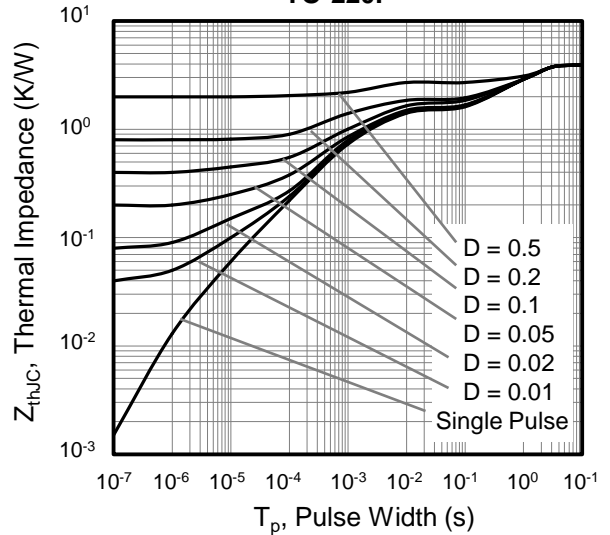


Figure 11. Safe Operating Area TO-220/TO-251/TO-252

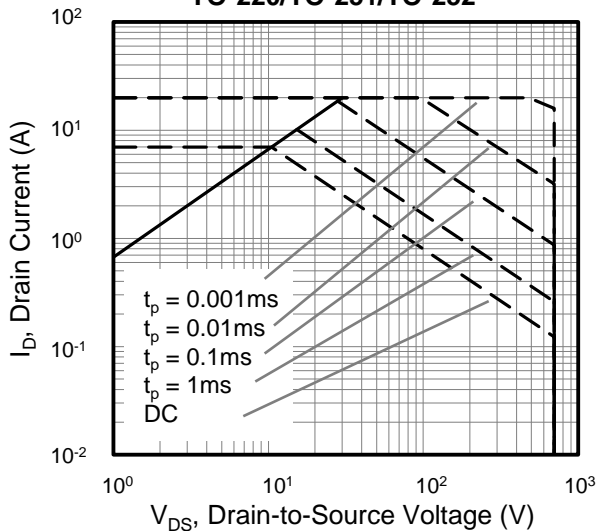


Figure 12. Safe Operating Area TO-220F

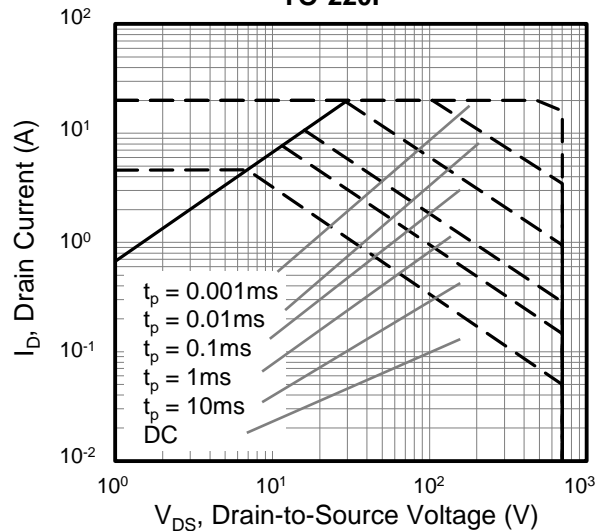




Figure A: Gate Charge Test Circuit and Waveform

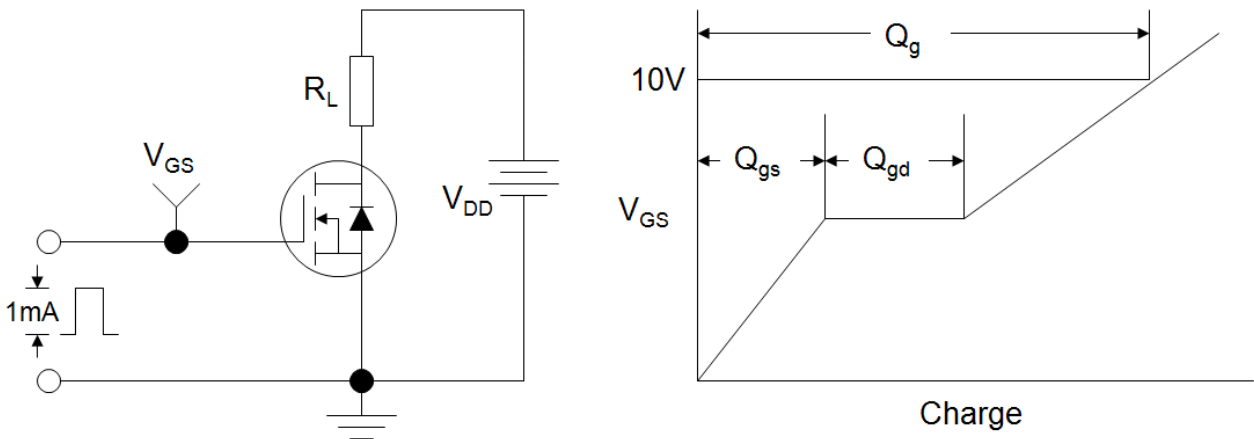


Figure B: Resistive Switching Test Circuit and Waveform

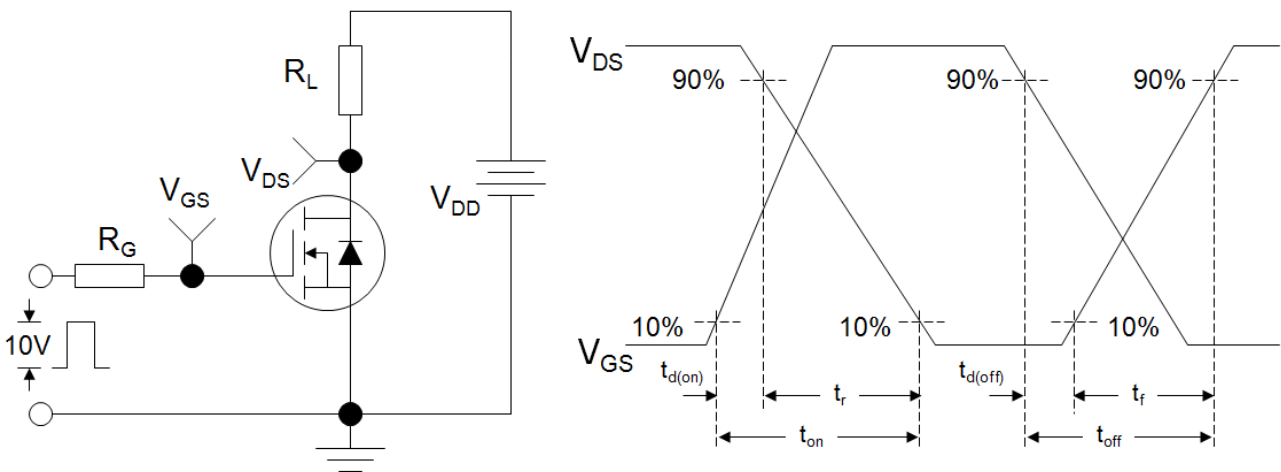
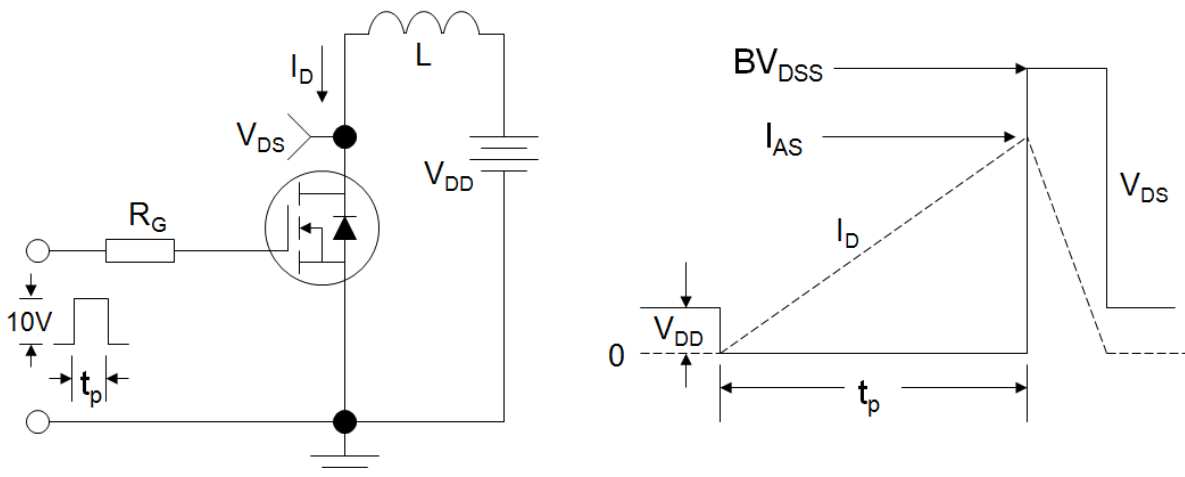
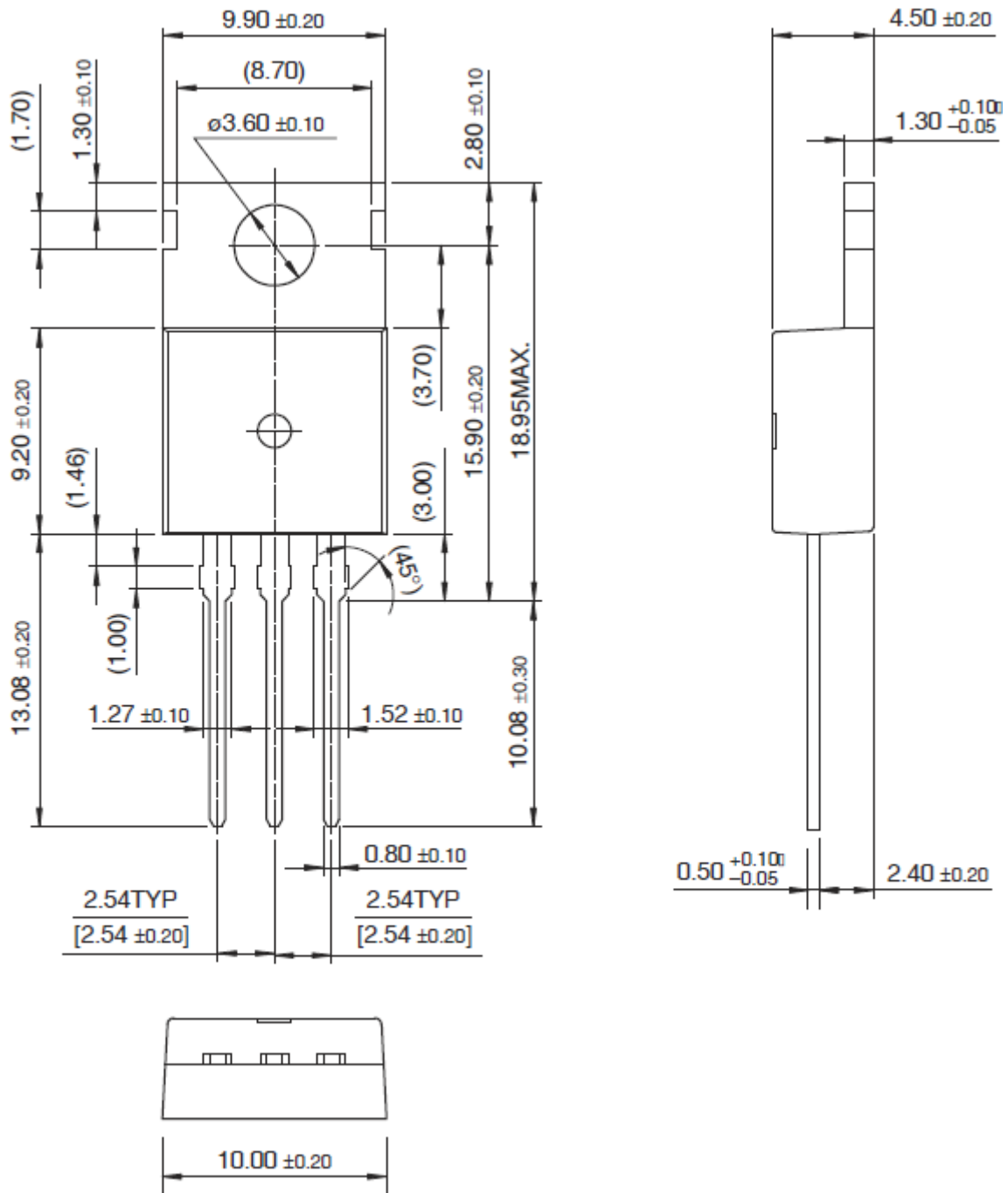


Figure C: Unclamped Inductive Switching Test Circuit and Waveform



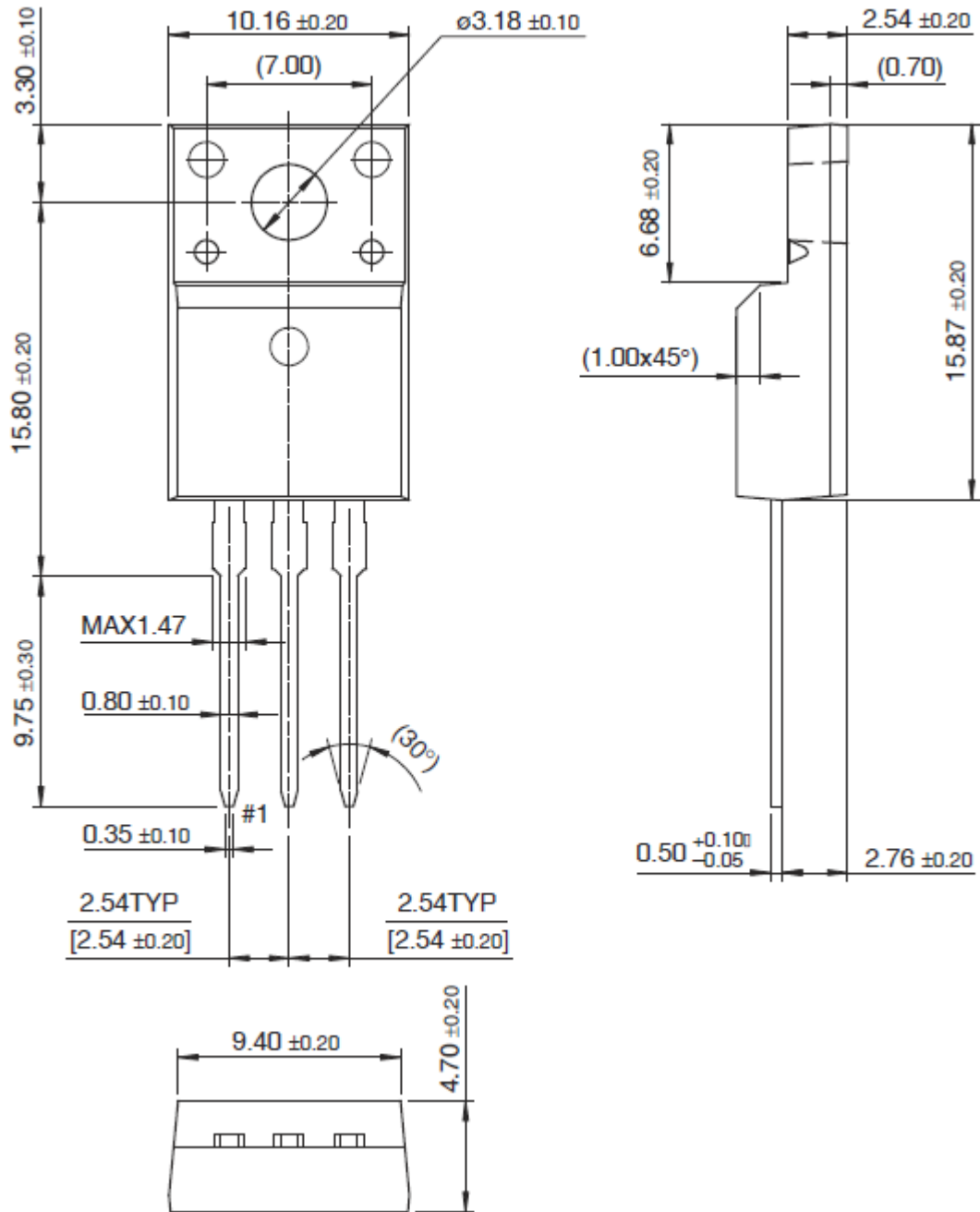


TO-220



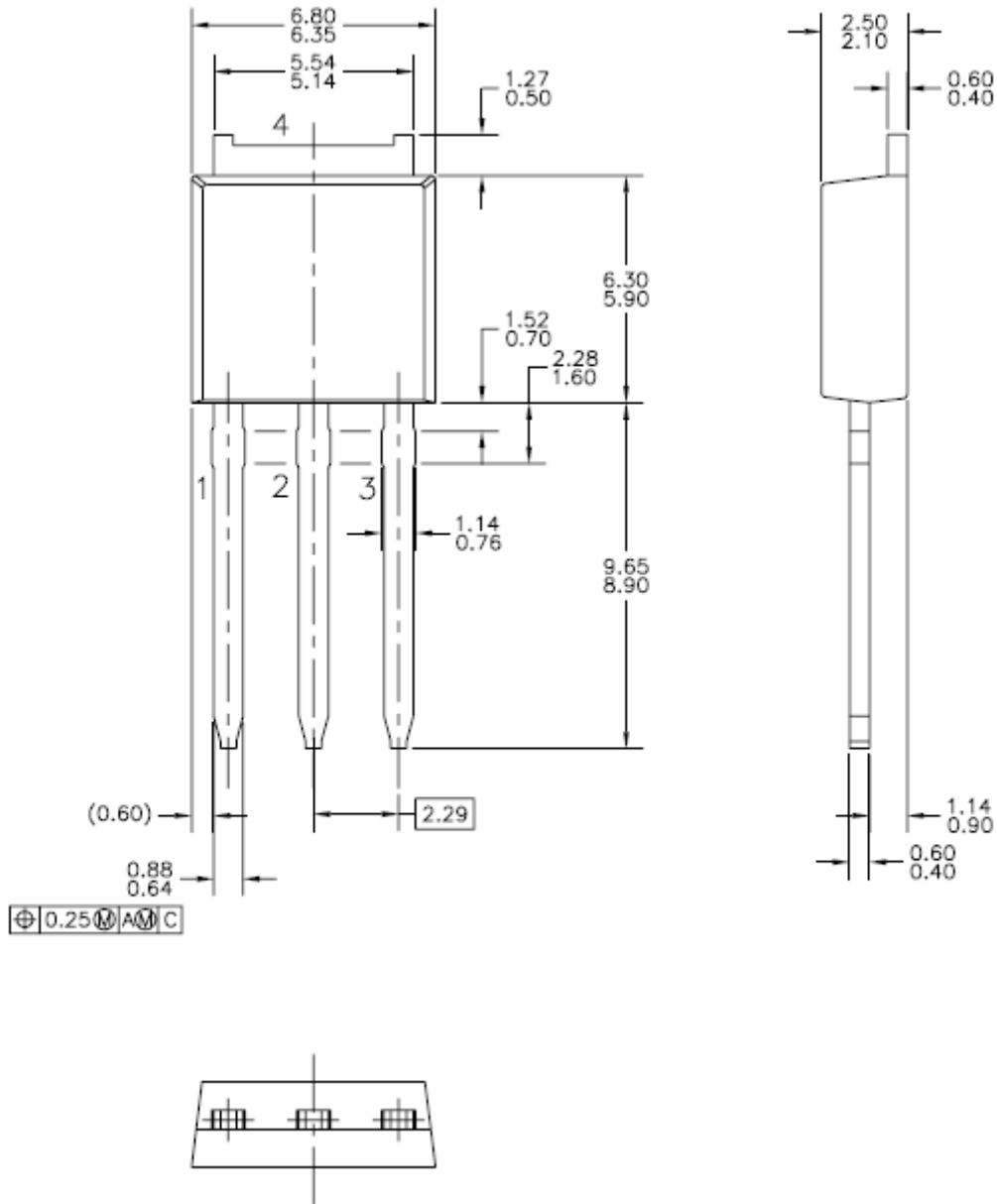


TO-220F



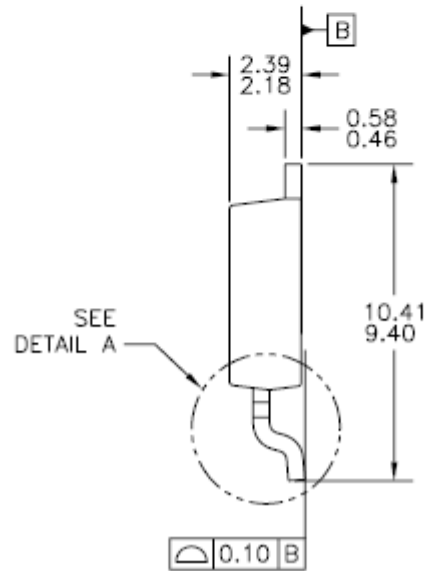
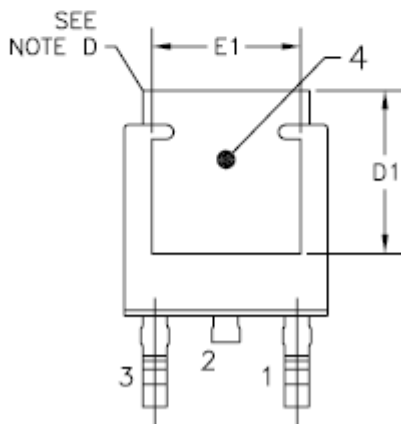
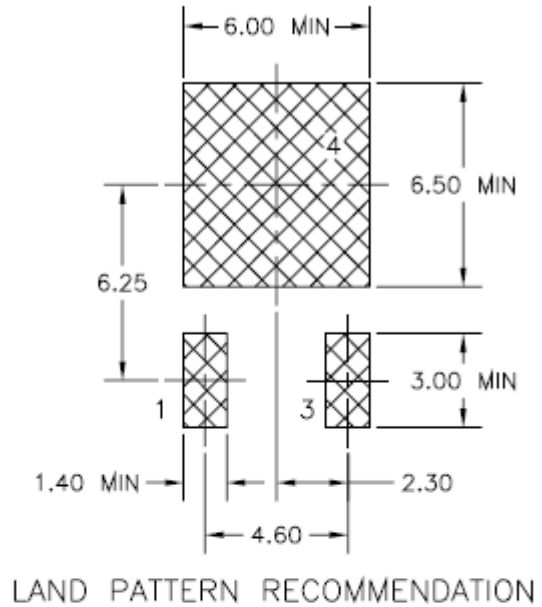
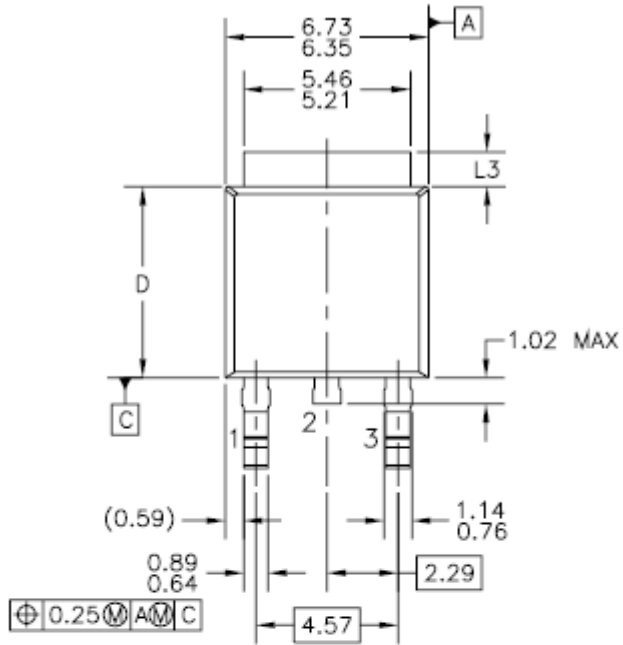


TO-251





TO-252





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